MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V _{RRM} V _{RWM} V _R	40	V
Average Rectified Forward Current (At Rated V _R , T _L = 112°C)	Io	1.0	А
Peak Repetitive Forward Current (At Rated V _R , Square Wave, 100 kHz, T _L = 95°C)	I _{FRM}	2.0	А
Non-Repetitive Peak Surge Current (Non-Repetitive peak surge current, halfwave, single phase, 60 Hz)	I _{FSM}	30	A
Storage Temperature	T _{stg}	-55 to 150	°C
Operating Junction Temperature	TJ	-55 to 125	°C
Voltage Rate of Change (Rated V _R , T _J = 25°C)	dv/dt	10,000	V/μs

THERMAL CHARACTERISTICS

Thermal Resistance, Junction-to-Lead (Note 1)	R _{til}	26	°C/W
Thermal Resistance, Junction-to-Lead (Note 2)	R _{til}	21	
Thermal Resistance, Junction-to-Ambient (Note 1)	R _{tia}	325	
Thermal Resistance, Junction-to-Ambient (Note 2)	R _{tja}	82	

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

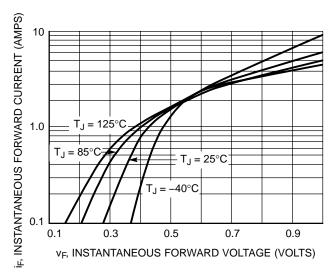
1. Mounted with minimum recommended pad size, PC Board FR4.

- 2. Mounted with 1 in. copper pad (Cu area 700 mm²).

ELECTRICAL CHARACTERISTICS

Maximum Instantaneous Forward Voltage (Note 3), See Figure 2	V _F	T _J = 25°C	T _J = 85°C	V
$(I_F = 0.1 \text{ A})$ $(I_F = 1.0 \text{ A})$ $(I_F = 3.0 \text{ A})$		0.36 0.55 0.85	0.30 0.515 0.88	
Maximum Instantaneous Reverse Current (Note 3), See Figure 4	I _R	T _J = 25°C	T _J = 85°C	mA
(V _R = 40 V) (V _R = 20 V)		0.5 0.15	25 18	

^{3.} Pulse Test: Pulse Width \leq 250 μ s, Duty Cycle \leq 2%.



1.0 T_J = 125°C T_J = 25°C T_J = 25°C V_F, MAXIMUM INSTANTANEOUS FORWARD VOLTAGE (VOLTS)

Figure 1. Typical Forward Voltage

Figure 2. Maximum Forward Voltage

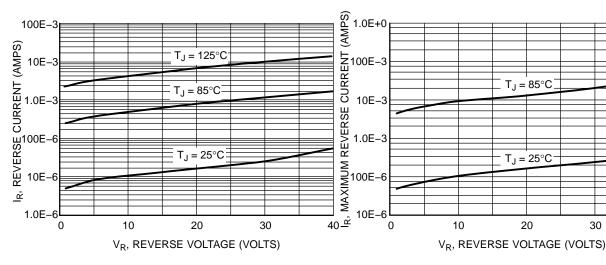
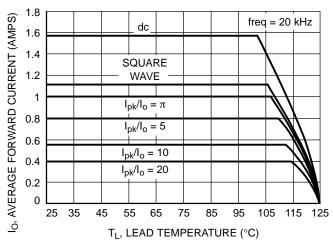


Figure 3. Typical Reverse Current

Figure 4. Maximum Reverse Current

40





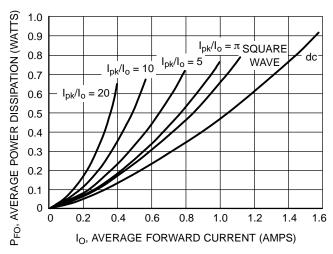


Figure 6. Forward Power Dissipation

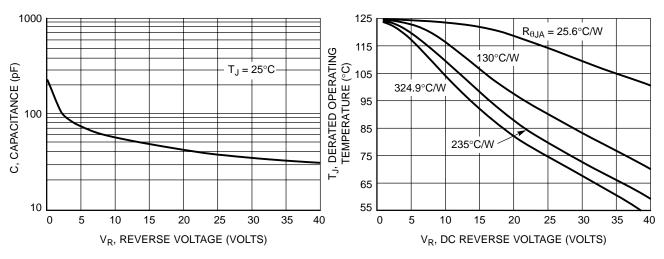


Figure 7. Capacitance

Figure 8. Typical Operating Temperature Derating*

* Reverse power dissipation and the possibility of thermal runaway must be considered when operating this device under any reverse voltage conditions. Calculations of T_J therefore must include forward and reverse power effects. The allowable operating T_J may be calculated from the equation: $T_J = T_{Jmax} - r(t)(Pf + Pr)$ where

r(t) = thermal impedance under given conditions,

Pf = forward power dissipation, and

Pr = reverse power dissipation

This graph displays the derated allowable T_J due to reverse bias under DC conditions only and is calculated as $T_J = T_{Jmax} - r(t)Pr$, where r(t) = Rthja. For other power applications further calculations must be performed.

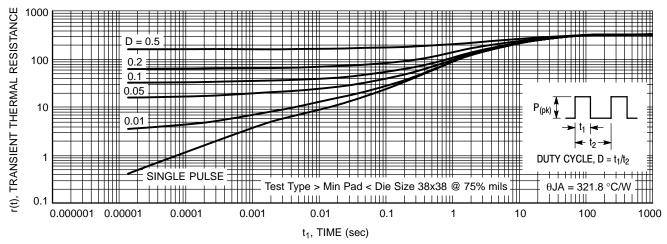
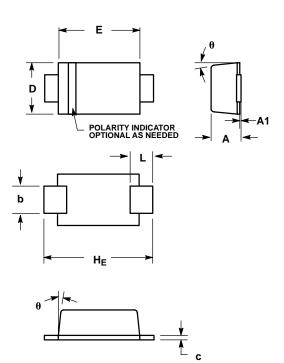


Figure 9. Thermal Response

PACKAGE DIMENSIONS

SOD-123LF CASE 498-01 ISSUE A

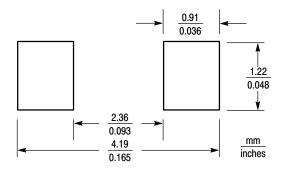


- NOTES: 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.

- 1982.
 2. CONTROLLING DIMENSION: MILLIMETER.
 3. DIMENSIONS A AND B DO NOT INCLUDE MOLD FLASH.
 4. DIMENSIONS D AND J ARE TO BE MEASURED ON FLAT
 SECTION OF THE LEAD: BETWEEN 0.10 AND 0.25 MM
 FROM THE LEAD TIP.

	MILLIMETERS			INCHES		
DIM	MIN	NOM	MAX	MIN	NOM	MAX
Α	0.90	0.95	1.00	0.035	0.037	0.039
A1	0.00	0.05	0.10	0.000	0.002	0.004
b	0.70	0.90	1.10	0.028	0.035	0.043
С	0.10	0.15	0.20	0.004	0.006	0.008
D	1.50	1.65	1.80	0.059	0.065	0.071
E	2.50	2.70	2.90	0.098	0.106	0.114
L	0.55	0.75	0.95	0.022	0.030	0.037
HE	3.40	3.60	3.80	0.134	0.142	0.150
θ	0°	_	8°	0°	_	8°

SOLDERING FOOTPRINT*



*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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