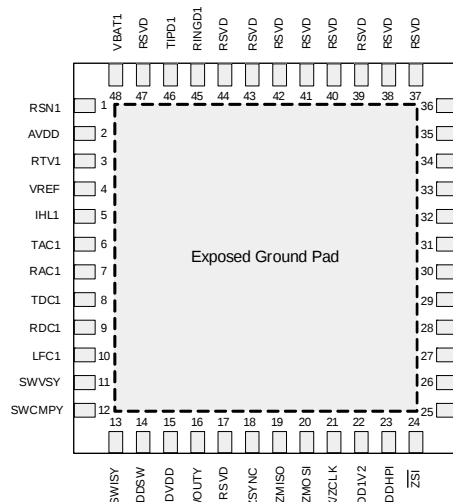


Selected Electrical Specifications

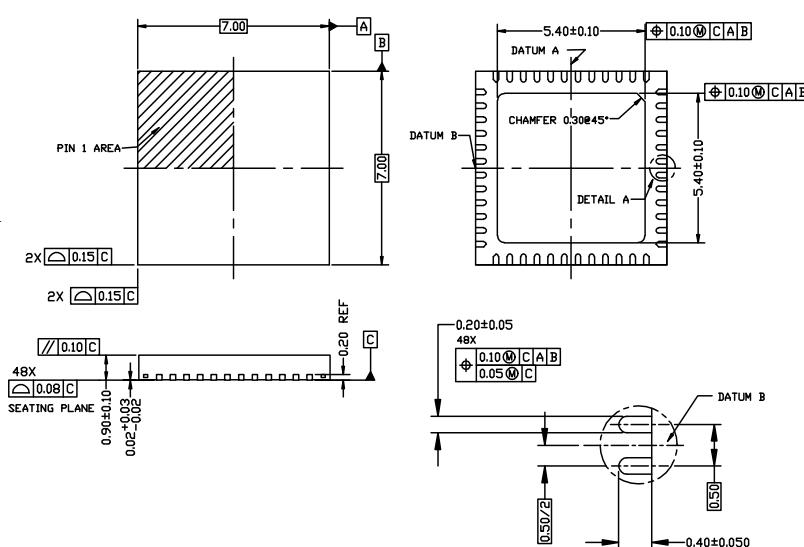
Description	Symbol	Test Conditions	Min	Typ	Max	Unit
Ambient Temperature, under Bias	T_A		-40		+85	°C
Digital and Analog Supply Voltages	DVDD, AVDD		3.135	3.3	3.465	V _{DC}
Operating Limits: VBAT ₁ (in active state)			-105 V _{DC} to -12 V _{DC}			V _{DC}
Line Current:	ILA		18	25	45	mA
Ringing Voltage	V _{RING}	5 REN		50	65	V _{RMS}
Two-Wire Return Loss	R _L	200 to 3400 Hz		30		dB
Longitudinal Balance		1 kHz		58		dB
Device Power Dissipation, Continuous	P _{D(max)}	T _A = 85°C		1.5		W
Junction to Ambient Thermal Resistance	θ_{JA}			29		°C/W

Device Power Consumption (Buck-Boost)	Symbol	Test Conditions	Typ	Unit
Shutdown	P _D	Switcher off	8	mW
Disconnect			37	
Low Power Idle Mode		On-Hook	52	
Idle		On-Hook	98	
Active		Off-Hook, 300 Ω , ILA = 25 mA	324	
Ringing		50 V _{RMS} , 1 REN	462	

Device Pinout



Package Drawings



Related Collateral

www.microsemi.com/voice-line-circuits

- Le9641 Tracking Battery miSLIC™ Line Circuit Preliminary Data Sheet, Document ID# 148556
 - ZLR964122L SM2 Line Module, ZSI, 1 FXS, Buck-Boost ABS, 85-V_{PK}, 5 REN
 - ZLR964124L SM2 Line Module, PCM/SPI, 1 FXS Buck-Boost and 1 FXS Inverting-Boost, 85-V_{PK}, 5 REN
- Le9642 Shared Battery Dual miSLIC™ Line Circuit Preliminary Data Sheet, Document ID# 148557
 - ZLR964222L SM2 Line Module, ZSI, 2 FXS, Buck-Boost ABS, 85-V_{PK}, 5 REN