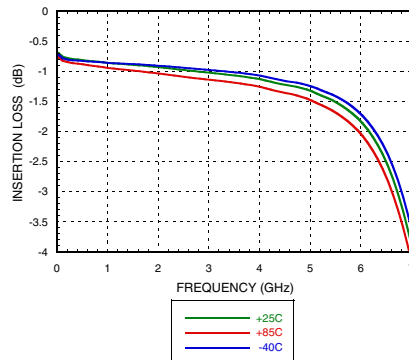
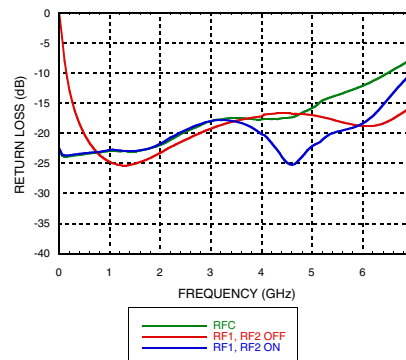


**HIGH ISOLATION SPDT
NON-REFLECTIVE SWITCH, DC - 6 GHz**

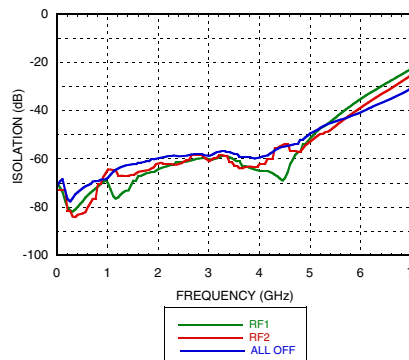
Insertion Loss



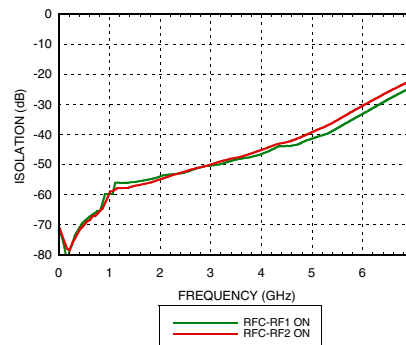
Return Loss ^[1]



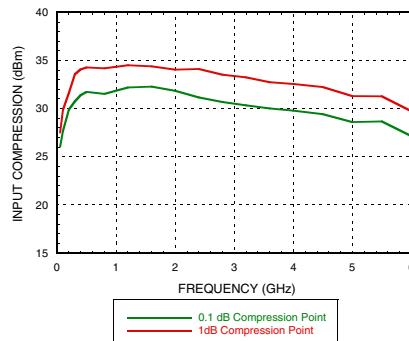
**Isolation Between
Ports RFC and RF1 / RF2**



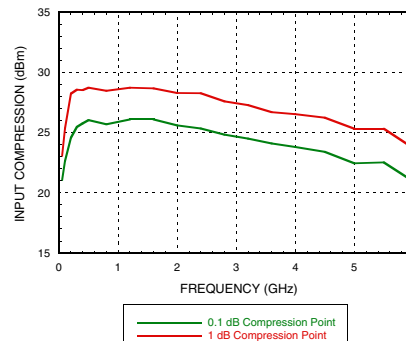
Isolation Between Ports RF1 and RF2



**0.1 and 1 dB Input Compression
Point, Vdd = 5V, Linear**



**0.1 and 1 dB Input Compression
Point, Vdd = 3V, Linear**

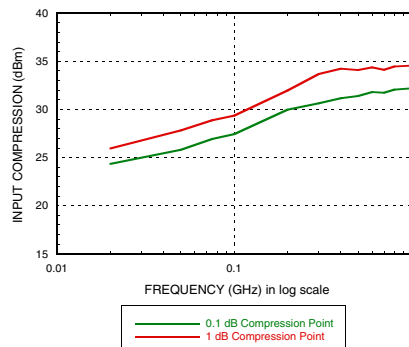


[1] RFC is reflective in "all off" state.

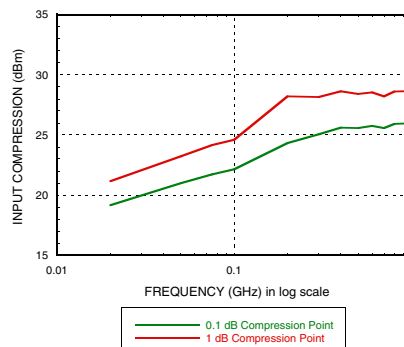


**HIGH ISOLATION SPDT
NON-REFLECTIVE SWITCH, DC - 6 GHz**

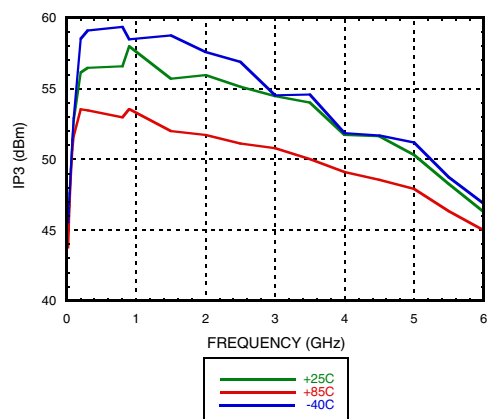
0.1 and 1 dB Input Compression Point, Vdd = 5V



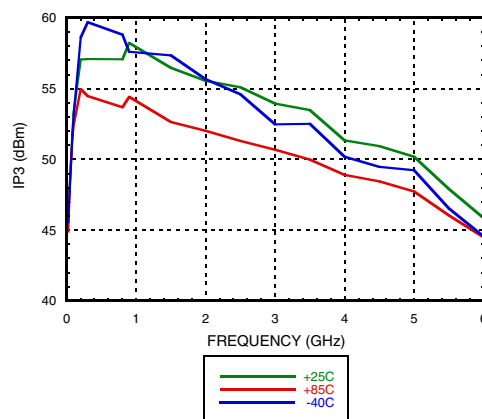
0.1 and 1 dB Input Compression Point, Vdd = 3V



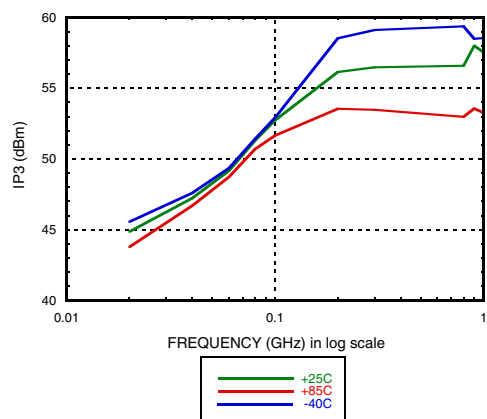
Input Third Order Intercept Point, Vdd = 5V, Linear



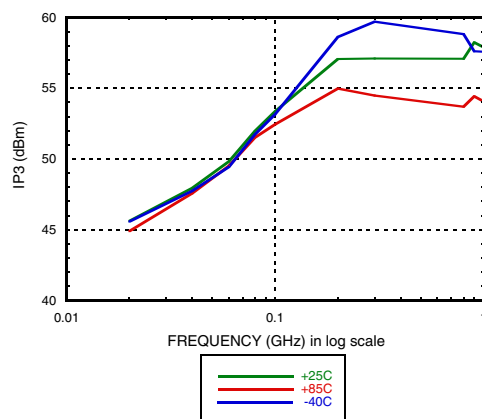
Input Third Order Intercept Point, Vdd = 3V, Linear



Input Third Order Intercept Point, Vdd = 5V



Input Third Order Intercept Point, Vdd = 3V





HIGH ISOLATION SPDT NON-REFLECTIVE SWITCH, DC - 6 GHz

Absolute Maximum Ratings

Bias Voltage (Vdd)	7V
Control Voltage (Vctl, EN)	-1V to Vdd +1V
RF Input Power *	Through Path 3V/5V Termination Path 3V/5V
	31 / 33 dBm 26.5 dBm
Channel Temperature	150 °C
Continuous Pdiss (T = 85 °C) (derate 14.9 mW/°C for through path, and 6.9 mW/°C for termination path above 85 °C)	Through Path Termination Path
	0.969 W 0.451 W
Thermal Resistance (channel to package bottom)	Through Path Termination Path
	67.1 °C/W 144.2°C/W
Storage Temperature	-65 to +150 °C
Operating Temperature	-40 to +85 °C
ESD Sensitivity (HBM)	Class 1A

* The RF input power is quite lower than the breakdown power levels. Hence, the only concern with this product is the thermal limit.



ELECTROSTATIC SENSITIVE DEVICE
OBSERVE HANDLING PRECAUTIONS

Bias Voltage & Current

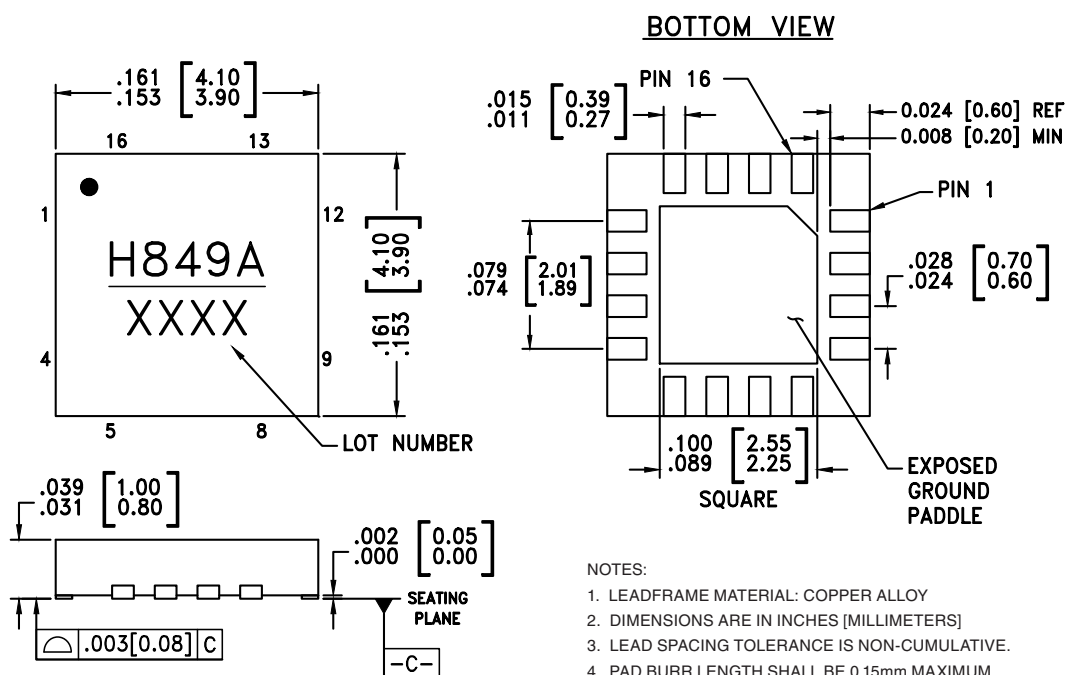
Vdd (V)	Idd (Typ.) (mA)
3	1.2
5	1.3

Digital Control Voltages

State	Bias Condition
Low	0 to +0.8 Vdc @ <1 µA Typical
High	+2.0 to +5.0 Vdc @ 40 µA Typical

Truth Table

Control Input		Signal Path State	
Vctl	EN	RFC - RF1	RFC - RF2
Low	Low	OFF	ON
High	Low	ON	OFF
Low	High	OFF	OFF
High	High	OFF	OFF

**HIGH ISOLATION SPDT
NON-REFLECTIVE SWITCH, DC - 6 GHz**
Outline Drawing**NOTES:**

1. LEADFRAME MATERIAL: COPPER ALLOY
2. DIMENSIONS ARE IN INCHES [MILLIMETERS]
3. LEAD SPACING TOLERANCE IS NON-CUMULATIVE.
4. PAD BURR LENGTH SHALL BE 0.15mm MAXIMUM.
PAD BURR HEIGHT SHALL BE 0.05mm MAXIMUM.
5. PACKAGE WARP SHALL NOT EXCEED 0.05mm.
6. ALL GROUND LEADS AND GROUND PADDLE MUST BE SOLDERED TO PCB RF GROUND.
7. REFER TO HITTITE APPLICATION NOTE FOR SUGGESTED LAND PATTERN.

Package Information

Part Number	Package Body Material	Lead Finish	MSL Rating	Package Marking ^[1]
HMC849ALP4CE	RoHS-compliant Low Stress Injection Molded Plastic	100% matte Sn	MSL3 ^[2]	H849A XXXX

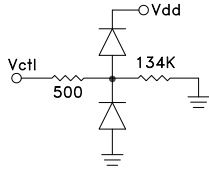
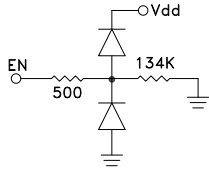

[1] 4-Digit lot number XXXX

[2] Max peak reflow temperature of 260 °C

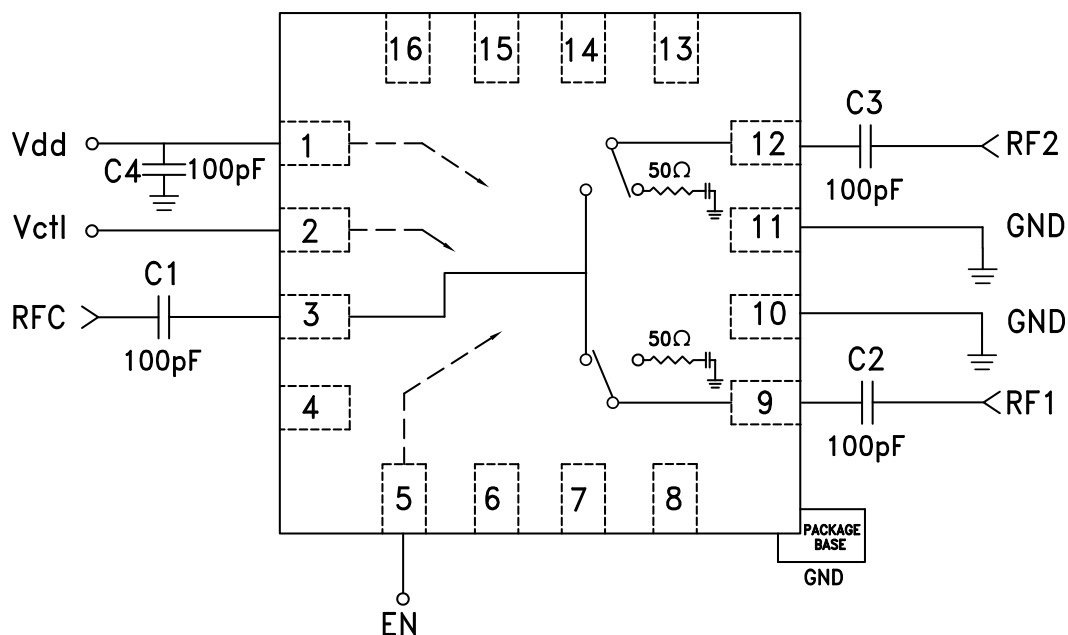


HIGH ISOLATION SPDT NON-REFLECTIVE SWITCH, DC - 6 GHz

Pin Descriptions

Pin Number	Function	Description	Interface Schematic
1	Vdd	Supply Voltage.	
2	Vctl	Control input. See truth and control voltage tables.	
3, 9, 12	RFC, RF1, RF2	These pins are DC coupled and matched to 50 Ohms. Blocking capacitors are required.	
4, 6, 7, 8, 13, 14, 15, 16	N/C	The pins are not connected internally; however, all data shown herein was measured with these pins connected to RF/DC ground externally.	
5	EN	Enable. See truth and control voltage tables.	
10, 11	GND	Package bottom must also be connected to PCB RF ground.	

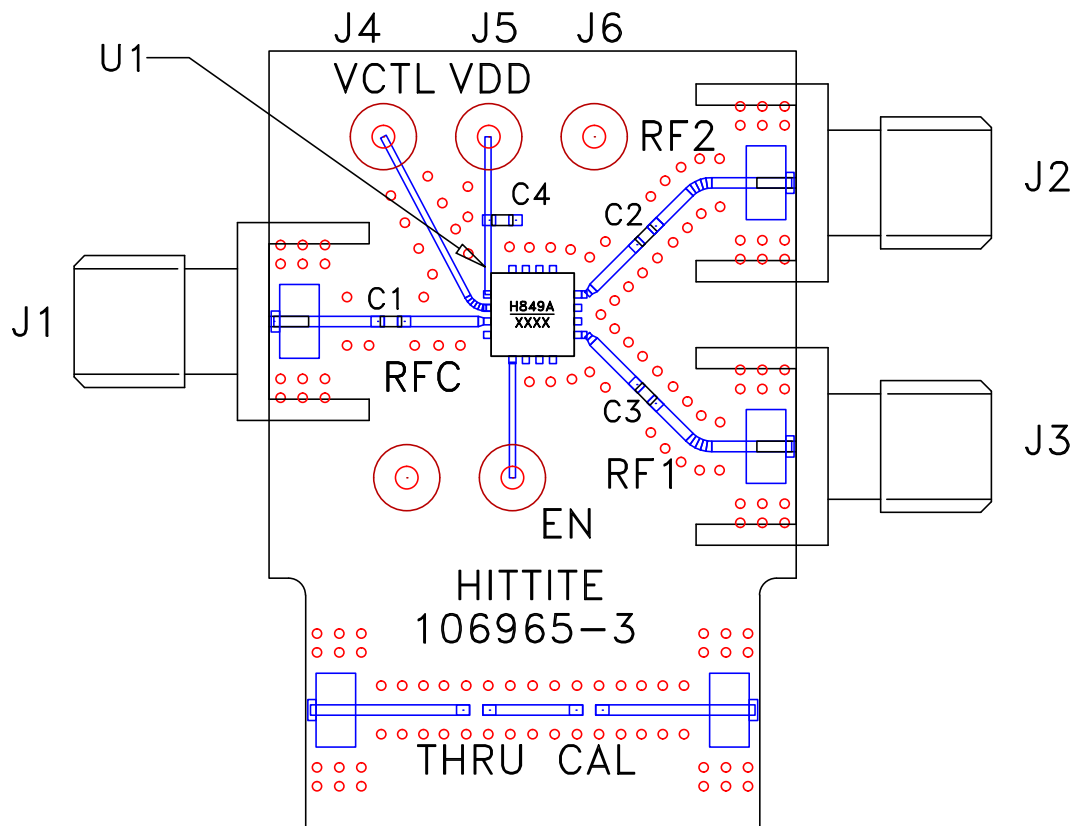
Application Circuit





**HIGH ISOLATION SPDT
NON-REFLECTIVE SWITCH, DC - 6 GHz**

Evaluation PCB



List of Materials for Evaluation PCB EV1HMC849ALP4C ^[1]

Item	Description
J1 - J3	PC Mount SMA RF Connector
J4 - J8	DC Pin
C1 - C4	100 pF Capacitor, 0402 Pkg.
U1	HMC849ALP4CE SPDT Switch
PCB [2]	106965 Evaluation PCB

[1] Reference this number when ordering complete evaluation PCB

[2] Circuit Board Material: Rogers 4350 or Arlon 25FR

The circuit board used in the final application should be generated with proper RF circuit design techniques. Signal lines at the RF port should have 50 Ohm impedance and the package ground leads and backside ground slug should be connected directly to the ground plane similar to that shown above. The evaluation circuit board shown above is available from Analog Devices, upon request.



Notes:

HMC849ALP4CE

v01.0818

**HIGH ISOLATION SPDT
NON-REFLECTIVE SWITCH, DC - 6 GHz**