

Maximum Ratings (@ $T_A = +25$ °C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	Conditions
Peak Pulse Power Dissipation	Ppp	70	W	8/20µs (Note 5)
Peak Pulse Current	IPP	7.5	Α	8/20µs (Note 5)
ESD Protection – Contact Discharge	V _{ESD_Contact}	±15	kV	Standard IEC 61000-4-2
ESD Protection – Air Discharge	V _{ESD Air}	±15	kV	Standard IEC 61000-4-2

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Thermal Resistance, Junction to Ambient Typical (Note 5)	$R_{ heta JA}$	+206	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Conditions
Reverse Breakdown Voltage	VBR	6	-	9	V	$I_R = 1mA$
Reverse Leakage Current (Note 6)	I_R	-	-	70	nA	V _R =3V
Dynamic Impedance	Rd		0.35	-	Ω	IPP = 1 to 5A, 8/20µs
Channel Input Capacitance	Cin	-	0.8	1.2	pF	$VIN = 0V$, $f = 1MHz$, $V_{OSC} = 30mV$

Notes:

- 5. Device mounted on FR-4 PCB pad layout (2oz copper) as shown on Diodes, Inc. suggested pad layout AP02001, which can be found on our website at http://www.diodes.com.
- 6. Short duration pulse test used to minimize self-heating effect.

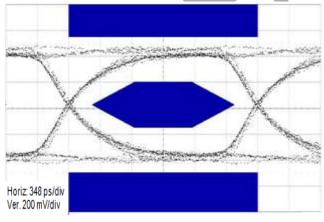


Figure 1. Eye diagram, board only (according to USB2.0 high speed specification)

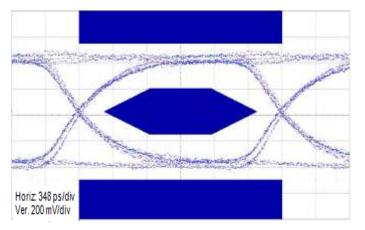
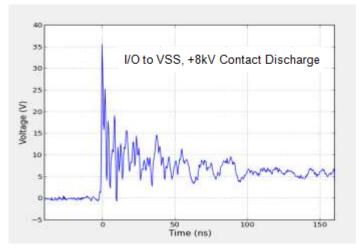


Figure 2. Eye diagram, board with DUSBULC6-CSP4 (according to USB2.0 high speed specification)





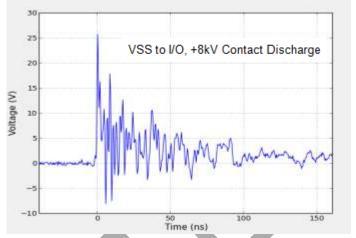
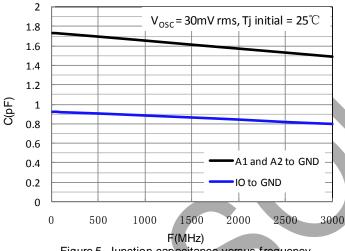


Figure 3. ESD response to IEC 61000-4-2

Figure 4. ESD response to IEC 61000-4-2



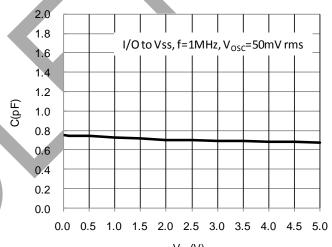
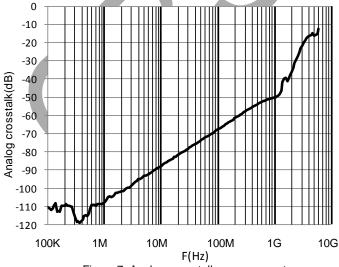


Figure 5. Junction capacitance versus frequency (typical values)

 $V_{\rm I/0}({\rm V})$ Figure 6. Junction Capacitance versus Input Voltage



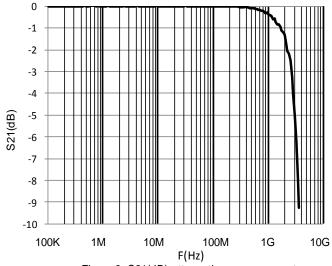


Figure 7, Analog crosstalk measurement

Figure 8, S21(dB) attenuation measurement



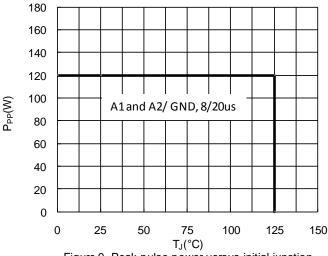


Figure 9. Peak pulse power versus initial junction temperature(maximum values, pulse 8/20us)

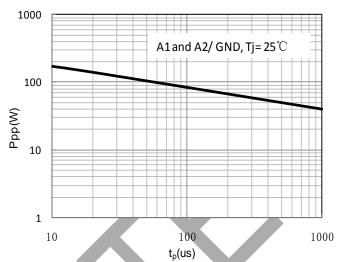


Figure 10. Peak pulse power versus exponential pulse duration(maximum values)

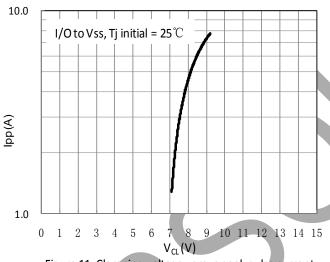


Figure 11. Clamping voltage versus peak pulse current (typical values, pulse 8/20us)

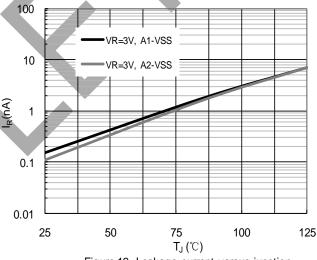
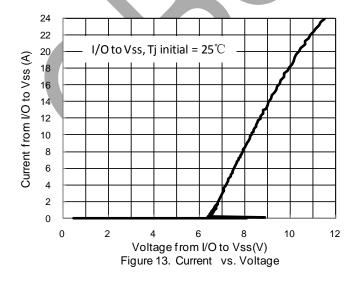


Figure 12. Leakage current versus junction temperature (typical values)



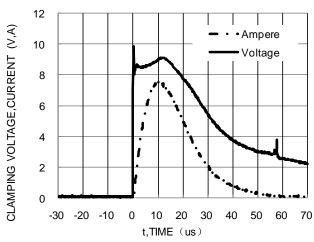
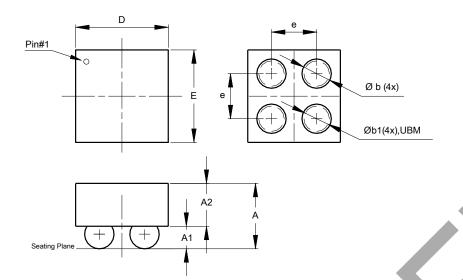


Figure 14. Waveform of Clamping Voltage, Current vs. Time(8/20us, I/O to Vss)



Package Outline Dimensions

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.

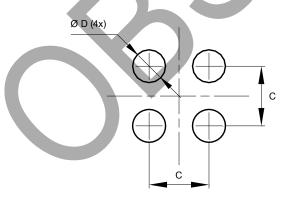


W-WLB0808-4					
Dim	Min	Max	Тур		
Α	0.545	0.665	0.605		
A1	0.170	0.230	0.200		
A2	0.375	0.435	0.405		
b	0.240	0.280	0.260		
b1	0.235	0.245	0.240		
D	0.790	0.850	0.820		
E	0.790	0.850	0.820		
e 0.400 BSC					
All Dimensions in mm					

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.





Dimensions	Value (in mm)
С	0.400
D	0.220



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