

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Drain-Source Voltage		V _{DSS}	60	V
Gate-Source Voltage		V _{GSS}	±20	V
Continuous Drain Current (Note 5)	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	ID	13.5 11	A
Continuous Drain Current (Note 6)	T _C = +25°C (Note 9) T _C = +70°C	I _D	80 77	A
Maximum Continuous Body Diode Forward Current (Note 6)		IS	80	А
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)		I _{DM}	125	А
Avalanche Current, L=0.1mH		I _{AS}	20	А
Avalanche Energy, L=0.1mH		E _{AS}	20	mJ

Thermal Characteristics

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)	T _A = +25°C	PD	2.2	W
Thermal Resistance, Junction to Ambient (Note 5)		R _{0JA}	57	°C/W
Total Power Dissipation (Note 6)	T _C = +25°C	PD	113	W
Thermal Resistance, Junction to Case (Note 6)		R _{ejc}	1.1	°C/W
Operating and Storage Temperature Range		T _{J,} T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						÷
Drain-Source Breakdown Voltage	BV _{DSS}	60	-	-	V	$V_{GS} = 0V, I_D = 1mA$
Zero Gate Voltage Drain Current	I _{DSS}	-	-	1	μA	$V_{DS} = 48V, V_{GS} = 0V$
Gate-Source Leakage	I _{GSS}	-	-	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V _{GS(TH)}	1	-	3	V	$V_{DS} = V_{GS}, I_D = 250 \mu A$
Static Drain-Source On-Resistance		-	6	8	mΩ	$V_{GS} = 10V, I_D = 20A$
	R _{DS(ON)}	-	8	12		$V_{GS} = 4.5V, I_D = 20A$
Diode Forward Voltage	V _{SD}	-	0.9	1.2	V	$V_{GS} = 0V, I_{S} = 20A$
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C _{iss}	-	2090	-	pF	$V_{DS} = 30V, V_{GS} = 0V,$ f = 1MHz
Output Capacitance	Coss	-	746	-		
Reverse Transfer Capacitance	C _{rss}	-	38.5	-		
Gate Resistance	Rg	-	0.59	-	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$
Total Gate Charge (V _{GS} = 4.5V)	Qg	-	19.3	-		V _{DS} = 30V, I _D = 20A
Total Gate Charge (V _{GS} = 10V)	Qg	-	41.3	-	nC	
Gate-Source Charge	Q _{gs}	-	6.0	-	nc	
Gate-Drain Charge	Q _{gd}	-	8.8	-		
Turn-On Delay Time	t _{D(ON)}	-	5.7	-		$V_{DD} = 30V, V_{GS} = 10V,$ $I_D = 20A, R_G = 3\Omega$
Turn-On Rise Time	t _R	-	4.3	-	ns	
Turn-Off Delay Time	t _{D(OFF)}	-	23.4	-		
Turn-Off Fall Time	tF	-	9.7	-	1	
Body Diode Reverse Recovery Time	t _{RR}	-	35.4	-	ns	
Body Diode Reverse Recovery Charge	Q _{RR}	-	38.2	-	nC	I _F = 20A, di/dt = 100A/μs

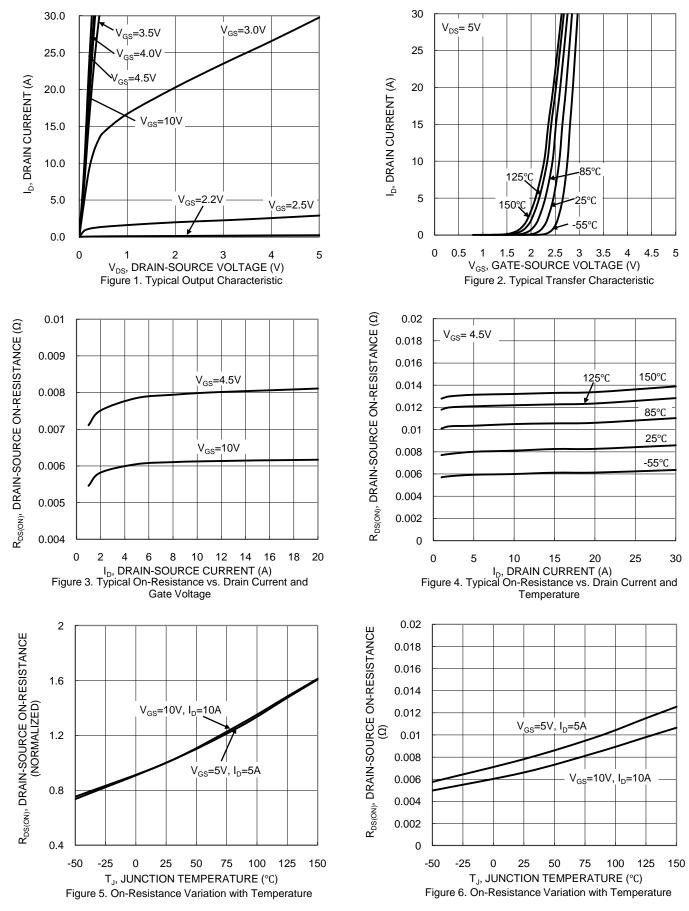
 Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate.
Thermal resistance from junction to soldering point (on the exposed drain pad). Notes:

7. Short duration pulse test used to minimize self-heating effect.

8. Guaranteed by design. Not subject to product testing.
9. Package limited.



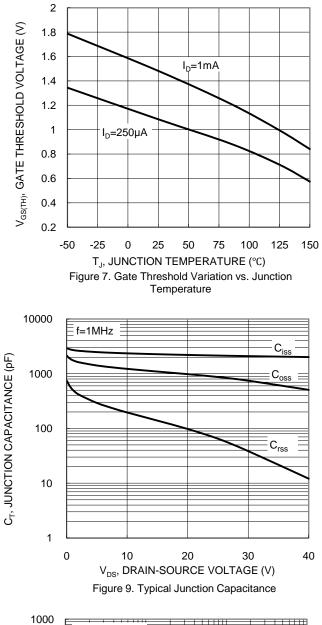
DMT6010LPS

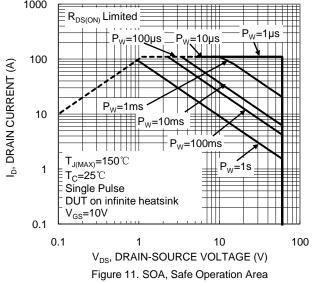


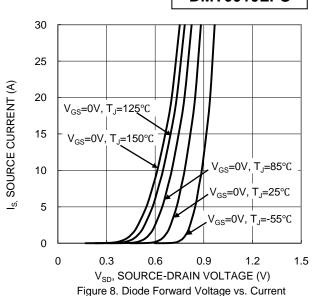
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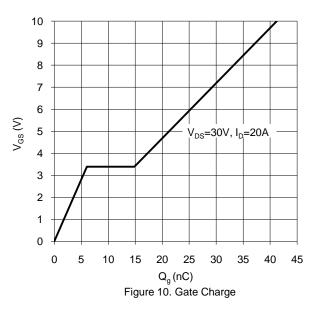


DMT6010LPS



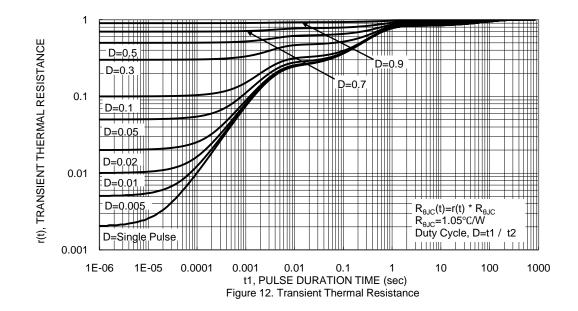








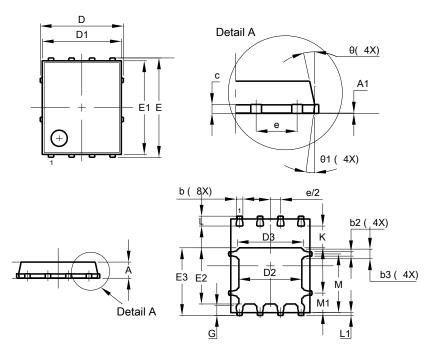






Package Outline Dimensions

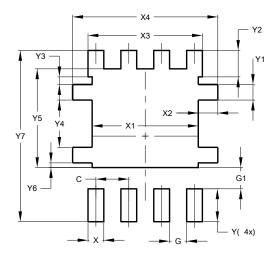
Please see http://www.diodes.com/package-outlines.html for the latest version.



PowerDI5060-8					
Dim	Min	Тур			
Α	0.90	1.10	1.00		
A1	0.00	0.05	-		
b	0.33	0.51	0.41		
b2	0.200	0.350	0.273		
b3	0.40	0.80	0.60		
С	0.230	0.330	0.277		
D	5.15 BSC				
D1	4.70	5.10	4.90		
D2	3.70	4.10	3.90		
D3	3.90	4.30	4.10		
ш	(6.15 BSC			
E1	5.60	6.00	5.80		
E2	3.28	3.68	3.48		
E3	3.99	4.39	4.19		
е	1.27 BSC				
G	0.51	0.71	0.61		
K	0.51	-	-		
L	0.51	0.71	0.61		
L1	0.100	0.200	0.175		
М	3.235	4.035	3.635		
M1	1.00	1.40	1.21		
θ	10º	12º	11º		
θ1	6°	8º	7°		
All Dimensions in mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



Dimensions	Value (in mm)
С	1.270
G	0.660
G1	0.820
Х	0.610
X1	4.100
X2	0.755
X3	4.420
X4	5.610
Y	1.270
Y1	0.600
Y2	1.020
Y3	0.295
Y4	1.825
Y5	3.810
Y6	0.180
Y7	6.610



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