

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V_{DSS}	-20	V
Gate-Source Voltage			V_{GSS}	±12	V
Continuous Drain Current (Note 6) V _{GS} = -4.5V	Steady State	$T_A = +25$ °C $T_A = +70$ °C	I _D	-2.5 -2.0	А
Maximum Continuous Body Diode Forward Current (Note 6)			I _S	-1.0	Α
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%) (Note 6)			I _{DM}	-15	Α

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

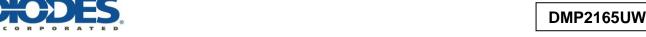
Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)		P_{D}	0.5	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	$R_{\theta JA}$	259	°C/W
Total Power Dissipation (Note 6)		P_{D}	0.7	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	$R_{\theta JA}$	175	°C/W
Operating and Storage Temperature Range		T _{J,} T _{STG}	-55 to +150	°C

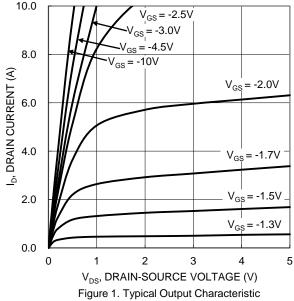
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

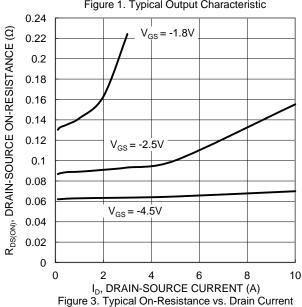
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BV _{DSS}	-20	_		V	V _{GS} = 0V, I _D = -250μA	
Zero Gate Voltage Drain Current T _J = +25°C	I _{DSS}	_	_	-1.0	μΑ	V _{DS} = -20V, V _{GS} = 0V	
Gate-Source Leakage	Igss	_	_	±100	nA	$V_{GS} = \pm 12V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	V _{GS(TH)}	-0.4	_	-1.0	٧	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	
			63	90	mΩ	$V_{GS} = -4.5V, I_D = -1.5A$	
Static Drain-Source On-Resistance	R _{DS(ON)}	_	83	120		$V_{GS} = -2.5V, I_D = -1.2A$	
	, ,		160	180		V _{GS} = -1.8V, I _D = -1.2A	
Diode Forward Voltage	V_{SD}	_	-0.7	-1.1	V	V _{GS} = 0V, I _S = -1A	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	C _{iss}	_	335	_	рF	V _{DS} = -15V, V _{GS} = 0V f = 1.0MHz	
Output Capacitance	Coss	_	72	_	pF		
Reverse Transfer Capacitance	C _{rss}	_	32	_	pF		
Gate Resistance	R_{G}	_	15.5	_	Ω	$V_{GS} = 0V, V_{DS} = 0V, f = 1.0MHz$	
Total Gate Charge	Qg	_	3.5	_	nC	V _{GS} = -4.5V, V _{DS} = -4V, -I _D = -3.5A	
Gate-Source Charge	Q _{qs}	_	0.4	_	nC		
Gate-Drain Charge	Q_{ad}	_	1.1	_	nC		
Turn-On Delay Time	t _{D(ON)}	_	3.7	_	ns	$V_{DS} = -4V, V_{GS} = -4.5V,$ $R_G = 6\Omega, I_D = -1A$	
Turn-On Rise Time	t _R	_	8.7	_	ns		
Turn-Off Delay Time	t _{D(OFF)}	_	17.8	_	ns		
Turn-Off Fall Time	t _F	_	8	_	ns		
Reverse Recovery Time	t _{RR}	_	9	_	ns	I _F = -4A, di/dt = 100A/μs	
Reverse Recovery Charge	Q _{RR}		1.5	_	nC	I _F = -4A, di/dt = 100A/μs	

Notes:

- 5. Device mounted on FR-4 PCB, with minimum recommended pad layout.
- 6. Device mounted on 1" x 1" FR-4 PCB with high coverage 2oz. Copper, single sided.
- 7. Short duration pulse test used to minimize self-heating effect.
- 8. Guaranteed by design. Not subject to product testing.







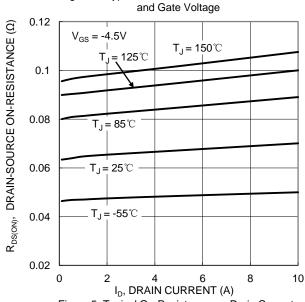
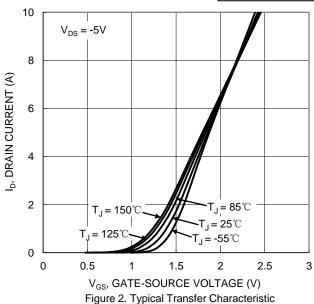


Figure 5. Typical On-Resistance vs. Drain Current and Junction Temperature



0.5 $R_{DS(ON)}$, DRAIN-SOURCE ON-RESISTANCE (Ω) 0.45 0.4 0.35 0.3 0.25 -1.2A 0.2 0.15 0.1 0.05 0 0 6 8 10 12

V_{GS}, GATE-SOURCE VOLTAGE (V)

Figure 4. Typical Transfer Characteristic

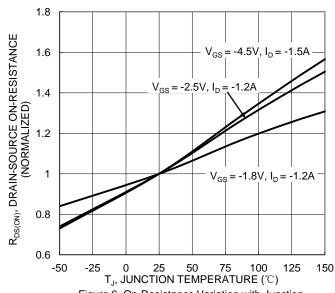
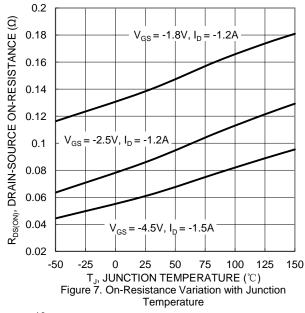
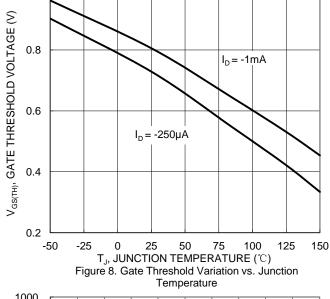


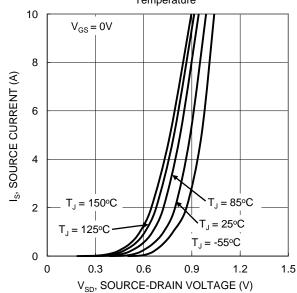
Figure 6. On-Resistance Variation with Junction Temperature

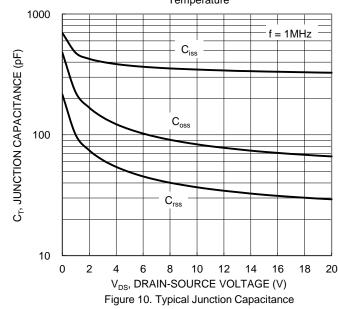


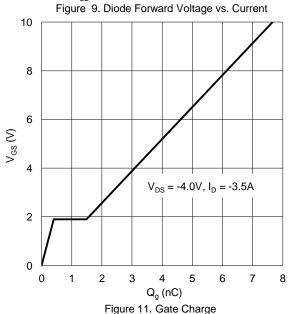


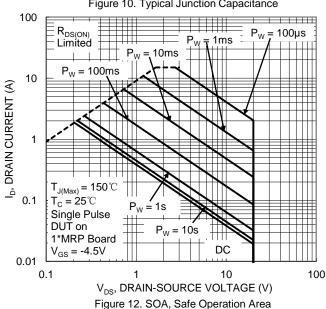














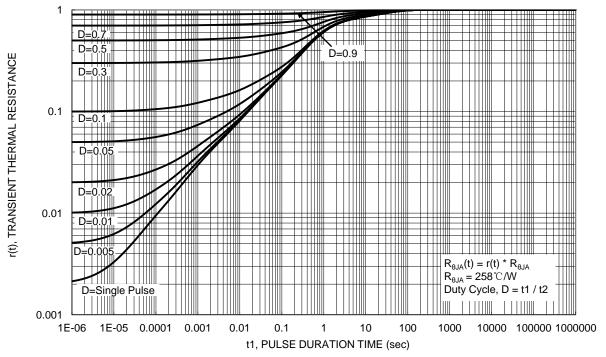


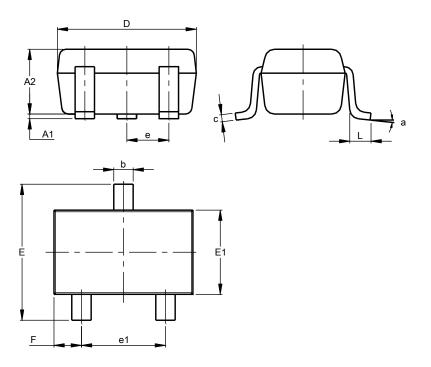
Figure 13. Transient Thermal Resistance



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT323

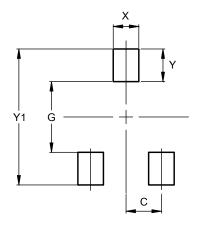


SOT323						
Dim	Min	Max	Тур			
A1	0.00	0.10	0.05			
A2	0.90	1.00	0.95			
b	0.25	0.40	0.30			
С	0.10	0.18	0.11			
D	1.80	2.20	2.15			
Е	2.00	2.20	2.10			
E1	1.15	1.35	1.30			
е	0.650 BSC					
e1	1.20	1.40	1.30			
F	0.375	0.475	0.425			
L	0.25	0.40	0.30			
а	0°	8°				
All Dimensions in mm						

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT323



Dimensions	Value (in mm)		
C	0.650		
G	1.300		
Х	0.470		
Y	0.600		
Y1	2 500		



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