

## Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units	
Drain-Source Voltage		V <sub>DSS</sub>	-20	V
Gate-Source Voltage		V <sub>GSS</sub>	±8	V
Continuous Drain Current (Note 6) $V_{GS} = -4.5V$	T <sub>A</sub> = +25°C T <sub>A</sub> = +70°C	lo	-200 -150	mA
Continuous Drain Current (Note 6) $V_{GS} = -2.5V$	T <sub>A</sub> = +25°C T <sub>A</sub> = +70°C	ID	-170 -130	mA
Pulsed Drain Current	T <sub>P</sub> = 10μs	I <sub>DM</sub>	-600	mA

# Thermal Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units
Total Power Dissipation (Note 6)	PD	330	mW
Thermal Resistance, Junction to Ambient (Note 6)	R <sub>ØJA</sub>	377.16	°C/W
Operating and Storage Temperature Range	TJ, TSTG	-55 to +150	°C

#### Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

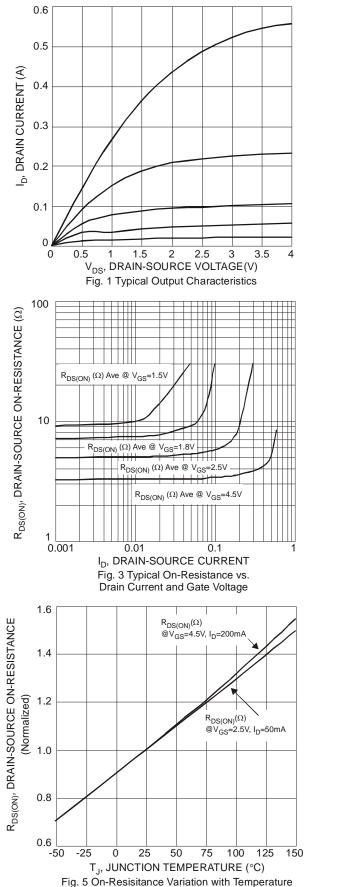
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	<b>BV</b> <sub>DSS</sub>	-20	—	_	V	$V_{GS} = 0V, I_D = -250\mu A$	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	_		-100	nA	$V_{DS} = -16V, V_{GS} = 0V$	
		_		-50	nA	$V_{DS} = -5.0V, V_{GS} = 0V$	
Gate-Source Leakage	1		—	±100	nA	$V_{GS} = \pm 5.0 V, V_{DS} = 0 V$	
5	I <sub>GSS</sub>			±1	μA	$V_{GS} = \pm 8.0V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	V <sub>GS(th)</sub>	-0.45	—	-1.15	V	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	
Static Drain-Source On-Resistance		_		5.5		$V_{GS} = -4.5V, I_D = -100mA$	
	R <sub>DS(ON)</sub>	_		7.5		$V_{GS} = -2.5V, I_{D} = -50mA$	
				11.5	Ω	$V_{GS} = -1.8V, I_D = -20mA$	
				17.5		$V_{GS} = -1.5V, I_D = -10mA$	
			20	_		$V_{GS} = -1.2V, I_D = -1mA$	
Forward Transfer Admittance	Y <sub>fs</sub>		200	_	mS	$V_{DS} = -10V, I_D = -0.2A$	
Diode Forward Voltage (Note 7)	V <sub>SD</sub>	-0.5		-1.2	V	$V_{GS} = 0V, I_{S} = -115mA$	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	Ciss	_	13.72	27.44	pF	V <sub>DS</sub> = -15V, V <sub>GS</sub> = 0V f = 1.0MHz	
Output Capacitance	Coss		4.01	8.02	pF		
Reverse Transfer Capacitance	Crss	_	2.34	4.68	pF		
SWITCHING CHARACTERISTICS (Note 8)							
Turn-On Delay Time	t <sub>d(on)</sub>	_	7.7	—			
Rise Time	tr		19.3	_	ns	$V_{GS} = -4.5V, V_{DD} = -15V$	
Turn-Off Delay Time	t <sub>d(off)</sub>		25.9		115	$I_{D} = -180 \text{mA}, R_{G} = 2.0 \Omega$	
Fall Time	t <sub>f</sub>		31.5	_			

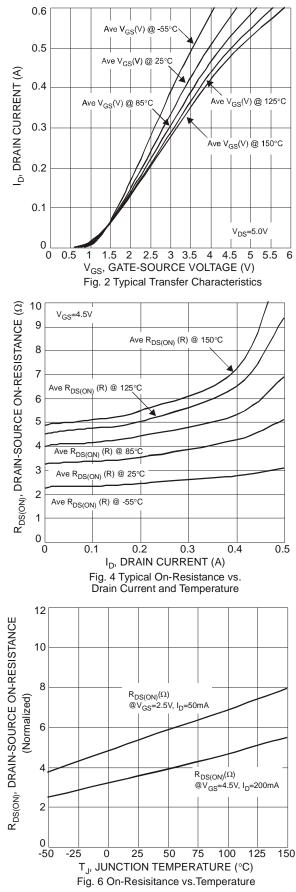
 Device mounted on 1" × 1" FR-4 substrate PCB, with minimum recommended pad layout, single sided.
Short duration pulse test used to minimize self-heating effect. Notes:

8. Guaranteed by design. Not subject to production testing.

## DMP210DUDJ



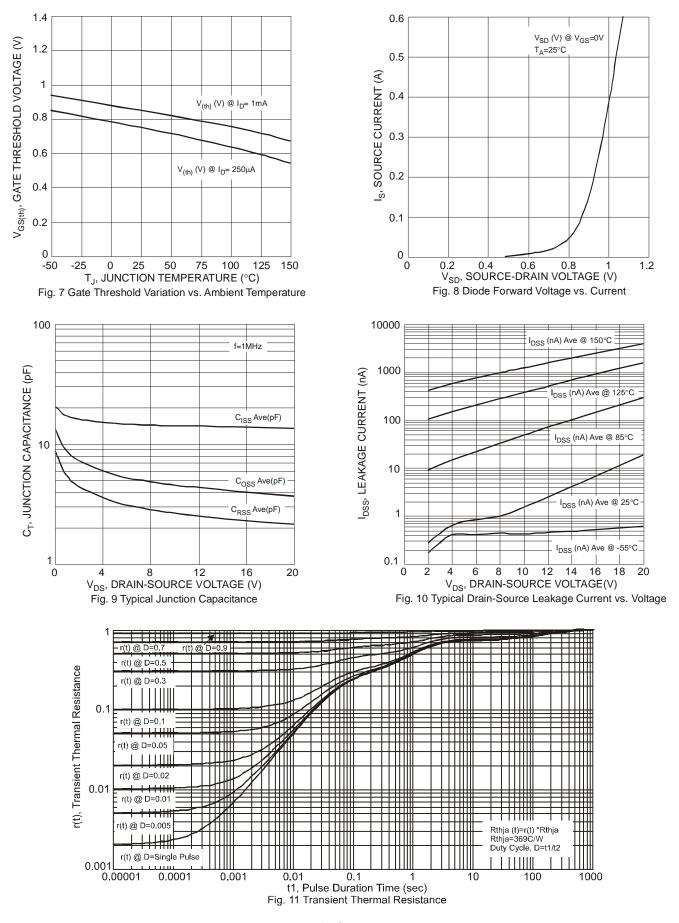




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## DMP210DUDJ

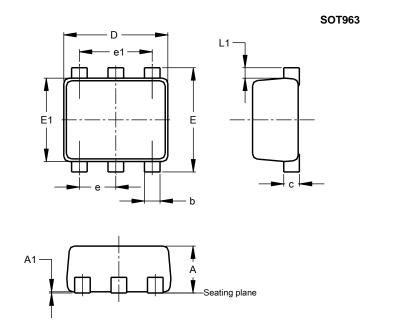






### **Package Outline Dimensions**

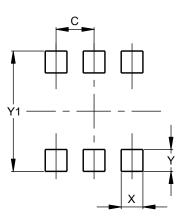
Please see http://www.diodes.com/package-outlines.html for the latest version.



SOT963					
Dim	Min	Max	Тур		
Α	0.40	0.50	0.45		
A1	0.00	0.05	—		
b	0.10	0.20	0.15		
С	0.120	0.180	0.150		
D	0.95	1.05	1.00		
Е	0.95	1.05	1.00		
E1	0.75	0.85	0.80		
е	_		0.35		
e1	_		0.70		
L1	0.05	0.15	0.10		
All Dimensions in mm					

### Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



SOT963

Dimensions	Value (in mm)		
С	0.350		
Х	0.200		
Y	0.200		
Y1	1.100		

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