

Maximum Ratings ($@T_A = +25^{\circ}C$, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V _{DSS}	-8	V
Gate-Source Voltage			V _{GSS}	-6	V
Continuous Drain Current (Note 5) V_{GS} = -4.5V	Steady State	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	I _D	-10 -8	А
Continuous Drain Current (Note 6) V_{GS} = -4.5V	Steady State	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	ID	-7.4 -6.0	А
Pulsed Drain Current (Pulse Duration 10µs, Duty Cycle ≤1%)			I _{DM}	-50	A
Continuous Source Pin Current (Note 6)			ls	-2	A
Pulsed Source Pin Current (Pulse Duration 10µs, Duty Cycle ≤1%)			Ism	-15	A
Continuous Gate Current			I _G	-0.5	A

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 5)	PD	0.89	W
Total Power Dissipation (Note 6)	PD	1.57	W
Thermal Resistance, Junction to Ambient (Note 5)	R _{0JA}	142.1	°C/W
Thermal Resistance, Junction to Ambient (Note 6)	R _{0JA}	80.5	°C/W
Operating and Storage Temperature Range	T _{J,} T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)				•			
Drain-Source Breakdown Voltage	BV _{DSS}	-8		_	V	$V_{GS} = 0V, I_D = -250\mu A$	
Gate to Source Voltage	BV _{SGS}	6			V	$V_{DS} = 0V, I_{S} = 250 \mu A$	
Zero Gate Voltage Drain Current $@T_C = +25^{\circ}C$	I _{DSS}	—	—	-1	μA	V_{DS} = -4.0V, V_{GS} = 0V	
Gate-Source Leakage	Igss		—	-100	nA	$V_{GS} = -4.0V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)				•			
Gate Threshold Voltage	V _{GS(TH)}	-0.4	-0.8	-1.1	V	$V_{DS} = V_{GS}$, $I_D = -250 \mu A$	
			8.2	10	mΩ	$V_{GS} = -4.5V, I_D = -2A$	
Static Drain-Source On-Resistance	R _{DS(ON)}		10	13		$V_{GS} = -3.0V, I_D = -2A$	
			11	14		$V_{GS} = -2.5V, I_D = -2A$	
Forward Transfer Admittance	Y _{fs}	_	16.8	_	S	$V_{DS} = -4V, I_D = -2A$	
Diode Forward Voltage (Note 6)	V _{SD}	_	-0.7	-1	V	$V_{GS} = 0V, I_{S} = -2A$	
Reverse Recovery Charge	Q _{RR}		6.3	_	nC	$V_{DD} = -5V, I_F = -2A,$	
Reverse Recovery Time	t _{RR}	_	18.5		ns	di/dt = 200A/µs	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	Ciss		817	1,060	pF	$V_{DS} = -4V$, $V_{GS} = 0V$, f = 1.0MHz	
Output Capacitance	Coss		595	770	pF		
Reverse Transfer Capacitance	Crss		269	350	pF		
Series Gate Resistance	R _G		1.9	_	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz$	
Total Gate Charge	Qg		8.1	10.5	nC	$V_{GS} = -4.5V, V_{DS} = -4V,$ $I_D = -2A$	
Gate-Source Charge	Q _{gs}		0.9	_	nC		
Gate-Drain Charge	Q _{gd}		1.8	_	nC		
Turn-On Delay Time	t _{D(ON)}	_	6.2	10	ns	$V_{DD} = -4V, V_{GS} = -4.5V,$ $I_{DS} = -2A, R_G = 10\Omega$	
Turn-On Rise Time	t _R		22.6		ns		
Turn-Off Delay Time	tD(OFF)		30.1	48	ns		
Turn-Off Fall Time	t _F		22.7	_	ns		

Notes: 5. Device mounted on FR-4 PCB with minimum recommended pad layout.

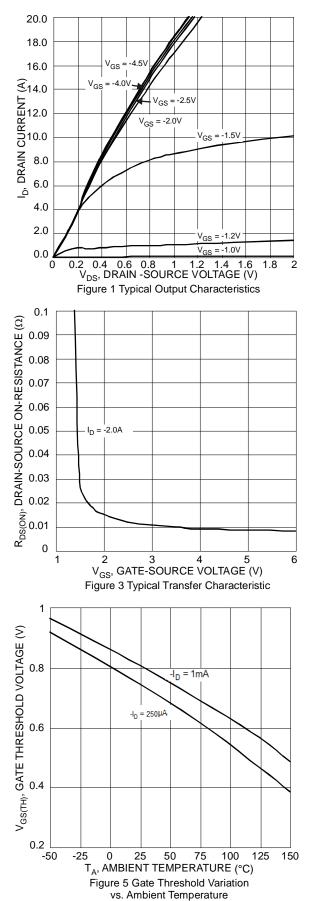
6. Device mounted on FR-4 material with 1-inch² (6.45cm²), 2oz (0.071mm thick) Cu.

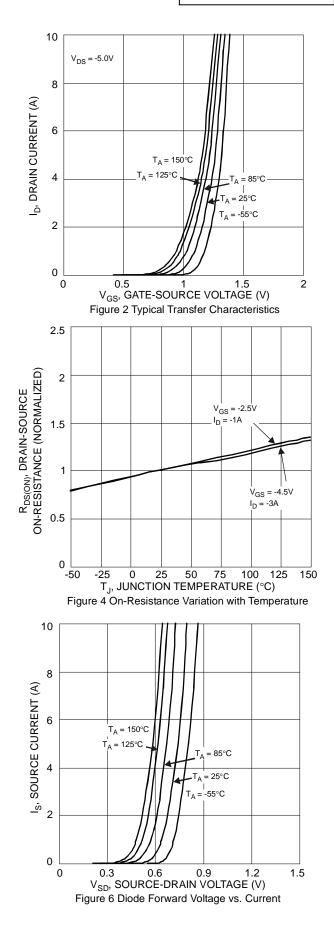
7. Short duration pulse test used to minimize self-heating effect.

8. Guaranteed by design. Not subject to production testing.

DMP1011UCB9



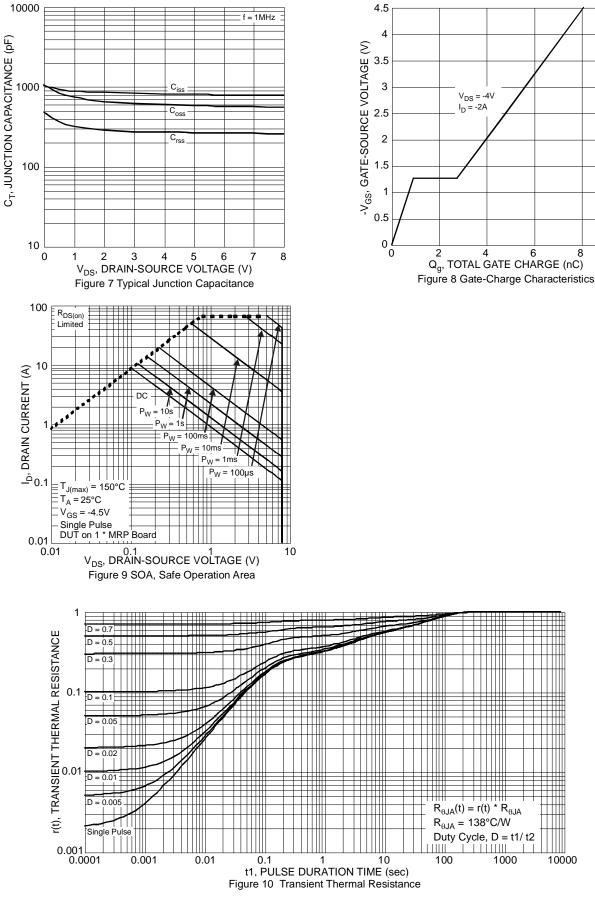




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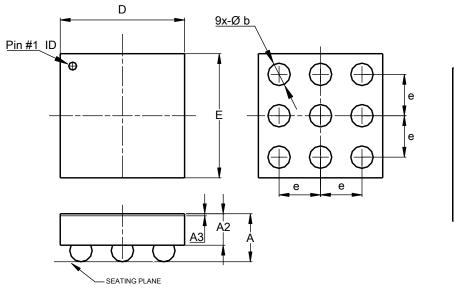
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Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

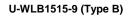
U-WLB1515-9 (Type B)

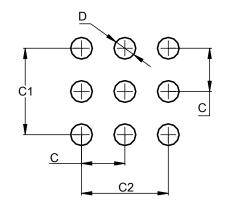


U-WLB1515-9 (Type B)					
Dim	Min	Max	Тур		
Α		0.60			
A2		0.36	0.36		
A3	0.020	0.030	0.025		
b	0.22	0.32	0.27		
D	1.47	1.50	1.49		
Е	1.47	1.50	1.49		
e			0.50		
All Dimensions in mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.





Dimensions	Value (in mm)
С	0.50
C1	1.00
C2	1.00
D	0.25



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