

Maximum Ratings (@ $T_A = +25^{\circ}C$, unless otherwise specified.)

Characteristic Drain-Source Voltage			Symbol	Value	Units V
			V _{DSS}	30	
Gate-Source Voltage			V _{GSS}	±20	V
Continuous Drain Current (Note 6) V_{GS} = 10V t<10s	,	T _A = +25°C T _A = +70°C	ID	6.6 5.3	А
	T _A = +25°C T _A = +70°C	ID	8.5 6.8	А	
Maximum Body Diode Forward Current (Note 6)			I _S	3.0	А
Pulsed Drain Current (10μs pulse, duty cycle = 1%)			I _{DM}	35	А

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Units
Total Power Dissipation (Note 5)	T _A = +25°C	D	1.2	W
	T _A = +70°C	PD	0.8	
Thermal Resistance, Junction to Ambient (Note 5)	Steady state	D	100	°C/W
	t<10s	$R_{ hetaJA}$	60	°C/W
Total Power Dissipation (Note 6)	T _A = +25°C	Р	1.5	W
	T _A = +70°C	PD	1.0	
Thermal Resistance, Junction to Ambient (Note 6)	Steady state	D	83	°C/W
	t<10s	R _{θJA}	50	°C/W
Thermal Resistance, Junction to Case (Note 6)		R _{θJC}	14.5	°C/W
Operating and Storage Temperature Range		T _{J,} T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

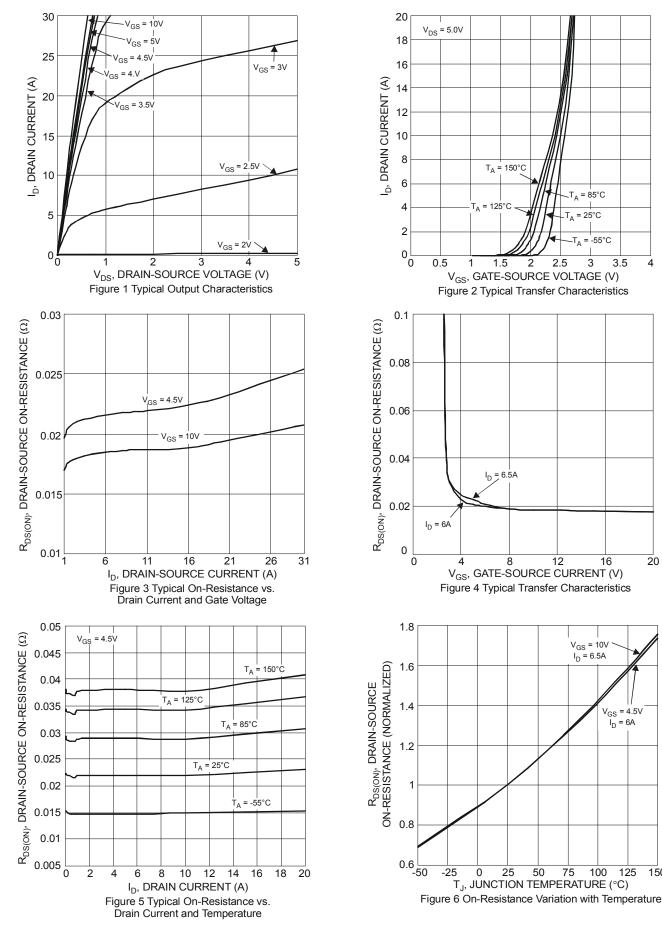
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)			. 16		-		
Drain-Source Breakdown Voltage	BV _{DSS}	30		_	V	$V_{GS} = 0V, I_D = 250 \mu A$	
Zero Gate Voltage Drain Current	I _{DSS}		_	1.0	μA	$V_{DS} = 30V, V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}		_	±100	nA	V _{GS} = ±20V, V _{DS} = 0V	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	V _{GS(th)}	1.0	1.5	2.0	V	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	
Static Drain-Source On-Resistance			19	23	mΩ	V _{GS} = 10V, I _D = 6.5A	
	R _{DS(ON)}		22	30		V _{GS} = 4.5V, I _D = 6.0A	
Diode Forward Voltage	V _{SD}		0.7	1.2	V	V _{GS} = 0V, I _S = 1.0A	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	C _{iss}	_	643	—		V_{DS} = 15V, V_{GS} = 0V f = 1.0MHz	
Output Capacitance	Coss	_	65	_	pF		
Reverse Transfer Capacitance	C _{rss}		49	_			
Gate Resistance	R _G		2.5	_	Ω	V _{DS} = 0V, V _{GS} = 0V, f = 1.0MHz	
Total Gate Charge (V _{GS} = 4.5V)	Qg		5.7	_			
Total Gate Charge (V _{GS} = 10V)	Qg	_	12.5	_	nC	V _{DS} = 15V, I _D = 4.0A	
Gate-Source Charge	Q _{gs}		1.7	_	nc		
Gate-Drain Charge	Q _{gd}		1.8	_			
Turn-On Delay Time	t _{D(on)}		2.2	_		V_{GS} = 10V, V_{DD} = 15V, R_{G} = 6.0 Ω , I _D = 6.5A	
Turn-On Rise Time	tr		2.5	_	-0		
Turn-Off Delay Time	t _{D(off)}		12.1	—	nS		
Turn-Off Fall Time	t _f		3.0	_	1		
Body Diode Reverse Recovery Time	t _{rr}		6.5	—	nS	I _F = 6.5A, dI/dt = 100A/µs	
Body Diode Reverse Recovery Charge	Q _{rr}		1.7	_	nC	I _F = 6.5A, dl/dt = 100A/µs	

 Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
 Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
 Short duration pulse test used to minimize self-heating effect.
 Guaranteed by design. Not subject to product testing. Notes:



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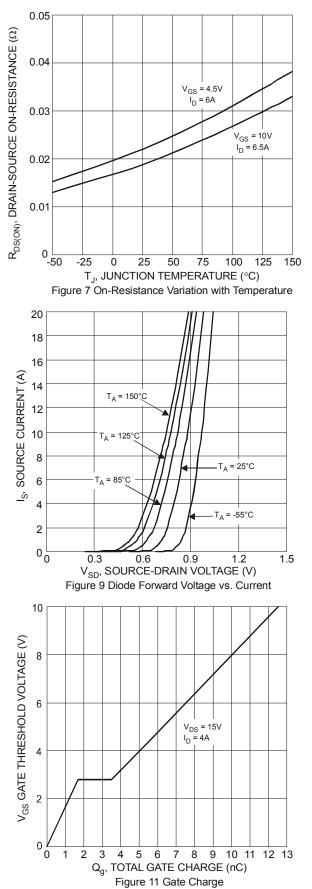


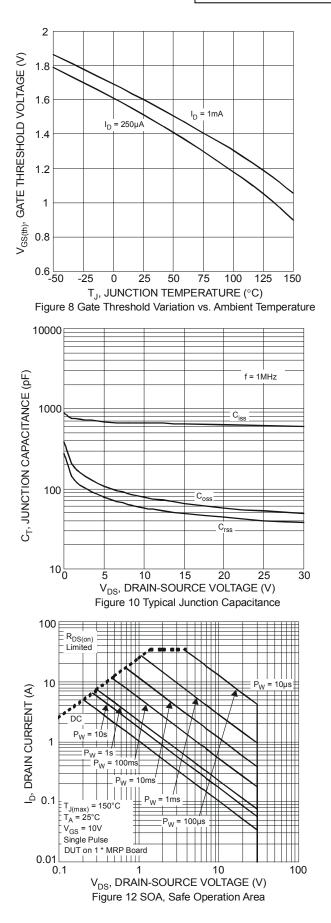
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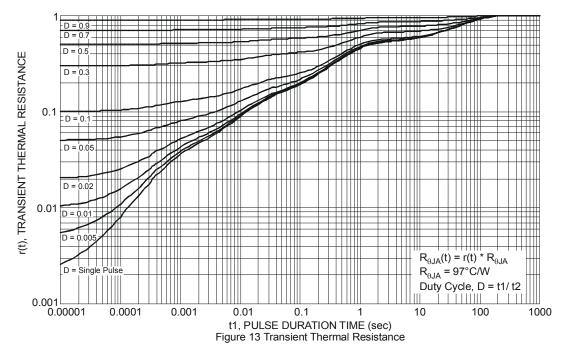






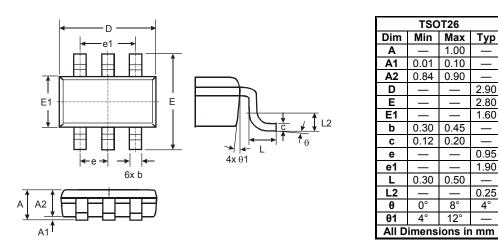
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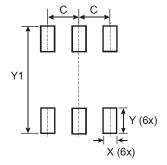
Package Outline Dimensions

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for latest version.



Dimensions	Value (in mm)
C	0.950
Х	0.700
Y	1.000
Y1	3.199



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