

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V _{DSS}	20	V
Gate-Source Voltage			V _{GSS}	±12	V
Continuous Drain Current (Note 6) V _{GS} = 10V	Steady State	T _A = +25°C T _A = +70°C	I _D	3.5 3.0	A
Maximum Continuous Body Diode Forward Current (Note 6)			I _S	1.0	A
Pulsed Drain Current (10μs Pulse, Duty Cycle = 1%) (Note 6)			I _{DM}	20	A

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Total Power Dissipation (Note 5)			P _D	0.5	mW
Thermal Resistance, Junction to Ambient (Note 5)	Steady State		R _{θJA}	259	°C/W
Total Power Dissipation (Note 6)			P _D	0.7	mW
Thermal Resistance, Junction to Ambient (Note 6)	Steady State		R _{θJA}	175	°C/W
Operating and Storage Temperature Range			T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV _{DSS}	20	—	—	V	V _{GS} = 0V, I _D = 250μA
Zero Gate Voltage Drain Current	I _{DSS}	—	—	1	μA	V _{DS} = 20V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	—	—	±100	nA	V _{GS} = ±12V, V _{DS} = 0V
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V _{GS(TH)}	0.4	—	1.2	V	V _{DS} = V _{GS} , I _D = 250μA
Static Drain-Source On-Resistance	R _{DS(ON)}	—	31.5	42	mΩ	V _{GS} = 10V, I _D = 3A
			32	45		V _{GS} = 4.5V, I _D = 2A
			40.5	60		V _{GS} = 2.5V, I _D = 2A
			48	91		V _{GS} = 1.8V, I _D = 1A
Diode Forward Voltage	V _{SD}	—	0.78	1.2	V	V _{GS} = 0V, I _S = 1A
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C _{iss}	—	281	—	pF	V _{DS} = 10V, V _{GS} = 0V f = 1.0MHz
Output Capacitance	C _{oss}	—	50	—	pF	
Reverse Transfer Capacitance	C _{rss}	—	39	—	pF	
Gate Resistance	R _g	—	3.1	—	Ω	f = 1.0MHz, V _{GS} = 0V, V _{DS} = 0V
Total Gate Charge (V _{GS} = 4.5V)	Q _g	—	3.6	—	nC	V _{DS} = 10V, I _D = 6.0A
Total Gate Charge (V _{GS} = 10V)	Q _g	—	7.7	—	nC	
Gate-Source Charge	Q _{gs}	—	0.5	—	nC	
Gate-Drain Charge	Q _{gd}	—	0.9	—	nC	
Turn-On Delay Time	t _{D(ON)}	—	2.0	—	ns	V _{GS} = 4.5V, V _{DD} = 10V, R _g = 6Ω, I _D = 6.0A
Turn-On Rise Time	t _r	—	4.9	—	ns	
Turn-Off Delay Time	t _{D(OFF)}	—	9.9	—	ns	
Turn-Off Fall Time	t _f	—	3.3	—	ns	
Body Diode Reverse Recovery Time	t _{RR}	—	5.4	—	ns	I _F = 6.0A, di/dt = 100A/μs
Body Diode Reverse Recovery Charge	Q _{RR}	—	0.7	—	nC	I _F = 6.0A, di/dt = 100A/μs

- Notes:
5. Device mounted on FR-4 PCB, with minimum recommended pad layout.
 6. Device mounted on 1" x 1" FR-4 PCB with high coverage 2oz. Copper, single sided.
 7. Short duration pulse test used to minimize self-heating effect.
 8. Guaranteed by design. Not subject to product testing.

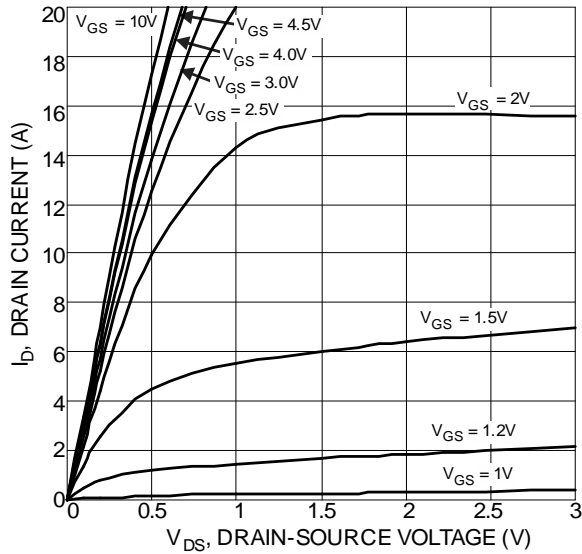


Figure 1 Typical Output Characteristic

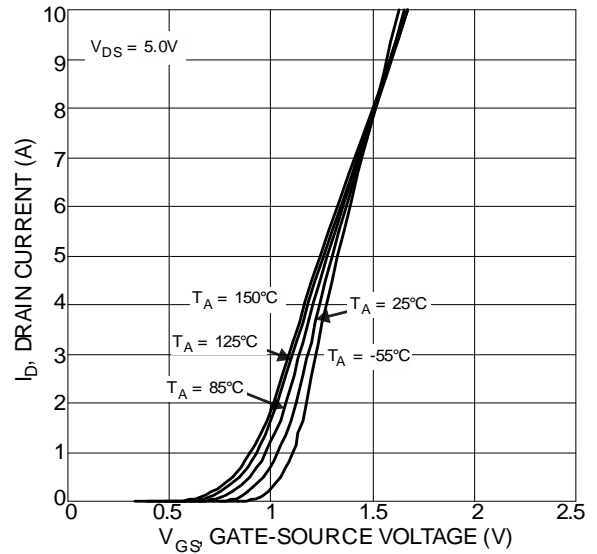


Figure 2 Typical Transfer Characteristics

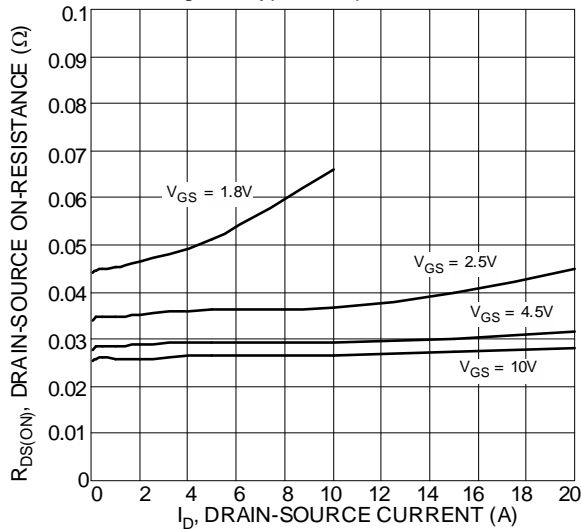


Figure 3 Typical On-Resistance vs. Drain Current and Gate Voltage

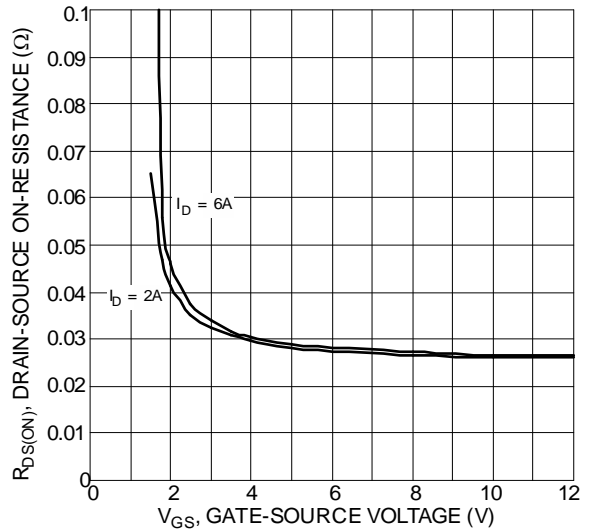


Figure 4 Typical Drain-Source On-Resistance vs. Gate-Source Voltage

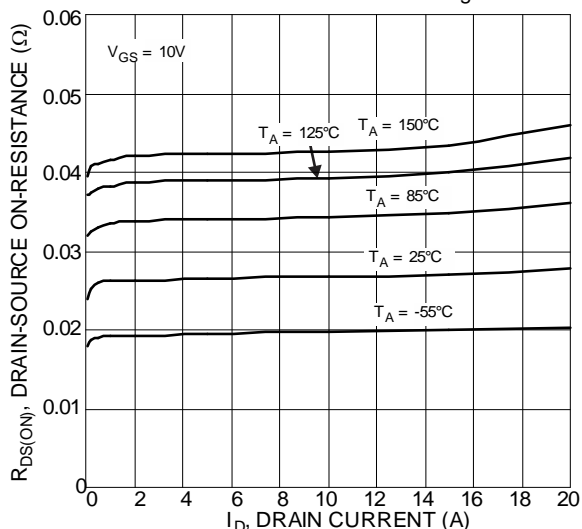


Figure 5 Typical On-Resistance vs. Drain Current and Temperature

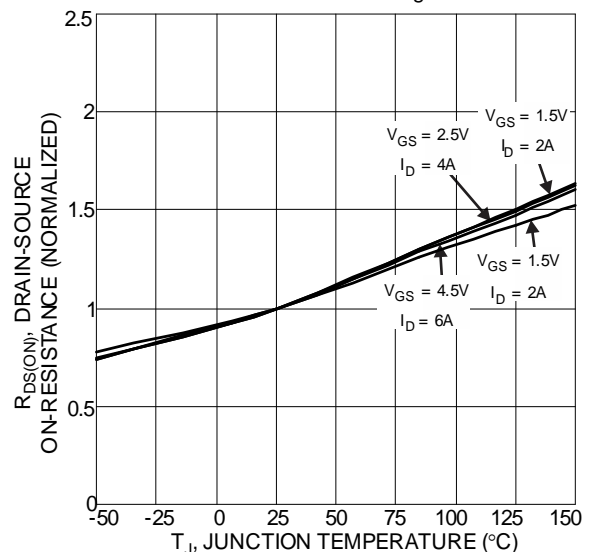


Figure 6 On-Resistance Variation with Temperature

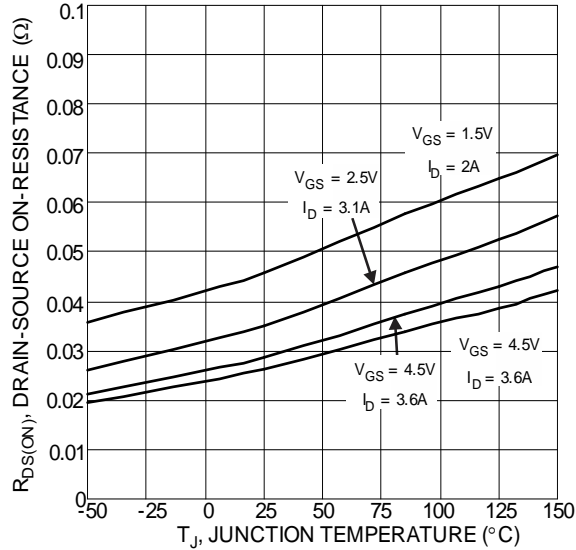


Figure 7 On-Resistance Variation with Temperature

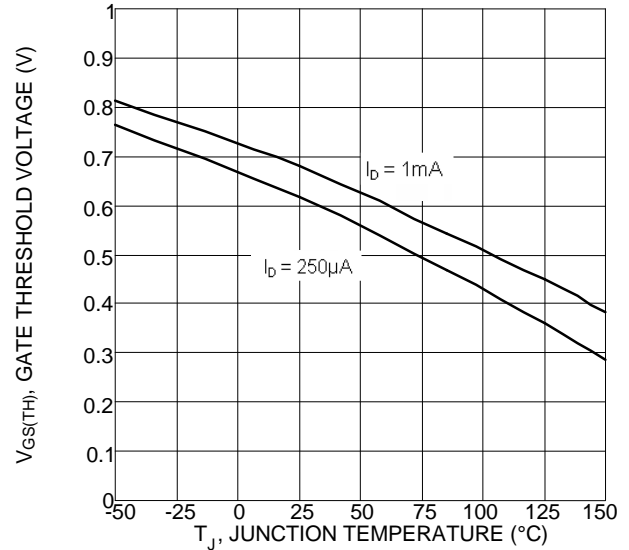


Figure 8 Gate Threshold Variation vs. Junction Temperature

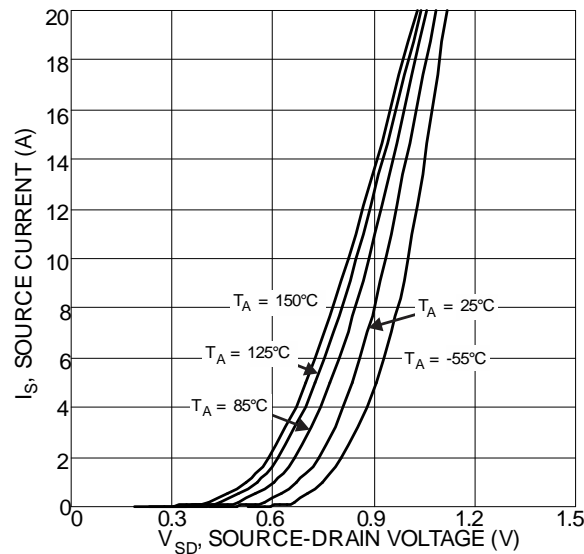


Figure 9 Diode Forward Voltage vs. Current

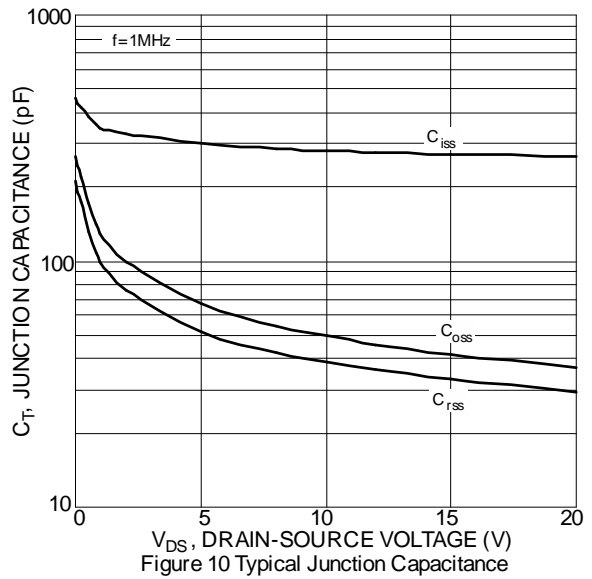


Figure 10 Typical Junction Capacitance

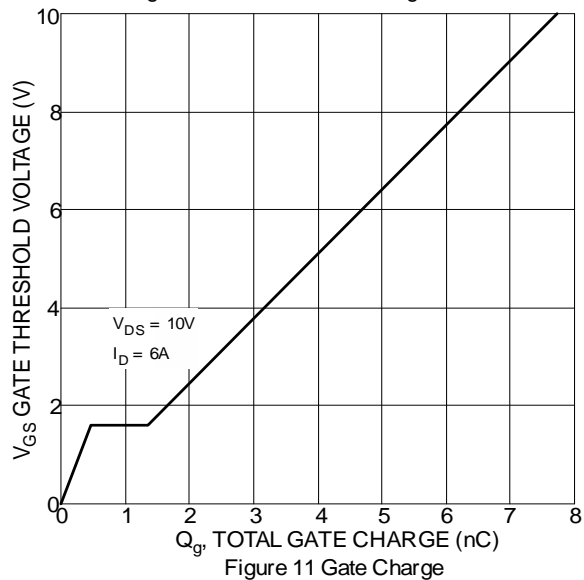


Figure 11 Gate Charge

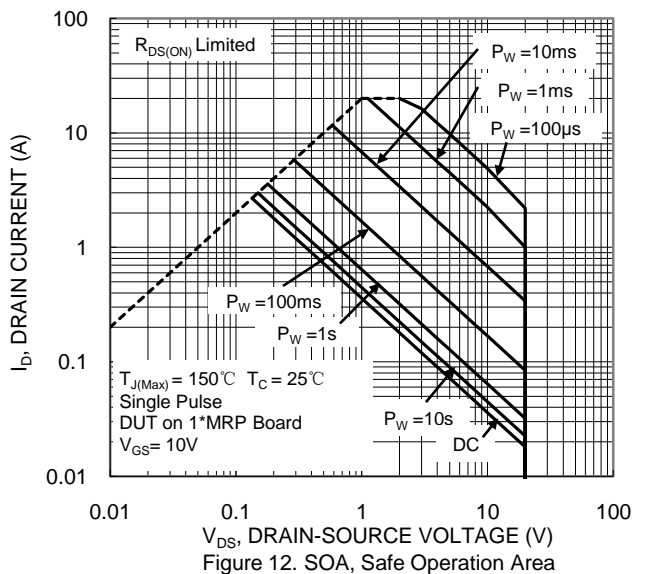


Figure 12. SOA, Safe Operation Area

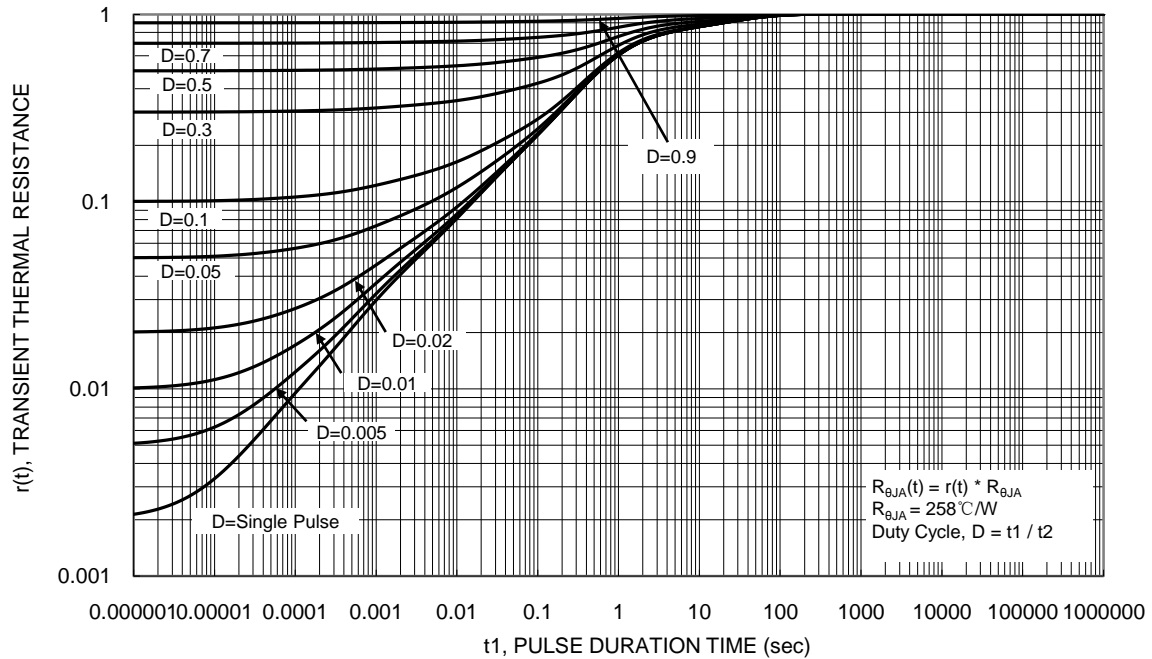
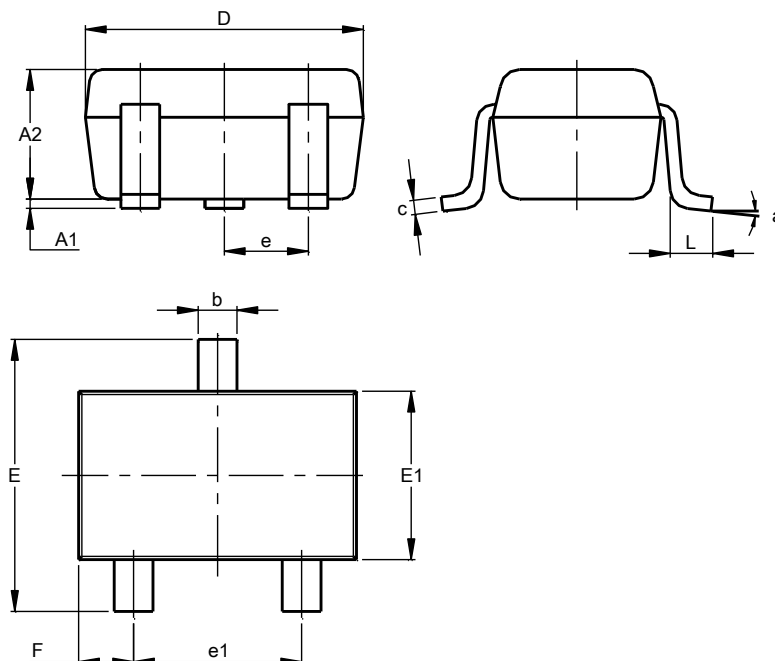


Figure 13. Transient Thermal Resistance

Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

SOT323

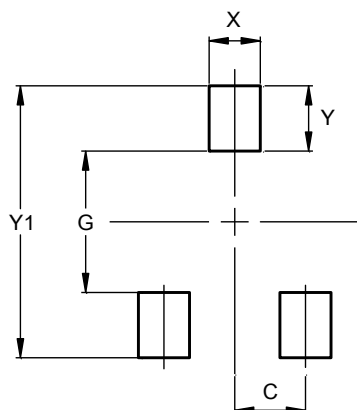


SOT323			
Dim	Min	Max	Typ
A1	0.00	0.10	0.05
A2	0.90	1.00	0.95
b	0.25	0.40	0.30
c	0.10	0.18	0.11
D	1.80	2.20	2.15
E	2.00	2.20	2.10
E1	1.15	1.35	1.30
e	0.650 BSC		
e1	1.20	1.40	1.30
F	0.375	0.475	0.425
L	0.25	0.40	0.30
a	0°	8°	--
All Dimensions in mm			

Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

SOT323



Dimensions	Value (in mm)
C	0.650
G	1.300
X	0.470
Y	0.600
Y1	2.500

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