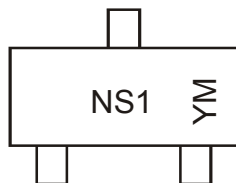


Marking Information



NS1 = Product Type Marking Code
 YM = Date Code Marking
 Y = Year (ex: 1 = 2021)
 M = Month (ex: 9 = September)

Date Code Key

Year	2007	2021	2022	2023	2024	2025	2026	2027	2028	2029	2030
Code	U	I	J	K	L	M	N	O	P	R	S

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

Maximum Ratings (@ T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Drain-Source Voltage	V _{DSS}	20	V
Gate-Source Voltage	V _{GSS}	± 8	V
Drain Current	I _D	1.2 4.0	A

Thermal Characteristics (@ T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Total Power Dissipation	P _d	500	mW
Thermal Resistance, Junction to Ambient	R _{θJA}	250	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics (@ T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 5)						
Drain-Source Breakdown Voltage	BV _{DSS}	20	—	—	V	V _{GS} = 0V, I _D = 250μA
Zero Gate Voltage Drain Current @ T _J = +25°C	I _{DSS}	—	—	10	μA	V _{DS} = 20V, V _{GS} = 0V
Gate-Body Leakage	I _{GSS}	—	—	± 10	μA	V _{GS} = ± 8V, V _{DS} = 0V
ON CHARACTERISTICS (Note 5)						
Gate Threshold Voltage	V _{GS(th)}	0.5	—	1.2	V	V _{DS} = 10V, I _D = 1.0mA
Static Drain-Source On-Resistance	R _{DS(on)}	—	—	0.10 0.14 0.25	Ω	V _{GS} = 4.5V, I _D = 0.5A V _{GS} = 2.5V, I _D = 0.5A V _{GS} = 1.5V, I _D = 0.1A
Diode Forward Voltage	V _{SD}	—	0.8	1.1	V	V _{GS} = 0V, I _S = 1A
DYNAMIC CHARACTERISTICS (Note 6)						
Input Capacitance	C _{iss}	—	220	—	pF	V _{DS} = 10V, V _{GS} = 0V f = 1.0MHz
Output Capacitance	C _{oss}	—	120	—	pF	
Reverse Transfer Capacitance	C _{rss}	—	45	—	pF	
SWITCHING CHARACTERISTICS (Note 6)						
Turn-On Delay Time	t _{D(on)}	—	10	—	ns	V _{DD} = 5V, I _D = 0.5A, V _{GS} = 10V, R _{GEN} = 50Ω
Turn-Off Delay Time	t _{D(off)}	—	75	—	ns	
Turn-On Rise Time	t _R	—	15	—	ns	
Turn-Off Fall Time	t _F	—	65	—	ns	

Notes: 5. Short duration pulse test used to minimize self-heating effect.
 6. Guaranteed by design. Not subject to product testing.

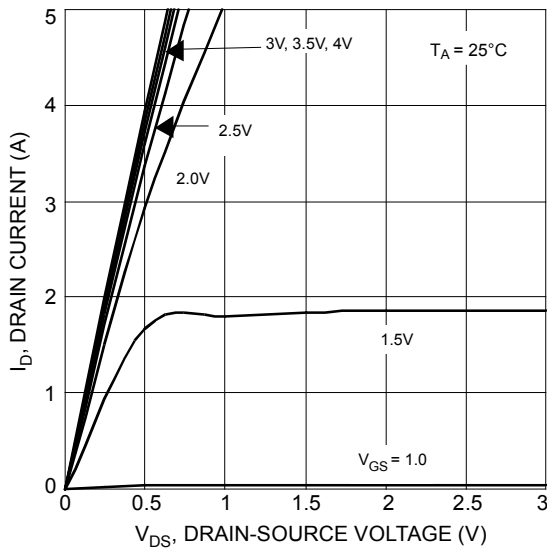


Fig. 1 Typical Output Characteristics

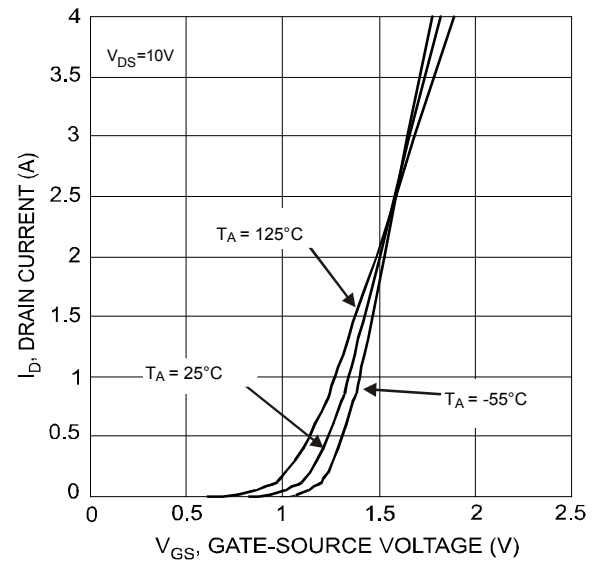


Fig. 2 Typical Transfer Characteristics

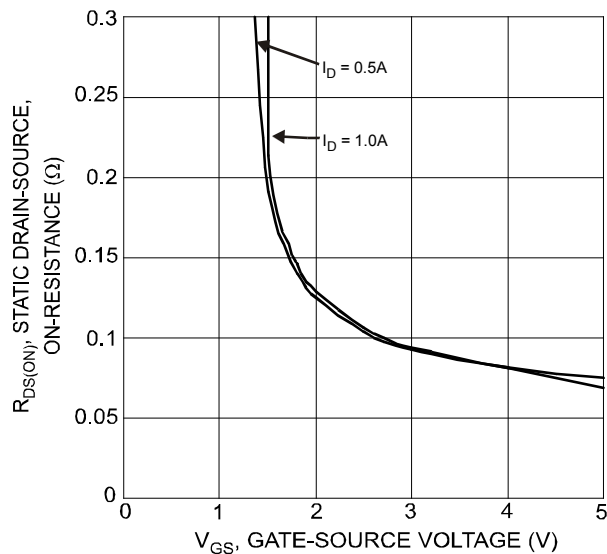


Fig. 3 On-Resistance vs. Gate Voltage

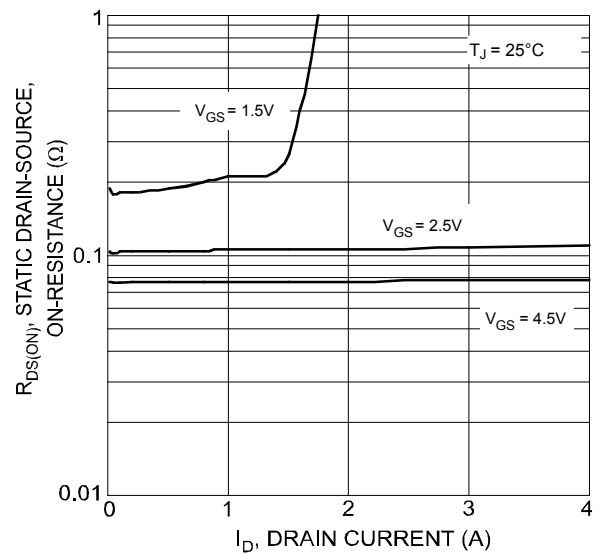


Fig. 4 On-Resistance vs. Drain Current

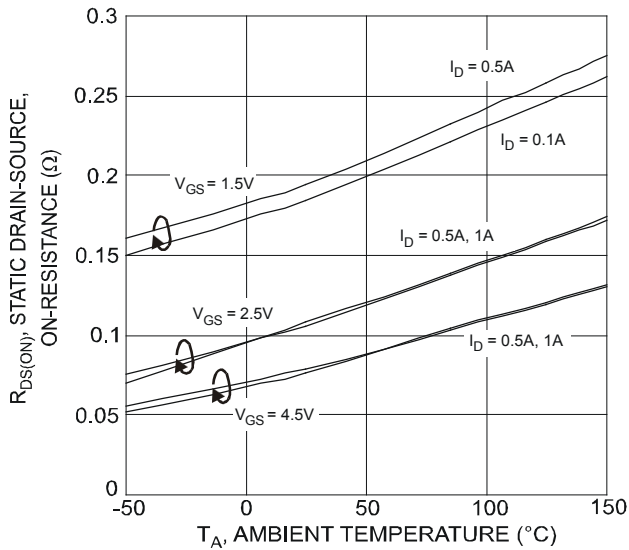


Fig. 5 On-Resistance Variation with Temperature

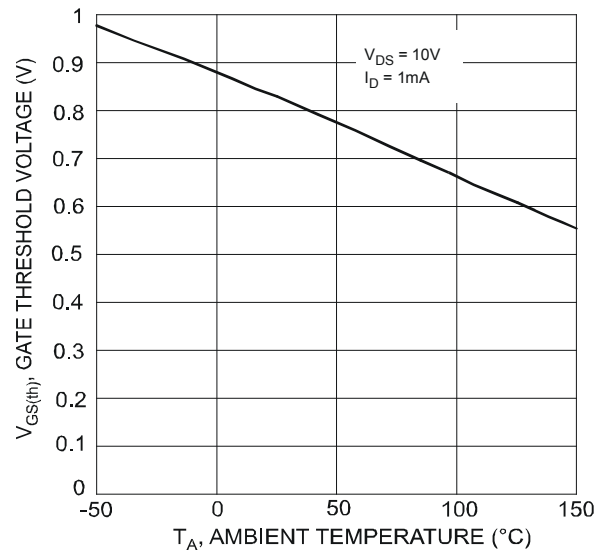


Fig. 6 Gate Threshold Voltage vs. Temperature

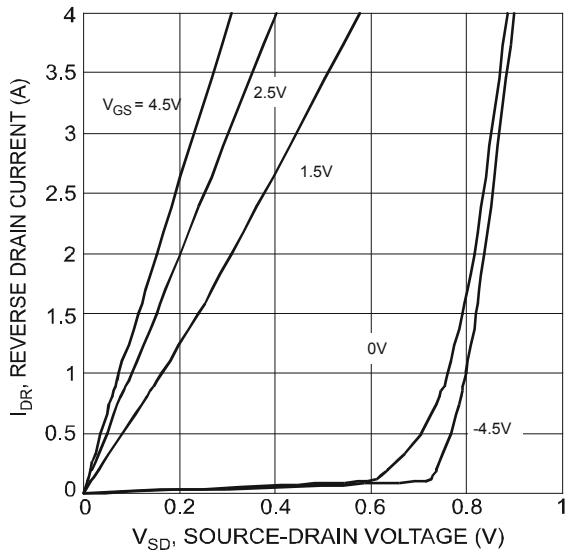


Fig. 7 Reverse Drain Current vs. Source-Drain Voltage

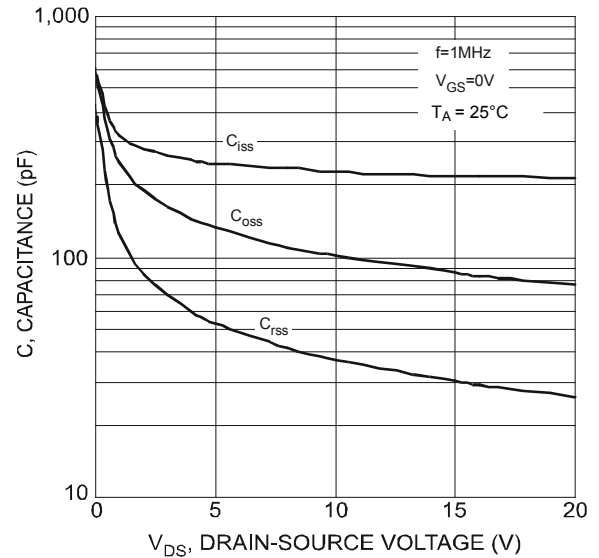
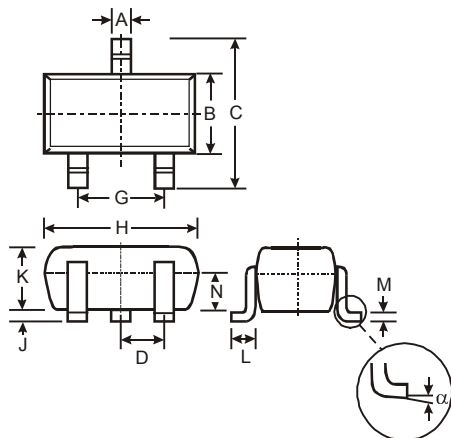


Fig. 8 Typical Junction Capacitance

Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

SC59

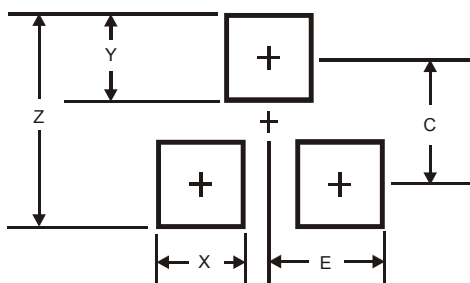


SC59			
Dim	Min	Max	Typ
A	0.35	0.50	0.38
B	1.50	1.70	1.60
C	2.70	3.00	2.80
D	-	-	0.95
G	-	-	1.90
H	2.90	3.10	3.00
J	0.013	0.10	0.05
K	1.00	1.30	1.10
L	0.35	0.55	0.40
M	0.10	0.20	0.15
N	0.70	0.80	0.75
α	0°	8°	-
All Dimensions in mm			

Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

SC59



Dimensions	SC59
Z	3.4
X	0.8
Y	1.0
C	2.4
E	1.35

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