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Symbol	Parameter	Conditions	Min	Тур	Max	Unit
		$V_D = 12 \text{ V}; I_T = 0.1 \text{ A}; T2+ \text{ G-};$ $T_j = 25 \text{ °C}; \frac{\text{Fig. 7}}{}$	-	-	10	mA
		$V_D = 12 \text{ V}; I_T = 0.1 \text{ A}; \text{ T2- G+};$ $T_j = 25 \text{ °C}; \frac{\text{Fig. 7}}{}$	-	-	10	mA
		$V_D = 12 \text{ V}; I_T = 0.1 \text{ A}; \text{ T2- G-};$ $T_j = 25 \text{ °C}; \frac{\text{Fig. 7}}{}$	-	-	25	mA
I _H	holding current	V _D = 12 V; T _j = 25 °C; <u>Fig. 9</u>	-	-	30	mA
V _T	on-state voltage	I _T = 15 A; T _j = 25 °C; <u>Fig. 10</u>	-	1.4	1.65	V
Dynamic chara	acteristics					
dV _D /dt	rate of rise of off-state voltage	V_{DM} = 402 V; T_j = 125 °C; (V_{DM} = 67% of V_{DRM}); exponential waveform; gate open circuit	-	50	-	V/µs

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	T1	main terminal 1	mb	T2—T1
2	T2	main terminal 2)	G sym051
3	G	gate		symoor.
mb	n.c.	mounting base; isolated		
			TO-220AB (SOT78D)	

6. Ordering information

Table 3. Ordering information

Type number	Package					
	Name	Description	Version			
BT138Y-600E	TO-220AB	plastic single-ended package; isolated heatsink mounted; 1 mounting hole; 3-lead TO-220	SOT78D			

7. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V_{DRM}	repetitive peak off-state voltage		-	600	V
I _{T(RMS)}	RMS on-state current	full sine wave; $T_{mb} \le 85$ °C; Fig. 1; Fig. 2; Fig. 3	-	12	А
I _{TSM}	non-repetitive peak on- state current	full sine wave; $T_{j(init)} = 25 \text{ °C}$; $t_p = 20 \text{ ms}$; Fig. 4; Fig. 5	-	95	А
		full sine wave; $T_{j(init)} = 25 ^{\circ}C$; $t_p = 16.7 \text{ms}$	-	105	Α
l ² t	I ² t for fusing	t _p = 10 ms; SIN	-	45	A²s
dI _T /dt	rate of rise of on-state	I _G = 100 mA; T2+ G+	-	50	A/µs
	current	I _G = 100 mA; T2+ G-	-	50	A/µs
		I _G = 100 mA; T2- G-	-	50	A/µs
		I _G = 100 mA; T2- G+	-	10	A/µs
I _{GM}	peak gate current		-	2	Α
P_{GM}	peak gate power		-	5	W
$P_{G(AV)}$	average gate power	over any 20 ms period	-	0.5	W
T _{stg}	storage temperature		-40	150	°C
Tj	junction temperature		-	125	°C

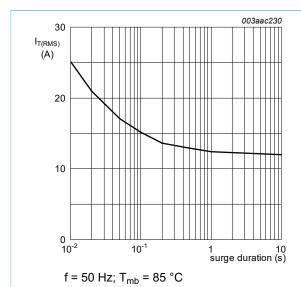


Fig. 1. RMS on-state current as a function of surge duration; maximum values

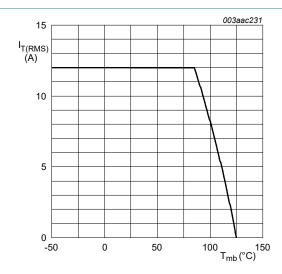


Fig. 2. RMS on-state current as a function of mounting base temperature; maximum values

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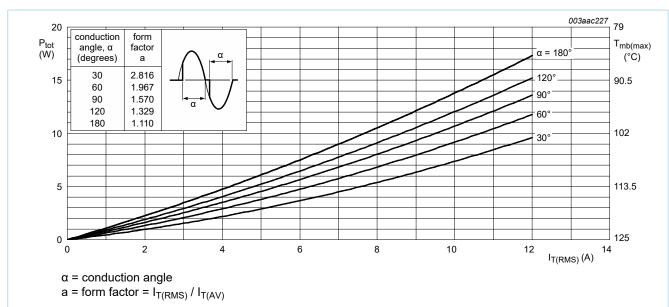


Fig. 3. Total power dissipation as a function of RMS on-state current; maximum values

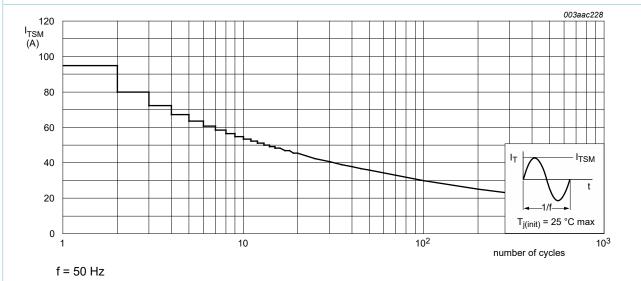
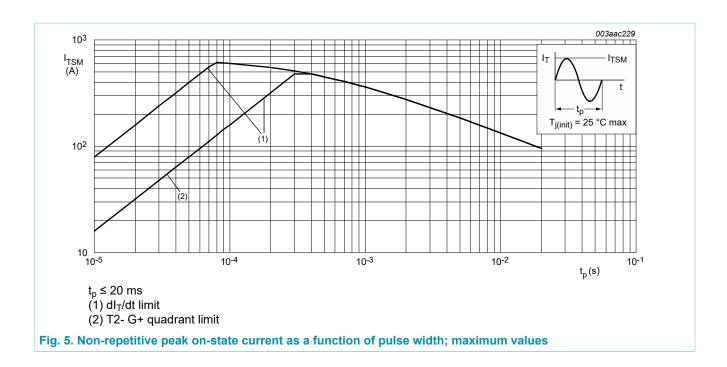


Fig. 4. Non-repetitive peak on-state current as a function of the number of sinusoidal current cycles; maximum values

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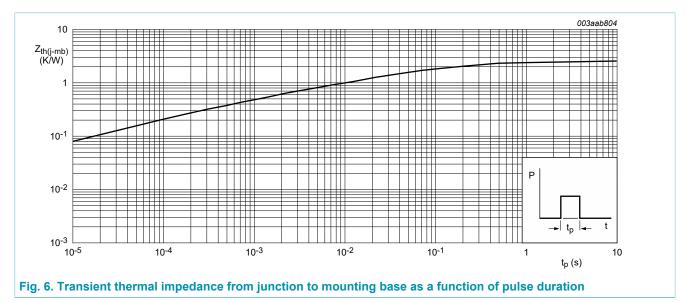
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8. Thermal characteristics

Table 5. Thermal characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
R _{th(j-mb)}	thermal resistance from junction to mounting base	full cycle; Fig. 6	-	-	2.3	K/W
$R_{th(j-a)}$	thermal resistance from junction to ambient free air	full cycle; in free air	-	60	-	K/W



9. Isolation characteristics

Table 6. Isolation characteristics

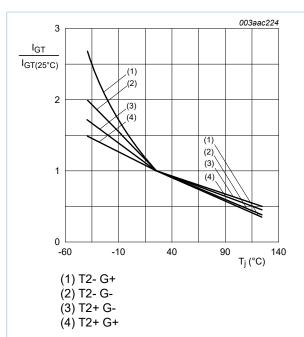
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{isol(RMS)}	RMS isolation voltage	from all terminals to external heatsink; sinusoidal waveform; clean and dust free; $50 \text{ Hz} \le f \le 60 \text{ Hz}$; RH $\le 65 \%$; $T_{mb} = 25 \text{ °C}$	-	-	2500	V
C _{isol}	isolation capacitance	from main terminal 2 to external heatsink; f = 1 MHz; T _{mb} = 25 °C	-	10	-	pF

10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static char	acteristics					
I _{GT}	gate trigger current	$V_D = 12 \text{ V}; I_T = 0.1 \text{ A}; T2+ G+;$ $T_j = 25 \text{ °C}; \frac{\text{Fig. 7}}{}$	-	-	10	mA
		$V_D = 12 \text{ V}; I_T = 0.1 \text{ A}; T2+ \text{ G-};$ $T_j = 25 \text{ °C}; \frac{\text{Fig. 7}}{}$	-	-	10	mA
		$V_D = 12 \text{ V}; I_T = 0.1 \text{ A}; \text{ T2- G+};$ $T_j = 25 \text{ °C}; \frac{\text{Fig. 7}}{}$	-	-	10	mA
		$V_D = 12 \text{ V}; I_T = 0.1 \text{ A}; \text{ T2- G-};$ $T_j = 25 \text{ °C}; \frac{\text{Fig. 7}}{}$	-	-	25	mA
IL	latching current	$V_D = 12 \text{ V}; I_G = 0.1 \text{ A}; T2+ G+;$ $T_j = 25 \text{ °C}; \frac{\text{Fig. 8}}{}$	-	-	30	mA
		$V_D = 12 \text{ V}; I_G = 0.1 \text{ A}; T2+ G-;$ $T_j = 25 \text{ °C}; \frac{\text{Fig. 8}}{}$	-	-	40	mA
		$V_D = 12 \text{ V}; I_G = 0.1 \text{ A}; \text{ T2- G-};$ $T_j = 25 \text{ °C}; \frac{\text{Fig. 8}}{}$	-	-	30	mA
		$V_D = 12 \text{ V}; I_G = 0.1 \text{ A}; \text{ T2- G+};$ $T_j = 25 \text{ °C}; \frac{\text{Fig. 8}}{}$	-	-	40	mA
I _H	holding current	V _D = 12 V; T _j = 25 °C; <u>Fig. 9</u>	-	-	30	mA
V _T	on-state voltage	I _T = 15 A; T _j = 25 °C; <u>Fig. 10</u>	-	1.4	1.65	V
V_{GT}	gate trigger voltage	$V_D = 12 \text{ V}; I_T = 0.1 \text{ A}; T_j = 25 \text{ °C};$ Fig. 11	-	0.7	1	V
		$V_D = 600 \text{ V}; I_T = 0.1 \text{ A}; T_j = 125 \text{ °C};$ Fig. 11	0.25	0.4	-	V
I _D	off-state current	V _D = 600 V; T _j = 125 °C	-	0.1	0.5	mA
Dynamic cl	naracteristics		'			
dV _D /dt	rate of rise of off-state voltage	V_{DM} = 402 V; T_j = 125 °C; (V_{DM} = 67% of V_{DRM}); exponential waveform; gate open circuit	-	50	-	V/µs
t _{gt}	gate-controlled turn-on time	I_{TM} = 16 A; V_D = 600 V; I_G = 100 mA; dI_G/dt = 5 A/ μ s	-	2	-	μs

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003aac225 3 I_{L} T_{L(25°C)} 2 1 0 -60 -10 40 140 T_i (°C)

Fig. 8. Normalized latching current as a function of junction temperature

Fig. 7. Normalized gate trigger current as a function of junction temperature

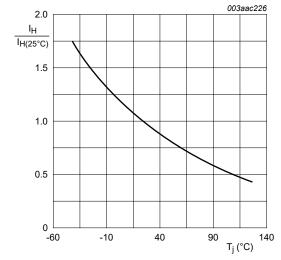
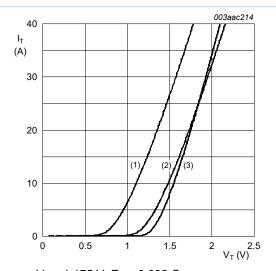


Fig. 9. Normalized holding current as a function of junction temperature



 V_o = 1.175 V; R_s = 0.032 Ω (1) T_j = 125 °C; typical values (2) T_j = 125 °C; maximum values (3) T_j = 25 °C; maximum values

Fig. 10. On-state current as a function of on-state voltage

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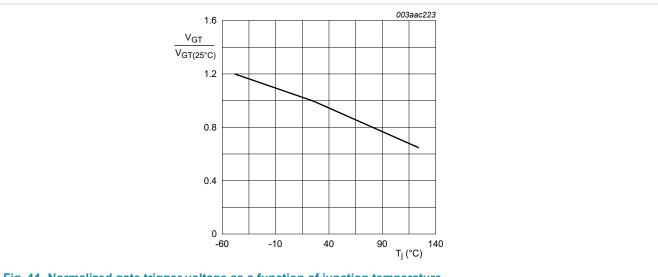


Fig. 11. Normalized gate trigger voltage as a function of junction temperature

11. Package outline

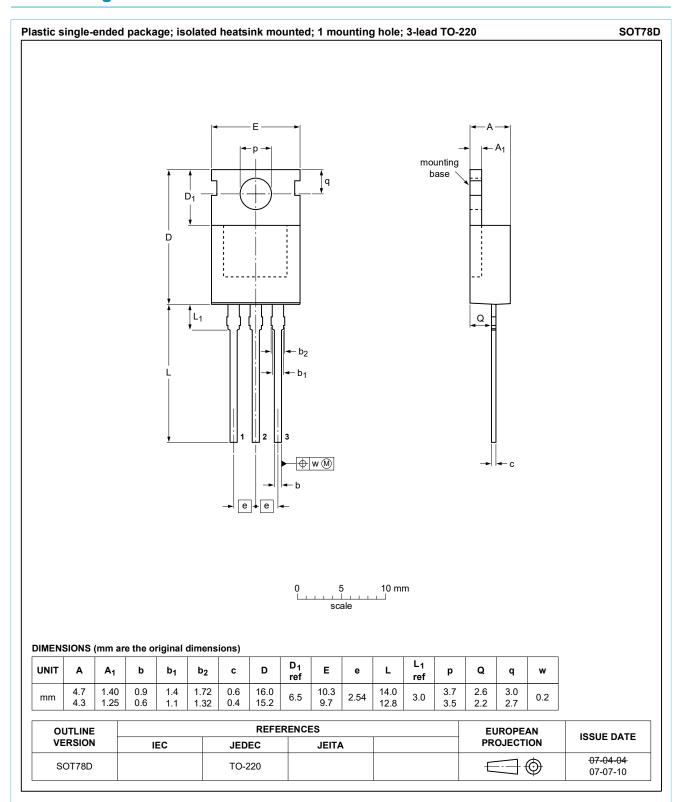


Fig. 12. Package outline TO-220AB (SOT78D)

12. Legal information

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Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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