**4Q Triac** 

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
		$V_D = 12 \text{ V; } I_T = 0.1 \text{ A; T2- G+;}$ $T_j = 25 \text{ °C; } \frac{\text{Fig. 7}}{}$	-	11	25	mA
I <sub>H</sub>	holding current	V <sub>D</sub> = 12 V; T <sub>j</sub> = 25 °C; <u>Fig. 9</u>	-	4	30	mA
V <sub>T</sub>	on-state voltage	I <sub>T</sub> = 15 A; T <sub>j</sub> = 25 °C; <u>Fig. 10</u>	-	1.4	1.65	V
Dynamic cha	racteristics					
dV <sub>D</sub> /dt	rate of rise of off-state voltage	$V_{DM}$ = 402 V; $T_j$ = 125 °C; $(V_{DM}$ = 67% of $V_{DRM}$ ); exponential waveform; gate open circuit	-	50	-	V/µs

# 5. Pinning information

## **Table 2. Pinning information**

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	T1	main terminal 1	mb	T2—T1
2	T2	main terminal 2		G sym051
3	G	gate		Symost
mb	T2	mounting base; main terminal 2		
			D2PAK (SOT404)	

# 6. Ordering information

## **Table 3. Ordering information**

Type number	Package					
	Name	Description	Version			
BT138B-600E	D2PAK	plastic single-ended surface-mounted package (D2PAK); 3 leads (one lead cropped)	SOT404			

# 7. Limiting values

### **Table 4. Limiting values**

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
$V_{DRM}$	repetitive peak off-state voltage		-	600	V
I <sub>T(RMS)</sub>	RMS on-state current	full sine wave; T <sub>mb</sub> ≤ 99 °C; <u>Fig. 1</u> ; <u>Fig. 2</u> ; <u>Fig. 3</u>	-	12	Α
I <sub>TSM</sub>	non-repetitive peak on- state current	full sine wave; $T_{j(init)}$ = 25 °C; $t_p$ = 20 ms; Fig. 4; Fig. 5	-	95	Α
		full sine wave; T <sub>j(init)</sub> = 25 °C; t <sub>p</sub> = 16.7 ms	-	105	Α
l <sup>2</sup> t	I <sup>2</sup> t for fusing	t <sub>p</sub> = 10 ms; sine-wave pulse	-	45	A²s
dl <sub>T</sub> /dt	rate of rise of on-state current	I <sub>G</sub> = 50 mA	-	50	A/µs
I <sub>GM</sub>	peak gate current		-	2	Α
$P_{GM}$	peak gate power		-	5	W
P <sub>G(AV)</sub>	average gate power	over any 20 ms period	-	0.5	W
T <sub>stg</sub>	storage temperature		-40	150	°C
Tj	junction temperature		-	125	°C

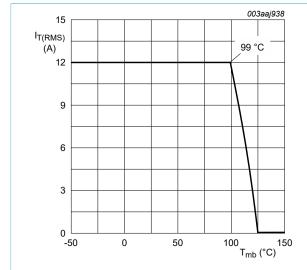


Fig. 1. RMS on-state current as a function of mounting base temperature; maximum values

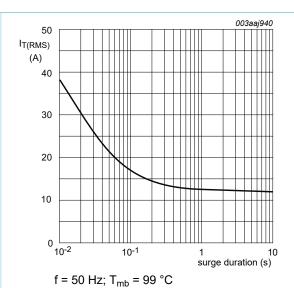


Fig. 2. RMS on-state current as a function of surge duration; maximum values

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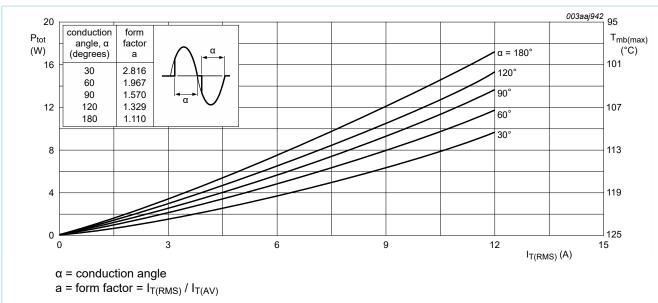


Fig. 3. Total power dissipation as a function of RMS on-state current; maximum values

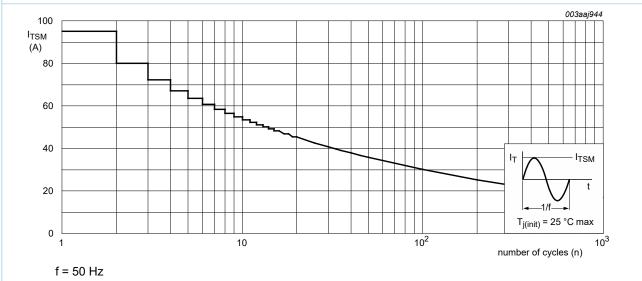
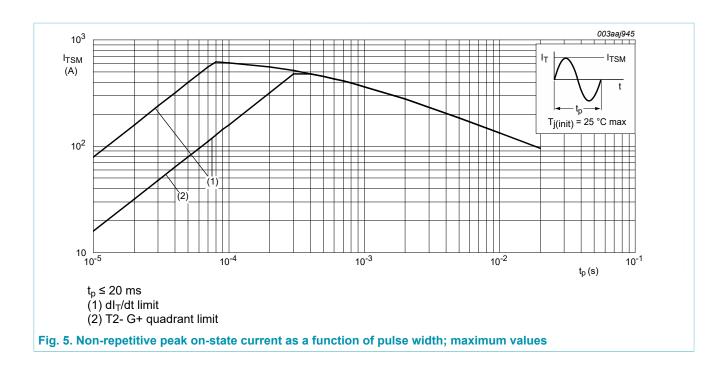


Fig. 4. Non-repetitive peak on-state current as a function of the number of sinusoidal current cycles; maximum values

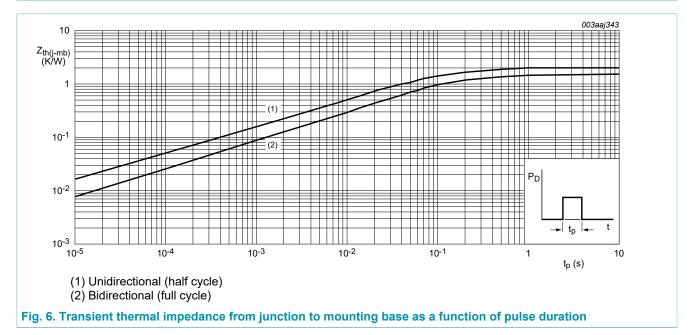
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## 8. Thermal characteristics

**Table 5. Thermal characteristics** 

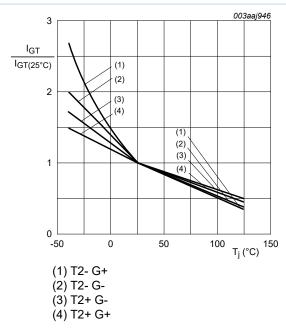
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
R <sub>th(j-mb)</sub>	thermal resistance	full cycle; Fig. 6	-	-	1.5	K/W
	from junction to mounting base	half cycle; Fig. 6	-	-	2	K/W
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient free air	minimum footprint: FR4 board	-	55	-	K/W



## 9. Characteristics

### **Table 6. Characteristics**

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static char	acteristics		'			_
I <sub>GT</sub>	gate trigger current	$V_D = 12 \text{ V; } I_T = 0.1 \text{ A; } T2 + G+;$ $T_j = 25 \text{ °C; } Fig. 7$	-	2.5	10	mA
		$V_D = 12 \text{ V; } I_T = 0.1 \text{ A; } T2 + G-;$ $T_j = 25 \text{ °C; } Fig. 7$	-	4	10	mA
		$V_D = 12 \text{ V; } I_T = 0.1 \text{ A; } T2\text{- }G\text{-}; $ $T_j = 25 \text{ °C; } Fig. 7$	-	5	10	mA
		$V_D = 12 \text{ V}; I_T = 0.1 \text{ A}; \text{ T2- G+};$ $T_j = 25 \text{ °C}; \underline{\text{Fig. 7}}$	-	11	25	mA
l <sub>L</sub>	latching current	$V_D = 12 \text{ V}; I_G = 0.1 \text{ A}; T2+ G+;$ $T_j = 25 \text{ °C}; Fig. 8$	-	3.2	30	mA
		$V_D = 12 \text{ V}; I_G = 0.1 \text{ A}; \text{ T2+ G-};$ $T_j = 25 \text{ °C}; \underline{\text{Fig. 8}}$	-	16	40	mA
		$V_D = 12 \text{ V}; I_G = 0.1 \text{ A}; \text{ T2- G-};$ $T_j = 25 \text{ °C}; \frac{\text{Fig. 8}}{2}$	-	4	30	mA
		$V_D = 12 \text{ V}; I_G = 0.1 \text{ A}; \text{ T2- G+};$ $T_j = 25 \text{ °C}; \underline{\text{Fig. 8}}$	-	5.5	40	mA
I <sub>H</sub>	holding current	$V_D = 12 \text{ V}; T_j = 25 ^{\circ}\text{C}; Fig. 9$	-	4	30	mA
V <sub>T</sub>	on-state voltage	I <sub>T</sub> = 15 A; T <sub>j</sub> = 25 °C; <u>Fig. 10</u>	-	1.4	1.65	V
$V_{GT}$	gate trigger voltage	$V_D = 12 \text{ V}; I_T = 0.1 \text{ A}; T_j = 25 \text{ °C};$ Fig. 11	-	0.7	1	V
		$V_D = 400 \text{ V}; I_T = 0.1 \text{ A}; T_j = 125 \text{ °C};$ Fig. 11	0.25	0.4	-	V
I <sub>D</sub>	off-state current	V <sub>D</sub> = 600 V; T <sub>j</sub> = 125 °C	-	0.1	0.5	mA
Dynamic cl	haracteristics		'			
dV <sub>D</sub> /dt	rate of rise of off-state voltage	$V_{DM}$ = 402 V; $T_j$ = 125 °C; ( $V_{DM}$ = 67% of $V_{DRM}$ ); exponential waveform; gate open circuit	-	50	-	V/µs



003aaj947 3 I<sub>L(25°C)</sub> 2 1 -50 0 50 100 <sub>Ti</sub> (°C) 150

Fig. 8. Normalized latching current as a function of junction temperature

Fig. 7. Normalized gate trigger current as a function of junction temperature

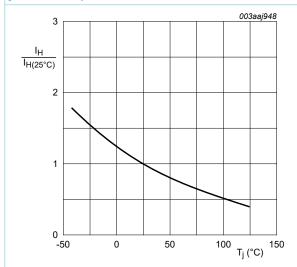
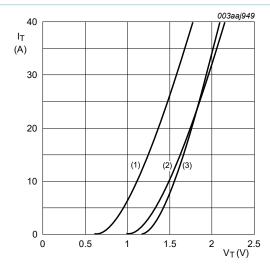


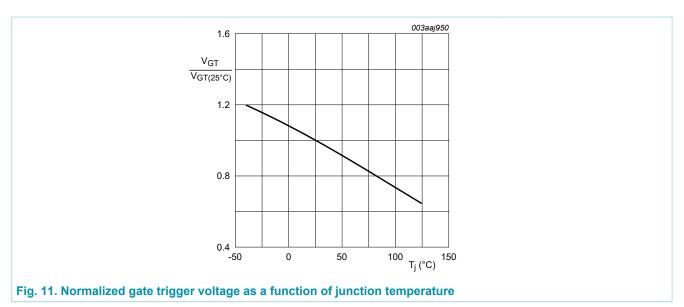
Fig. 9. Normalized holding current as a function of junction temperature



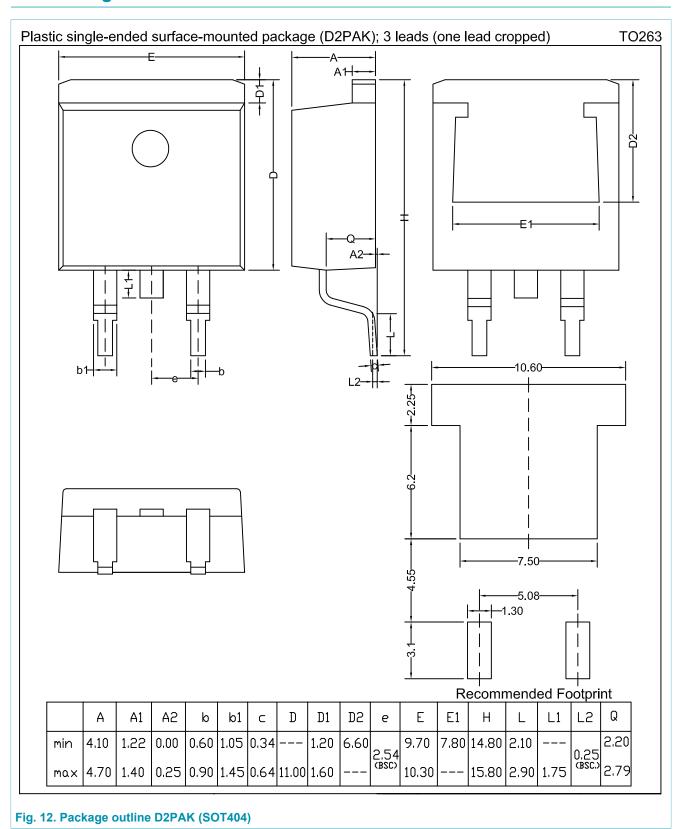
 $V_o$  = 1.145 V;  $R_s$  = 0.031 Ω (1)  $T_j$  = 125 °C; typical values (2)  $T_j$  = 125 °C; maximum values (3)  $T_j$  = 25 °C; maximum values

Fig. 10. On-state current as a function of on-state voltage

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# 10. Package outline



## 11. Legal information

#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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