

DYNAMIC CHARACTERISTICS

APT35GP120B2DQ2(G)

Symbol	Characteristic	Test Conditions	MIN	TYP	MAX	UNIT
C_{ies}	Input Capacitance	Capacitance $V_{GE} = 0V, V_{CE} = 25V$ $f = 1 \text{ MHz}$		3240		pF
C_{oes}	Output Capacitance			250		
C_{res}	Reverse Transfer Capacitance			31		
V_{GEP}	Gate-to-Emitter Plateau Voltage	Gate Charge		7.5		V
Q_g	Total Gate Charge ^③	$V_{GE} = 15V$		150		nC
Q_{ge}	Gate-Emitter Charge	$V_{CE} = 600V$		21		
Q_{gc}	Gate-Collector ("Miller") Charge	$I_C = 35A$		60		
RBSOA	Reverse Bias Safe Operating Area	$T_J = 150^\circ C, R_G = 4.3\Omega, V_{GE} = 15V, L = 100\mu H, V_{CE} = 960V$	140			A
$t_{d(on)}$	Turn-on Delay Time	Inductive Switching (25°C) $V_{CC} = 600V$ $V_{GE} = 15V$ $I_C = 35A$ $R_G = 4.3\Omega$ $T_J = +25^\circ C$		16		ns
t_r	Current Rise Time			20		
$t_{d(off)}$	Turn-off Delay Time			95		
t_f	Current Fall Time			40		μJ
E_{on1}	Turn-on Switching Energy ^④			750		
E_{on2}	Turn-on Switching Energy (Diode) ^⑤		1305			
E_{off}	Turn-off Switching Energy ^⑥		680			
$t_{d(on)}$	Turn-on Delay Time	Inductive Switching (125°C) $V_{CC} = 600V$ $V_{GE} = 15V$ $I_C = 35A$ $R_G = 4.3\Omega$ $T_J = +125^\circ C$		16		ns
t_r	Current Rise Time			20		
$t_{d(off)}$	Turn-off Delay Time			145		
t_f	Current Fall Time			75		μJ
E_{on1}	Turn-on Switching Energy ^④			750		
E_{on2}	Turn-on Switching Energy (Diode) ^⑤		2130			
E_{off}	Turn-off Switching Energy ^⑥		1745			

THERMAL AND MECHANICAL CHARACTERISTICS

Symbol	Characteristic	MIN	TYP	MAX	UNIT
$R_{\theta JC}$	Junction to Case (IGBT)			.23	°C/W
$R_{\theta JC}$	Junction to Case (DIODE)			.61	
W_T	Package Weight		5.9		gm

- ① Repetitive Rating: Pulse width limited by maximum junction temperature.
- ② For Combi devices, I_{ces} includes both IGBT and FRED leakages
- ③ See MIL-STD-750 Method 3471.
- ④ E_{on1} is the clamped inductive turn-on energy of the IGBT only, without the effect of a commutating diode reverse recovery current adding to the IGBT turn-on loss. Tested in inductive switching test circuit shown in figure 21, but with a Silicon Carbide diode.
- ⑤ E_{on2} is the clamped inductive turn-on energy that includes a commutating diode reverse recovery current in the IGBT turn-on switching loss. (See Figures 21, 22.)
- ⑥ E_{off} is the clamped inductive turn-off energy measured in accordance with JEDEC standard JESD24-1. (See Figures 21, 23.)

APT Reserves the right to change, without notice, the specifications and information contained h

TYPICAL PERFORMANCE CURVES

APT35GP120B2DQ2(G)

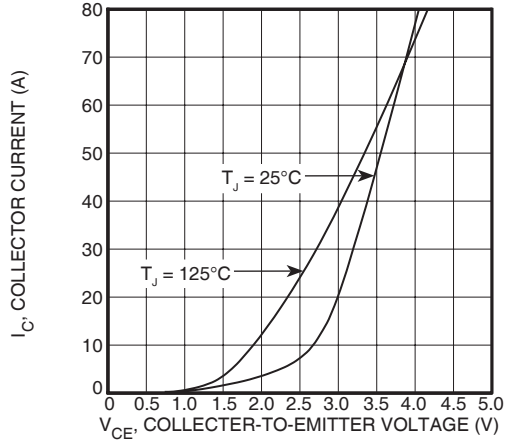


FIGURE 1, Output Characteristics ($T_J = 25^\circ\text{C}$)

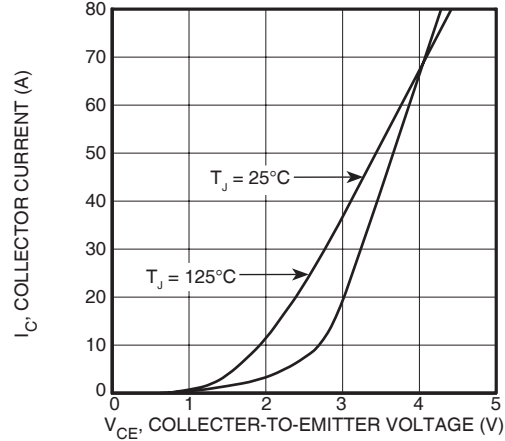


FIGURE 2, Output Characteristics ($T_J = 125^\circ\text{C}$)

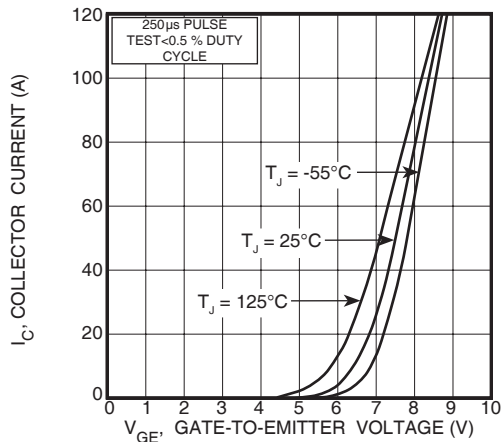


FIGURE 3, Transfer Characteristics

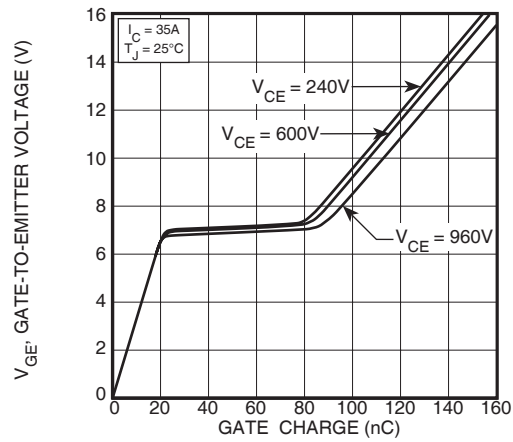


FIGURE 4, Gate Charge

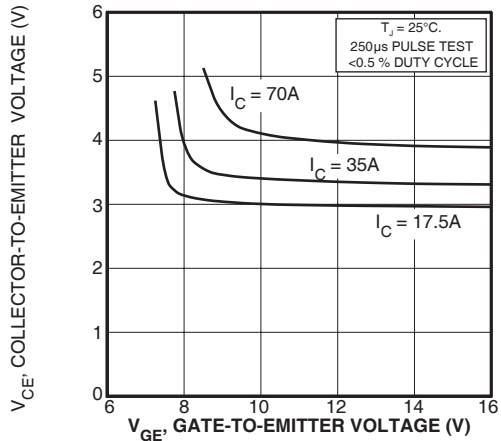


FIGURE 5, On State Voltage vs Gate-to-Emitter Voltage

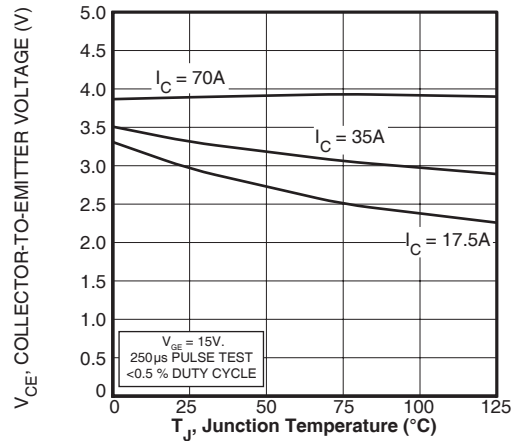


FIGURE 6, On State Voltage vs Junction Temperature

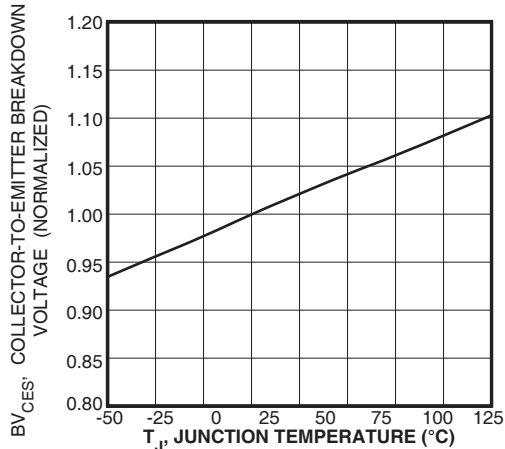


FIGURE 7, Breakdown Voltage vs. Junction Temperature

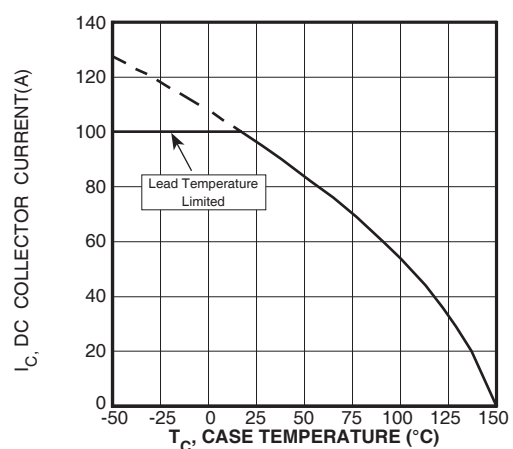


FIGURE 8, DC Collector Current vs Case Temperature

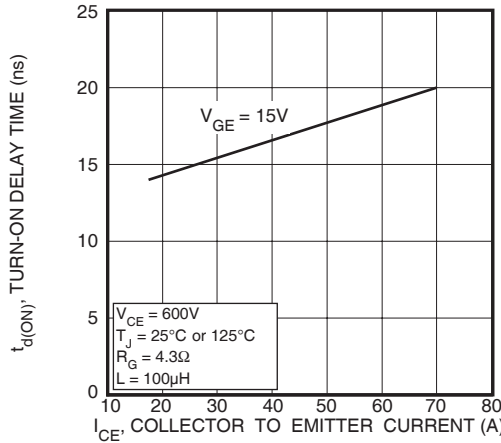


FIGURE 9, Turn-On Delay Time vs Collector Current

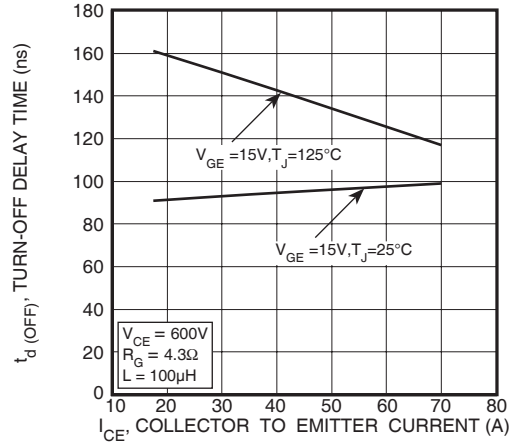


FIGURE 10, Turn-Off Delay Time vs Collector Current

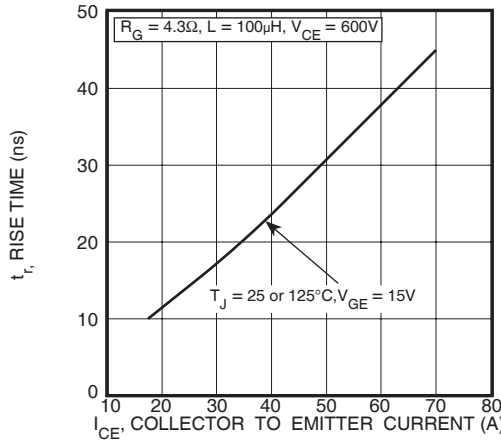


FIGURE 11, Current Rise Time vs Collector Current

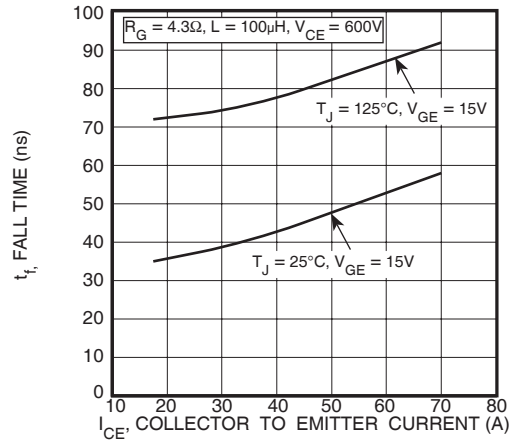


FIGURE 12, Current Fall Time vs Collector Current

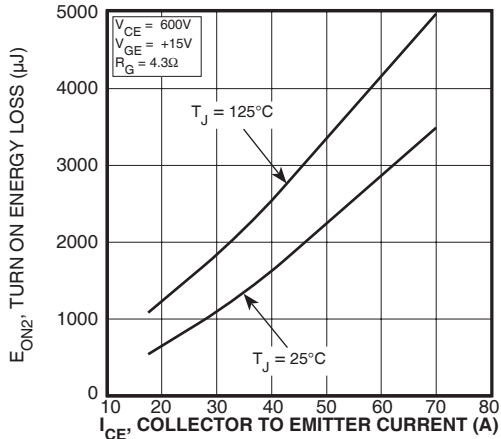


FIGURE 13, Turn-On Energy Loss vs Collector Current

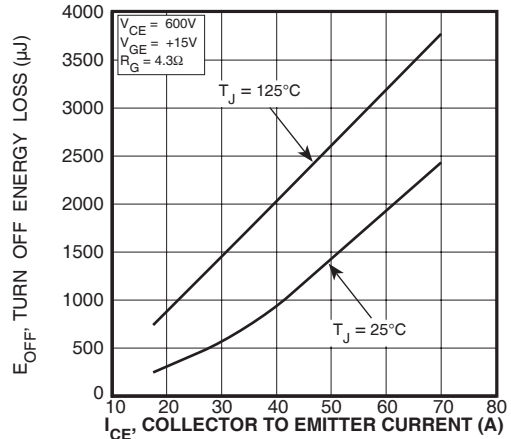


FIGURE 14, Turn Off Energy Loss vs Collector Current

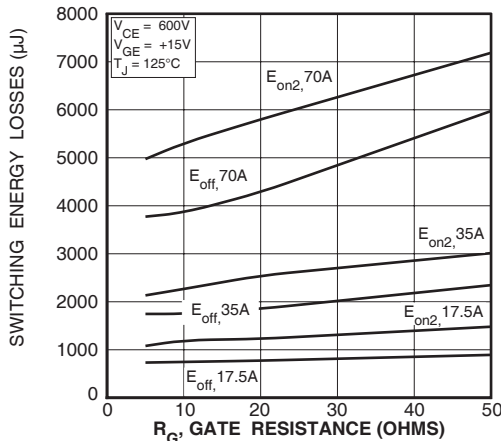


FIGURE 15, Switching Energy Losses vs. Gate Resistance

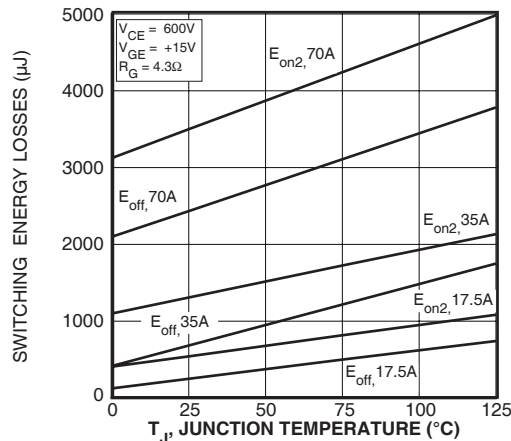


FIGURE 16, Switching Energy Losses vs Junction Temperature

TYPICAL PERFORMANCE CURVES

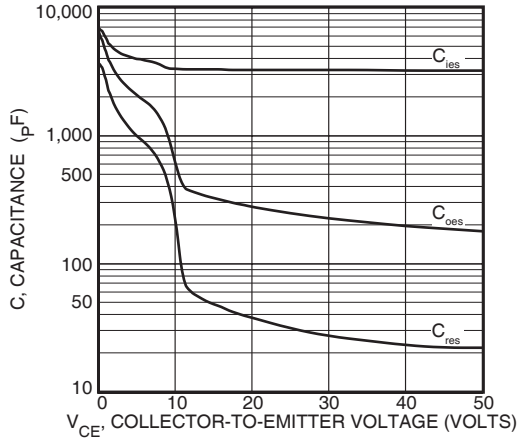


Figure 17, Capacitance vs Collector-To-Emitter Voltage

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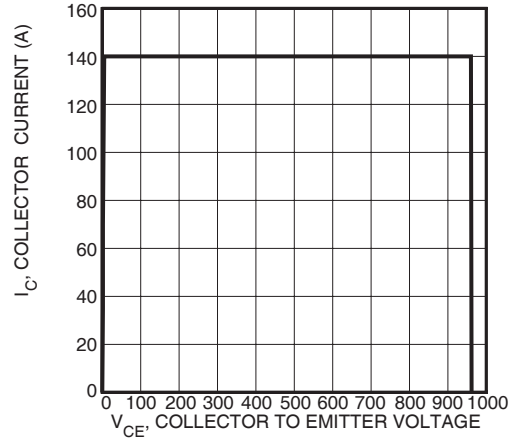


Figure 18, Minimum Switching Safe Operating Area

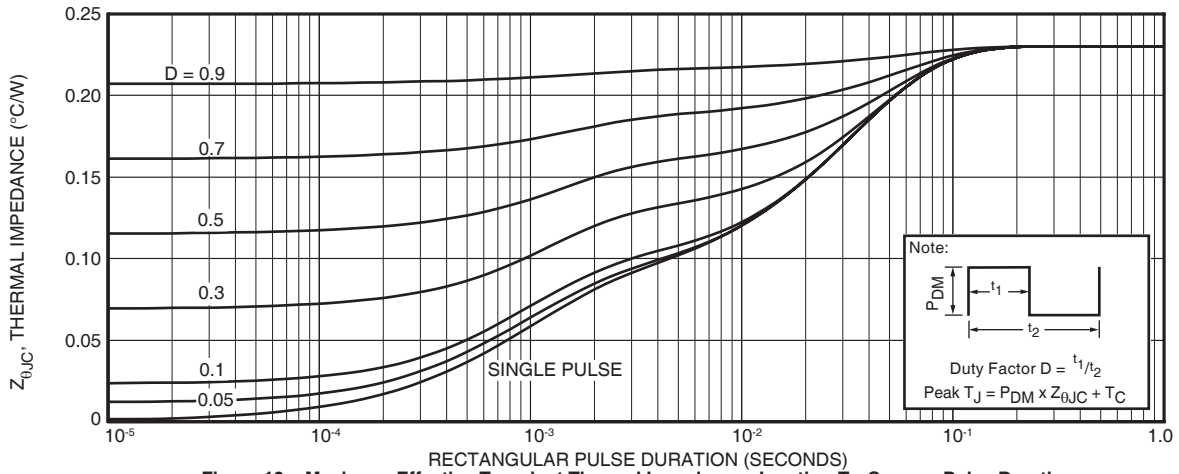


Figure 19a, Maximum Effective Transient Thermal Impedance, Junction-To-Case vs Pulse Duration

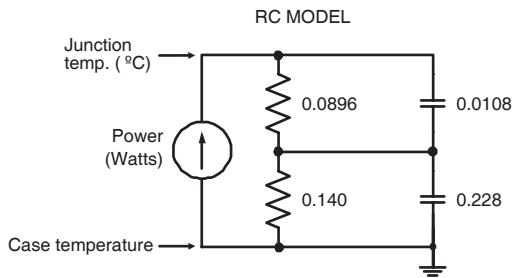


Figure 19b, Transient Thermal Impedance Model

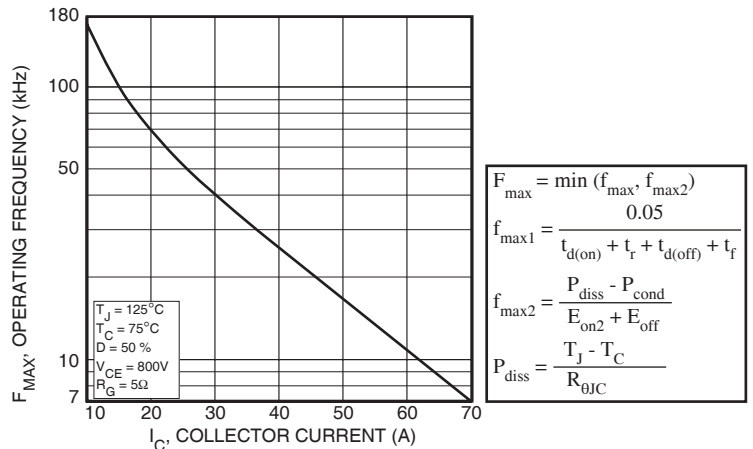


Figure 20, Operating Frequency vs Collector Current

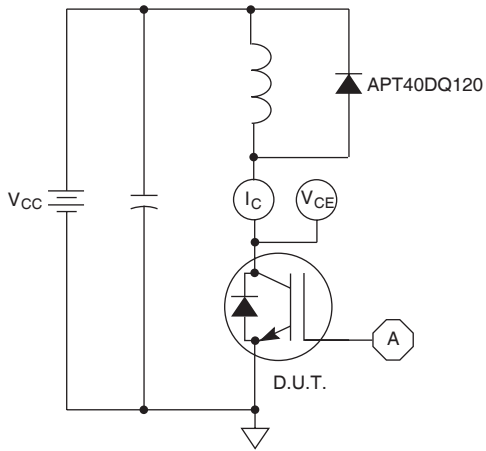


Figure 21, Inductive Switching Test Circuit

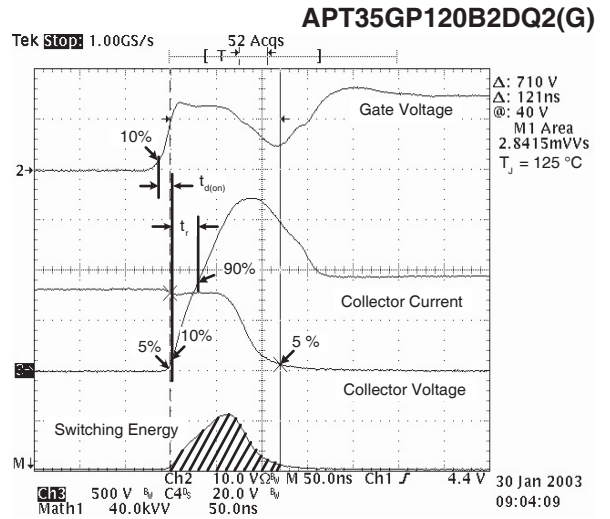


Figure 22, Turn-on Switching Waveforms and Definitions

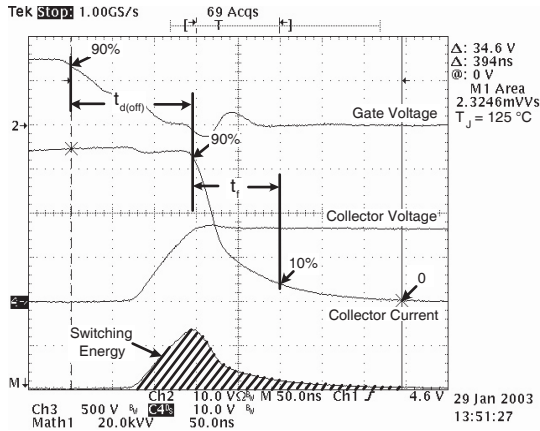


Figure 23, Turn-off Switching Waveforms and Definitions

ULTRAFAST SOFT RECOVERY ANTI-PARALLEL DIODE

MAXIMUM RATINGS

All Ratings: $T_C = 25^\circ\text{C}$ unless otherwise specified.

Symbol	Characteristic / Test Conditions	APT35GP120B2DQ2(G)		UNIT
$I_F(\text{AV})$	Maximum Average Forward Current ($T_C = 112^\circ\text{C}$, Duty Cycle = 0.5)		40	Amps
$I_F(\text{RMS})$	RMS Forward Current (Square wave, 50% duty)		63	
I_{FSM}	Non-Repetitive Forward Surge Current ($T_J = 45^\circ\text{C}$, 8.3ms)		210	

STATIC ELECTRICAL CHARACTERISTICS

Symbol	Characteristic / Test Conditions	MIN	TYP	MAX	UNIT
V_F	Forward Voltage		$I_F = 35\text{A}$	2.7	Volts
			$I_F = 70\text{A}$	3.28	
			$I_F = 35\text{A}, T_J = 125^\circ\text{C}$	2.07	

DYNAMIC CHARACTERISTICS

Symbol	Characteristic	Test Conditions	MIN	TYP	MAX	UNIT
t_{rr}	Reverse Recovery Time	$I_F = 1\text{A}, di_F/dt = -100\text{A}/\mu\text{s}, V_R = 30\text{V}, T_J = 25^\circ\text{C}$	-	26		ns
t_{rr}	Reverse Recovery Time	$I_F = 40\text{A}, di_F/dt = -200\text{A}/\mu\text{s}, V_R = 800\text{V}, T_C = 25^\circ\text{C}$	-	350		
Q_{rr}	Reverse Recovery Charge		-	570		nC
I_{RRM}	Maximum Reverse Recovery Current		-	4	-	Amps
t_{rr}	Reverse Recovery Time	$I_F = 40\text{A}, di_F/dt = -200\text{A}/\mu\text{s}, V_R = 800\text{V}, T_C = 125^\circ\text{C}$	-	430		ns
Q_{rr}	Reverse Recovery Charge		-	2200		nC
I_{RRM}	Maximum Reverse Recovery Current		-	9	-	Amps
t_{rr}	Reverse Recovery Time	$I_F = 40\text{A}, di_F/dt = -1000\text{A}/\mu\text{s}, V_R = 800\text{V}, T_C = 125^\circ\text{C}$	-	210		ns
Q_{rr}	Reverse Recovery Charge		-	3400		nC
I_{RRM}	Maximum Reverse Recovery Current		-	29		Amps

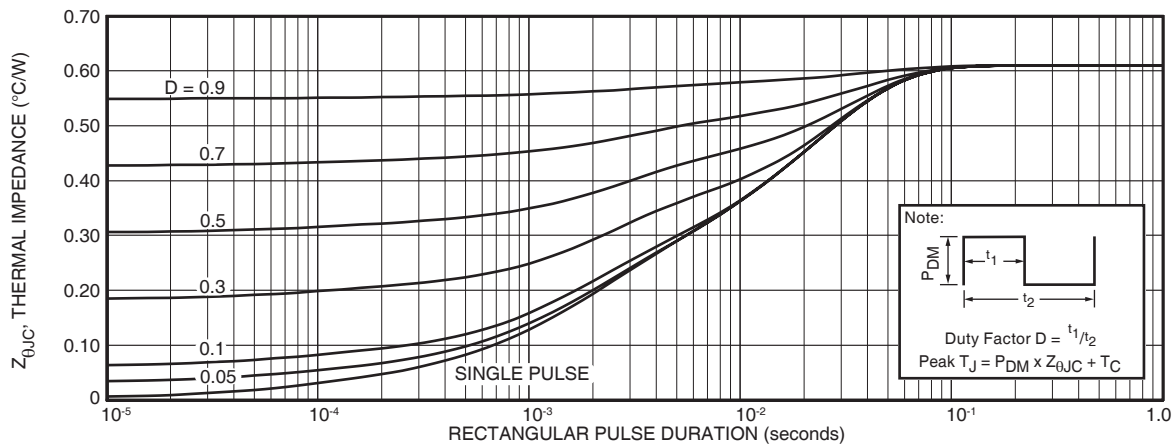


FIGURE 24a. MAXIMUM EFFECTIVE RECTANGULAR THERMAL IMPEDANCE, JUNCTION-TO-CASE vs. PULSE DURATION RC MODEL

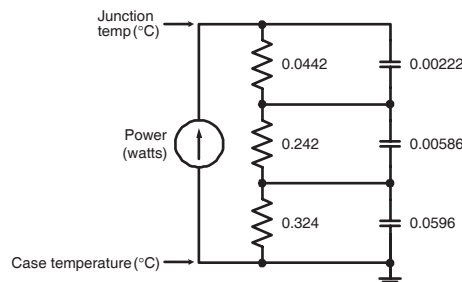


FIGURE 24b. TRANSIENT THERMAL IMPEDANCE MODEL

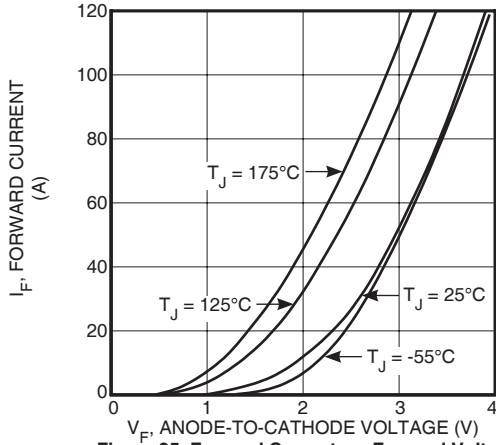


Figure 25. Forward Current vs. Forward Voltage

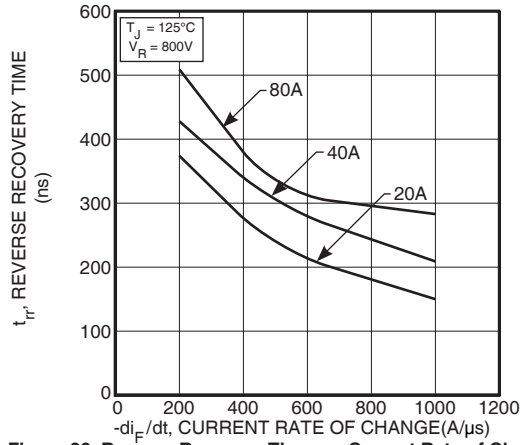


Figure 26. Reverse Recovery Time vs. Current Rate of Change

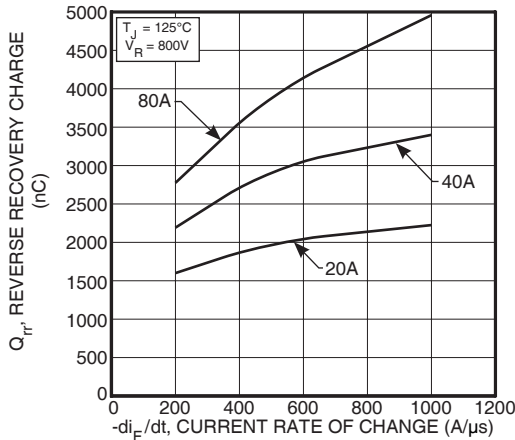


Figure 27. Reverse Recovery Charge vs. Current Rate of Change

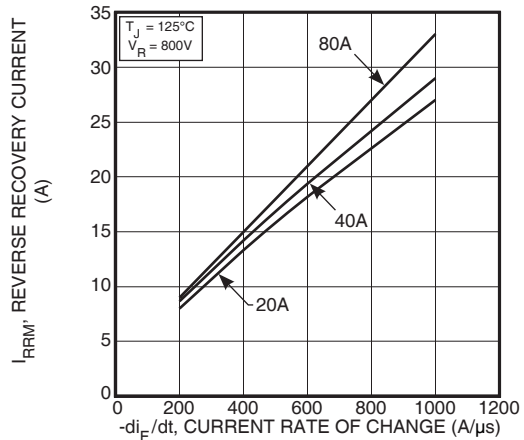


Figure 28. Reverse Recovery Current vs. Current Rate of Change

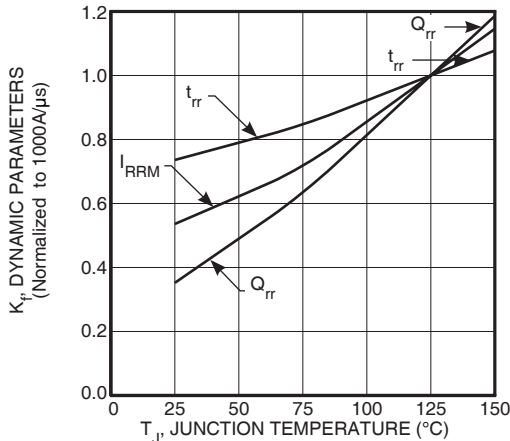


Figure 29. Dynamic Parameters vs. Junction Temperature

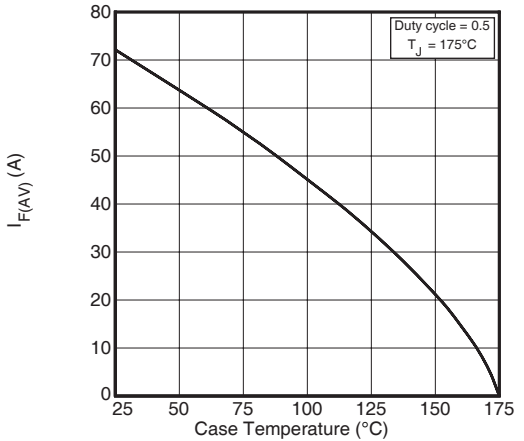


Figure 30. Maximum Average Forward Current vs. Case Temperature

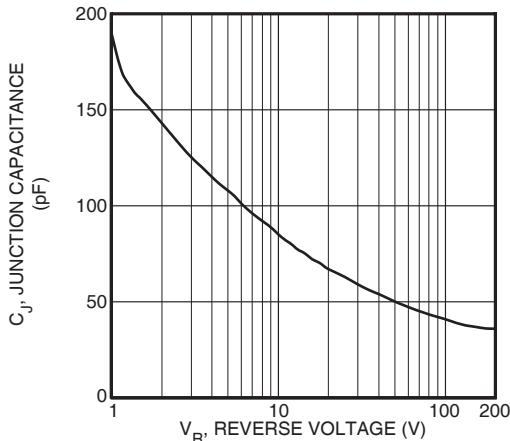


Figure 31. Junction Capacitance vs. Reverse Voltage

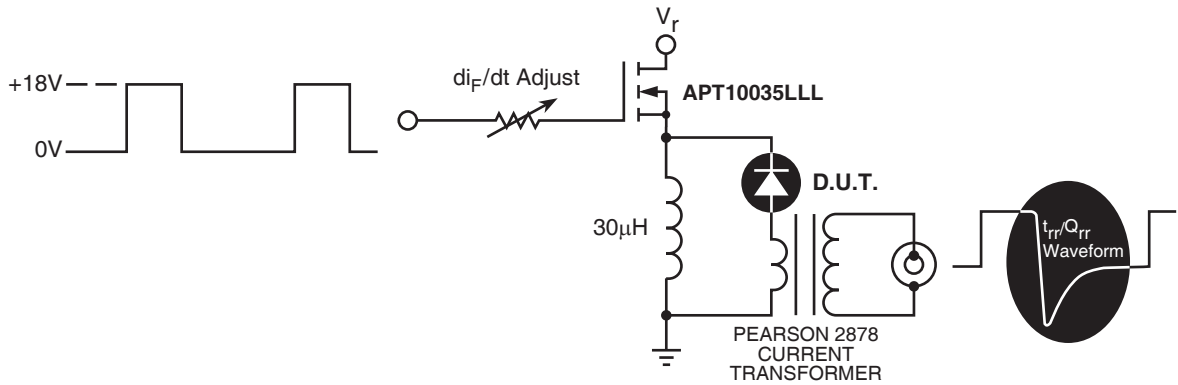


Figure 32. Diode Test Circuit

- 1 I_F - Forward Conduction Current
- 2 di_F/dt - Rate of Diode Current Change Through Zero Crossing.
- 3 I_{RRM} - Maximum Reverse Recovery Current.
- 4 t_{rr} - Reverse Recovery Time, measured from zero crossing where diode current goes from positive to negative, to the point at which the straight line through I_{RRM} and $0.25 \cdot I_{RRM}$ passes through zero.
- 5 Q_{rr} - Area Under the Curve Defined by I_{RRM} and t_{rr} .

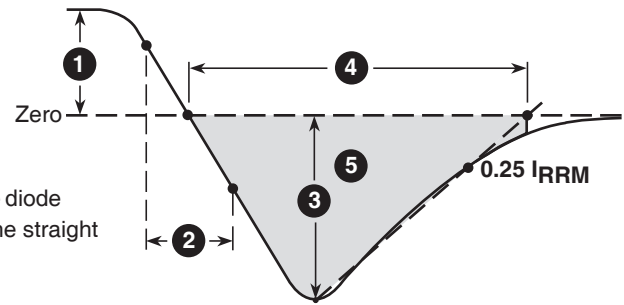
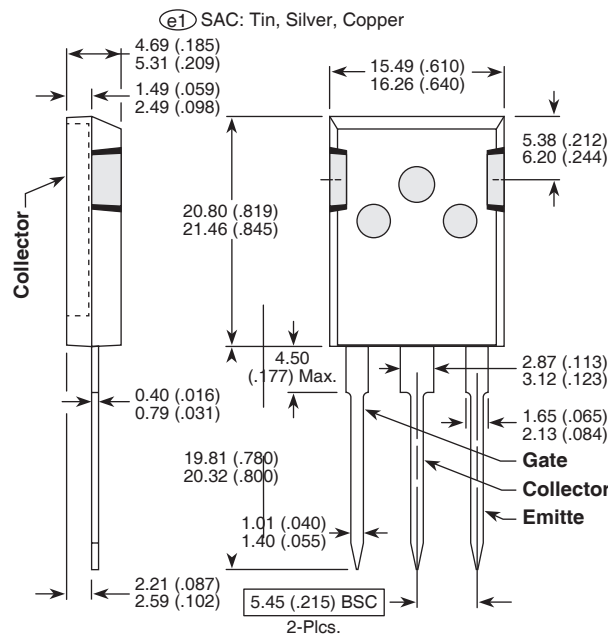


Figure 33. Diode Reverse Recovery Waveform and Definitions

T-MAX® (B2) Package Outline



Dimensions in Millimeters and (Inches)

APT's products are covered by one or more of U.S. patents 4,895,810 5,045,903 5,089,434 5,182,234 5,019,522 5,262,336 6,503,786 5,256,583 4,748,103 5,283,202 5,231,474 5,434,095 5,528,058 and foreign patents. US and Foreign patents pending. All Rights Reserved.