

## PWM Controller

**AP3021**

### Pin Configuration

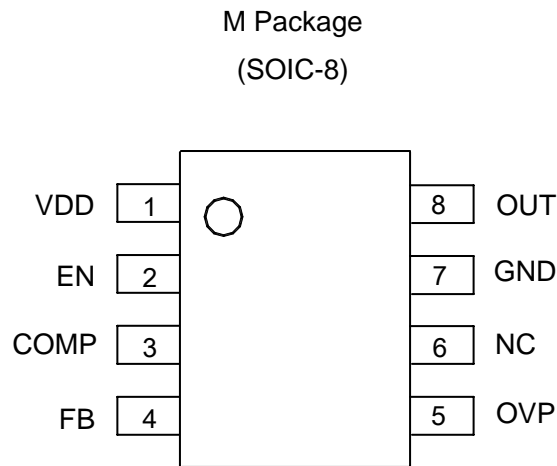


Figure 2. Pin Configuration of AP3021 (Top View)

### Pin Description

Pin Number	Pin Name	Function
1	VDD	Supply input pin. A capacitor should be connected between the VDD pin and GND pin to keep the DC input voltage constant
2	EN	Enable and dimming Pin. When EN input voltage is lower than 0.3V, disable IC. When EN input voltage works at 0.5V to 2.4V, enable DC dimming. When EN input voltage is higher than 2.6V, enable IC
3	COMP	Compensation pin. This pin is the output of the internal error amplifier. COMP is forced to low when $V_{DD} < 3.6V$
4	FB	Feedback pin. This pin is connected to an external resistor divider to program the system output voltage
5	OVP	Over voltage detection pin
6	NC	No connection
7	GND	Ground pin
8	OUT	Output pin, this pin is connected to external MOSFET gate to turn it on/off

## PWM Controller

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### Functional Block Diagram

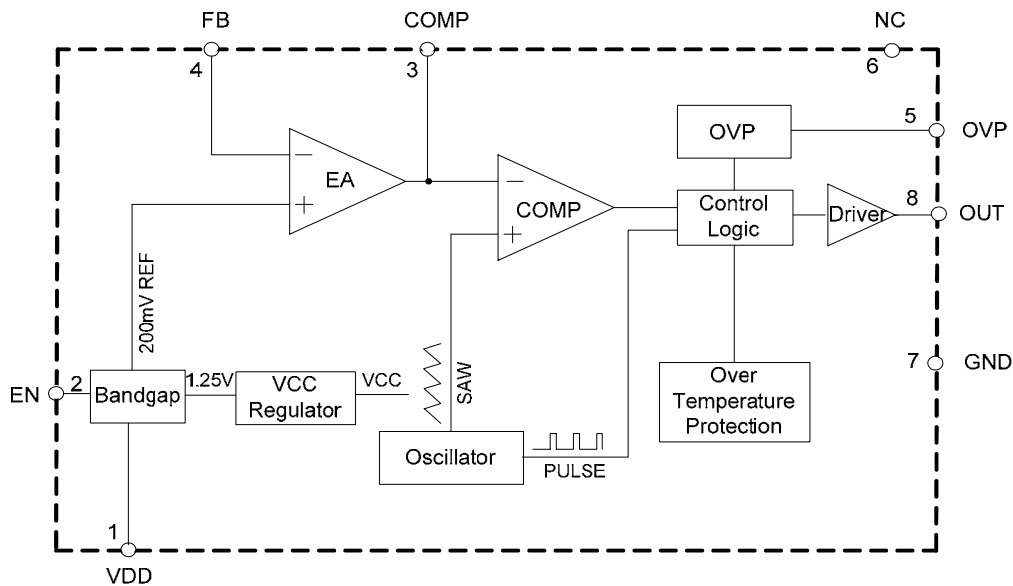
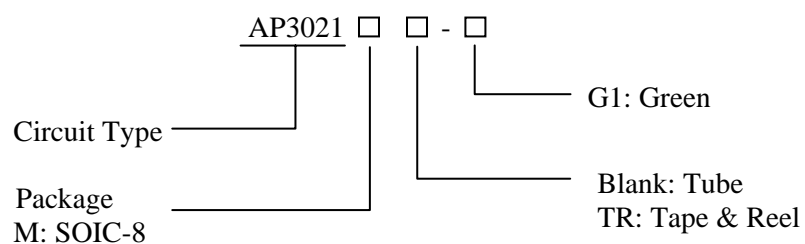


Figure 3. Functional Block Diagram of AP3021

### Ordering Information



Package	Temperature Range	Part Number	Marking ID	Packing Type
SOIC-8	-40 to 85°C	AP3021M-G1	3021M-G1	Tube
		AP3021MTR-G1	3021M-G1	Tape & Reel

BCD Semiconductor's Pb-free products, as designated with "G1" suffix in the part number, are RoHS compliant and green.

**PWM Controller****AP3021****Absolute Maximum Ratings (Note 1)**

Parameter	Symbol	Value	Unit
VDD Pin Voltage	$V_{DD}$	-0.3 to 20	V
EN Pin Voltage	$V_{EN}$	-0.3 to $V_{DD}+0.3$	V
OUT Pin Voltage	$V_{OUT}$	20	V
OVP Pin Voltage	$V_{OVP}$	7	V
Feedback Pin Voltage	$V_{FB}$	-0.3 to 5.5	V
Thermal Resistance	$\theta_{JA}$	105	°C/W
Operating Junction Temperature	$T_J$	150	°C
Storage Temperature	$T_{STG}$	-65 to 150	°C
Lead Temperature (Soldering, 10sec)	$T_{LEAD}$	260	°C

Note 1: Stresses greater than those listed under “Absolute Maximum Ratings” may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under “Recommended Operating Conditions” is not implied. Exposure to “Absolute Maximum Ratings” for extended periods may affect device reliability.

**Recommended Operating Conditions**

Parameter	Symbol	Min	Max	Unit
Input Voltage	$V_{DD}$	4.0	18	V
Operating Ambient Temperature	$T_A$	-40	85	°C

**PWM Controller****AP3021****Electrical Characteristics** $V_{DD}=5V$ ,  $T_A=25^{\circ}C$ , unless otherwise specified.

Parameters	Symbol	Conditions	Min	Typ	Max	Unit
SUPPLY VOLTAGE (VDD PIN)						
Input Voltage	V <sub>DD</sub>		4.0		18	V
Quiescent Current	I <sub>Q</sub>	V <sub>FB</sub> =0.3V		5		mA
Shutdown Current	I <sub>SHDN</sub>	V <sub>EN</sub> =0V		0.1		μA
UNDER VOLTAGE LOCKOUT						
Input UVLO Threshold	V <sub>UVLO</sub>	V <sub>DD</sub> Rising		3.8		V
Input UVLO Hysteresis	V <sub>HYS</sub>		100	200	300	mV
OVER VOLTAGE PROTECTION						
Over Voltage Threshold			1.45	1.55	1.65	V
OVER TEMPERATURE PROTECTION						
Thermal Shutdown Temperature	T <sub>OTSD</sub>			160		°C
Thermal Shutdown Hysteresis				20		°C
ENABLE (EN PIN)						
EN Pin Threshold	V <sub>EN-OFF</sub>		0.3			V
	V <sub>EN-ON</sub>				2.6	V
EN Pin Dimming Threshold	V <sub>EN-DIM</sub>		0.4	0.5		V
				2.4	2.6	V
VOLTAGE REFERENCE (FB PIN)						
Feedback Voltage	V <sub>FB</sub>		0.194	0.2	0.206	V
Feedback Bias Current	I <sub>FB</sub>			35	100	nA
Line Regulation		V <sub>DD</sub> =4.5V to 8V T <sub>A</sub> =25°C		2	20	mV
		V <sub>DD</sub> =8V to 18V T <sub>A</sub> =25°C		4	40	mV
OUTPUT						
Oscillator Frequency	f <sub>OSC</sub>		247	330	412	kHz
Maximum Duty Cycle	D <sub>MAX</sub>		85	90	95	%
OUT Voltage (Note2)	V <sub>OUT</sub>	I <sub>OUT</sub> =10mA		V <sub>DD</sub> -1		V
Rising Time	t <sub>RISE</sub>	Output Pin Load=1nF		65.5		ns
Falling Time	t <sub>FALL</sub>			88.5		ns

Note 2: Guaranteed by design.

**PWM Controller****AP3021****Operation Description****1. PWM Operation**

The AP3021 uses a constant frequency, voltage mode control scheme to provide excellent line and load regulation. To drive LED load, AP3021 can be connected as current regulator (Figure 5) or voltage regulator (Figure 6), both topologies can realize LED lighting and dimming. Load regulation is less than 3% even if AP3021 is connected as voltage regulator. The maximum duty cycle of the PWM controller is limited to 93%. For more information please refer to the functional block diagram (Figure 3).

**2. Enable Control**

The EN pin is used to turn on and off the controller. When EN input voltage is lower than 0.3V, the controller turns off and the shutdown current can be limited under 0.1 $\mu$ A. When EN input voltage goes higher than 2.6V, the controller turns on and the feedback voltage is set to 0.2V. When EN voltage works at 0.5V to 2.4V, the IC will operate in dimming mode, more details please refer to section 4.

The AP3021 has an internal 80 $\pm$ 15%k $\Omega$  pull low resistor on the EN pin, thus the device is normally in the OFF state or shutdown mode operation.

**3. Soft Start Control**

The AP3021 external compensation circuit can clamp the output waveform to soft start smoothly.

**4. DC Dimming Control**

The DC Dimming is to inject a constant DC voltage to EN pin. The feedback voltage is indirectly adjusted when the EN pin voltage is between 0.5V to 2.4V, which can be used as dimming control. Application in Figure 4 is designed so that a voltage signal of 0.5V to 2.4V varies the LED current from minimum output current to maximum output current. Comparing with all kinds of dimming control, this method features a best output voltage and LEDs current.

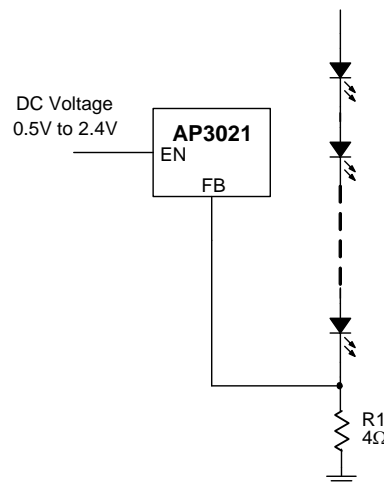


Figure 4. Dimming Control Using A DC Voltage Signal in EN Pin

**5. Operation Protection****5.1 Over Voltage Protection**

The controller has an internal OVP circuit. The controller output voltage will boost too high when LEDs are disconnected from the circuit or fail open. The AP3021 will latch off the output if the OVP pin senses a voltage that is higher than 1.55V, and the power switching will be turned off. The latched off status can be reset by reducing the supply voltage to lower than 3.6V.

**5.2 UVLO**

The AP3021 provides an under voltage lockout circuit to prevent it from undefined status when startup. The UVLO circuit turns the driver off when supply voltage drops below 3.6V. The UVLO circuit has 200mV hysteresis, which means the device starts up again when  $V_{DD}$  rises to 3.8V.

**5.3 OTP**

The AP3021 monitors the temperature of itself. If the temperature exceeds the threshold value (typically 160°C), the AP3021 shuts itself off. This is a no-latched protection. The device recovers once the temperature has decreased approximately 20°C.

## Typical Application

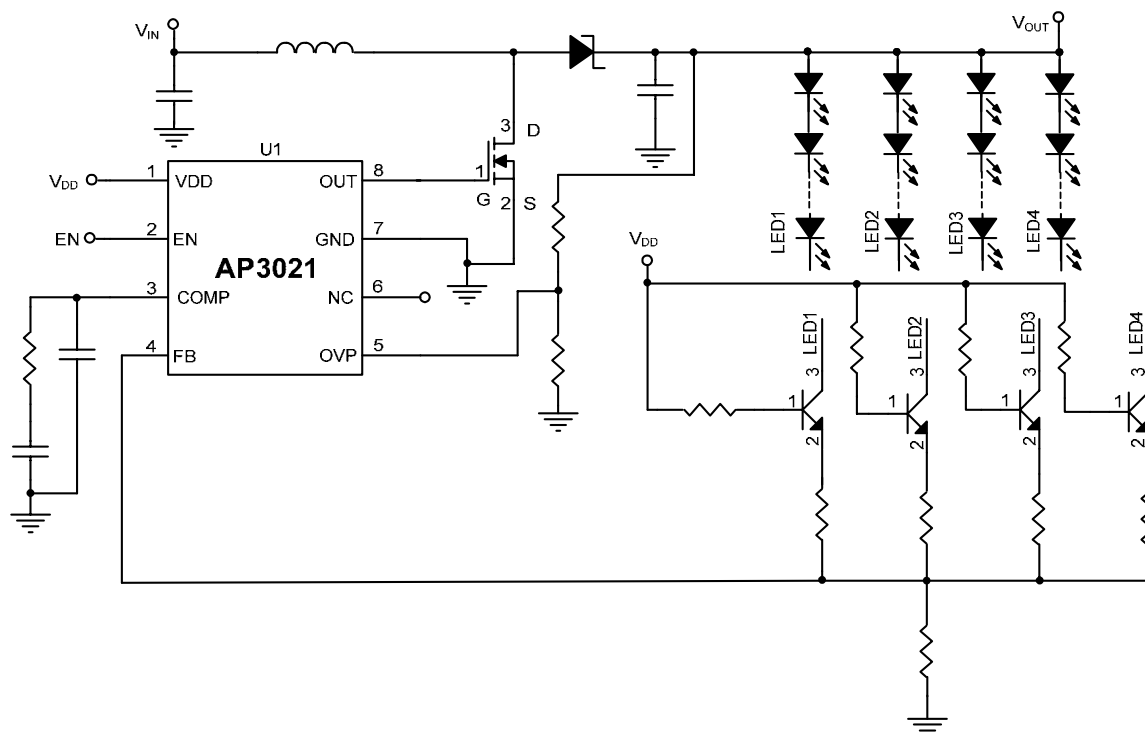


Figure 5. Current Regulator to Drive LEDs

## Typical Application (Continued)

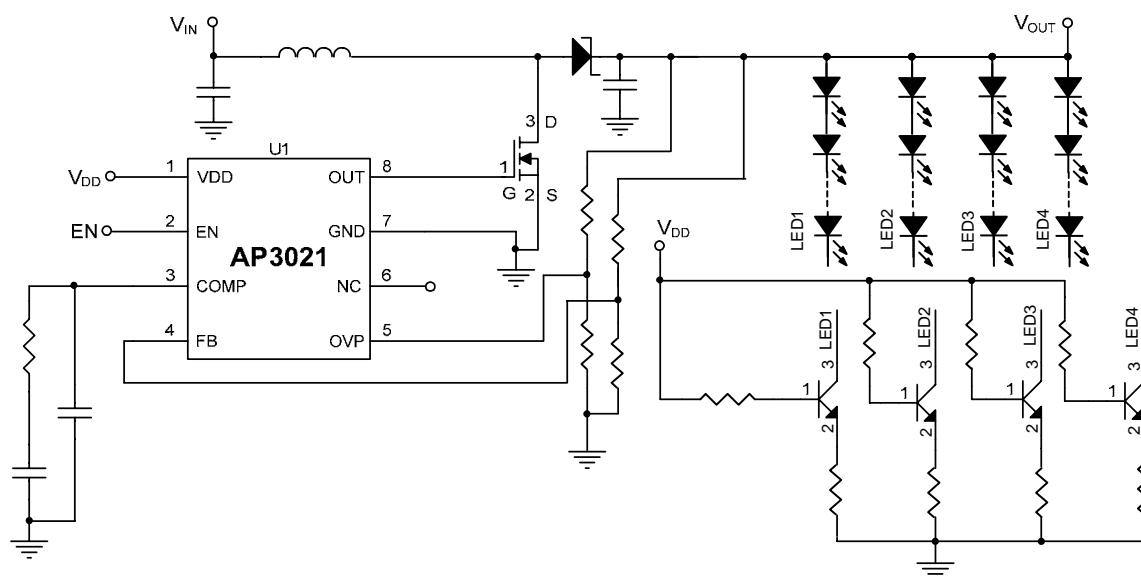


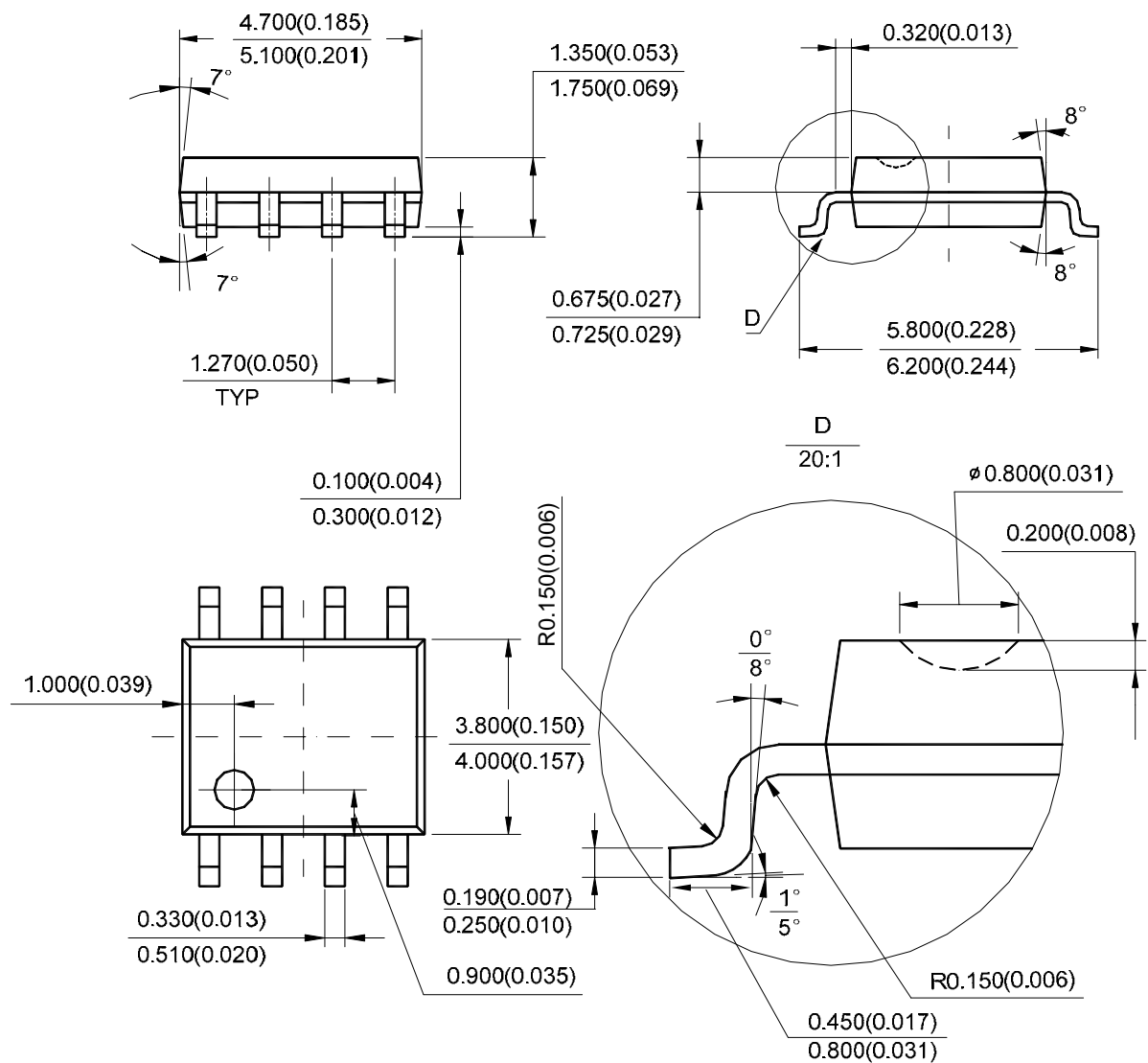
Figure 6. Voltage Regulator to Drive LEDs



## Mechanical Dimensions

SOIC-8

Unit: mm(inch)



Note: Eject hole, oriented hole and mold mark is optional.



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#### **MAIN SITE**

##### **- Headquarters**

##### **BCD Semiconductor Manufacturing Limited**

No. 1600, Zi Xing Road, Shanghai ZiZhu Science-based Industrial Park, 200241, China  
Tel: +86-21-24162266, Fax: +86-21-24162277

##### **- Wafer Fab**

##### **Shanghai SIM-BCD Semiconductor Manufacturing Co., Ltd.**

800 Yi Shan Road, Shanghai 200233, China  
Tel: +86-21-6485 1491, Fax: +86-21-5450 0008

#### **REGIONAL SALES OFFICE**

##### **Shenzhen Office**

##### **Shanghai SIM-BCD Semiconductor Manufacturing Co., Ltd., Shenzhen Office**

Unit A Room 1203, Skyworth Bldg., Gaoxin Ave. I.S., Nanshan District, Shenzhen, China  
Tel: +86-755-8826 7951  
Fax: +86-755-8826 7865

##### **Taiwan Office**

##### **BCD Semiconductor (Taiwan) Company Limited**

4F, 298-1, Rui Guang Road, Nei-Hu District, Taipei, Taiwan  
Tel: +886-2-2656 2808  
Fax: +886-2-2656 2806

##### **USA Office**

##### **BCD Semiconductor Corp.**

30920 Huntwood Ave. Hayward, CA 94544, USA  
Tel : +1-510-324-2988  
Fax: +1-510-324-2788