

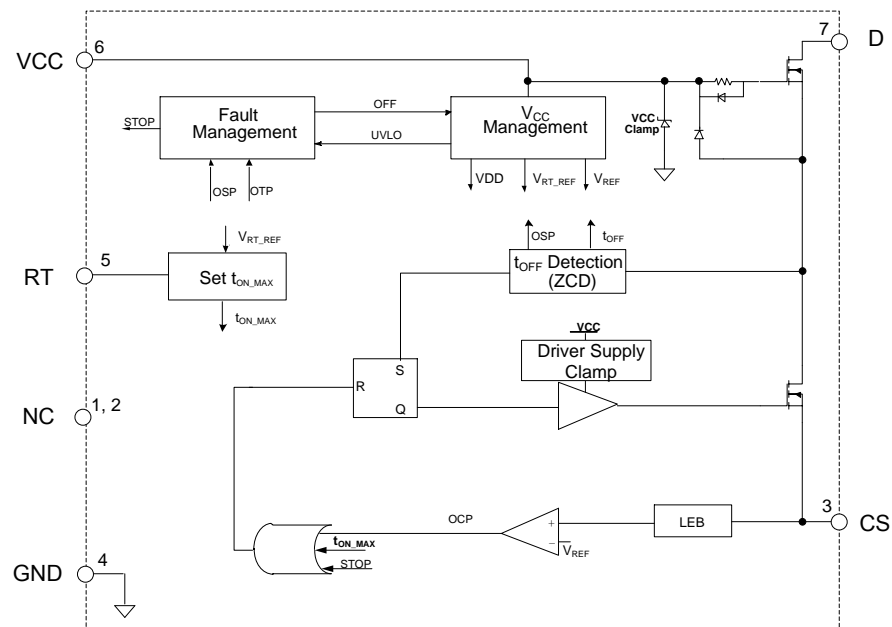
The circuit diagram illustrates a 1W LED driver. It begins with an AC input connected to a bridge rectifier (DB1) through a fuse (FR1). The rectified output is filtered by a capacitor (C1) and then passes through an inductor (L1). A network of resistors (R1, R2, R3) and capacitors (C2, C3) is used for voltage regulation and filtering. The AL1696 IC (U1) is configured with its NC pins (1, 2) to ground, CS (3) to the input, and GND (4) to ground. The RT pin (5) is connected to ground through a resistor (R6). The D pin (7) is connected to the input through a resistor (R2) and to the output through a resistor (R3). The output of the IC is connected to a diode (D1) and a network of resistors (R4, R7) and capacitors (C4, C5) to drive the LEDs. A diode (D2) is also connected to ground through a resistor (R4).

The circuit diagram illustrates a 12V LED driver. It begins with an AC input connected to a bridge rectifier (DB1) through a fuse (FR1). The rectified output is filtered by a capacitor (C1). The circuit then splits into two parallel branches, each containing a resistor (R1, R2) and a capacitor (C2, C3). The main power path continues through an inductor (L1) to the input of the U1 AL1696 IC. The IC's pins are configured as follows: NC1 (1), NC2 (2), CS (3), GND (4), RT (5), VCC (6), and D (7). A resistor (R5) is connected between the GND pin and the common ground. The output of the IC (pin 7) is connected to a resistor (R2) and a capacitor (C4). The output voltage is regulated by a feedback network consisting of a resistor (R4) and a diode (D2) connected to the VCC pin (6). The output is further filtered by an inductor (L2) and a capacitor (C5) to produce a stable 12V output for the LEDs. A diode (D1) is connected in parallel with the output to protect against reverse voltage.

## Pin Descriptions

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## Functional Block Diagram



## Absolute Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.) (Note 4)

Symbol	Parameter	Rating	Unit
V <sub>CC</sub>	Power Supply Voltage	18	V
V <sub>D</sub>	Voltage on D Pin (AL1696-30AS7-13)	300	V
	Voltage on D Pin (AL1696-30BAS7-13)	400	V
	Voltage on D Pin (AL1696-20BS7-13)	500	V
	Voltage on D Pin (AL1696-20CS7-13)	600	V
I <sub>D</sub>	Continuous Drain Current T <sub>C</sub> = +25°C (AL1696-30AS7-13)	3.0	A
	Continuous Drain Current T <sub>C</sub> = +25°C (AL1696-30BAS7-13)	3.0	A
	Continuous Drain Current T <sub>C</sub> = +25°C (AL1696-20BS7-13)	2.0	A
	Continuous Drain Current T <sub>C</sub> = +25°C (AL1696-20CS7-13)	2.0	A
V <sub>CS</sub>	Voltage on CS Pin	-0.3 to 7	V
V <sub>RT</sub>	Voltage on RT Pin	-0.3 to 7	V
T <sub>J</sub>	Operating Junction Temperature	-40 to +150	°C
T <sub>STG</sub>	Storage Temperature	-65 to +150	°C
T <sub>LEAD</sub>	Lead Temperature (Soldering, 10 seconds)	+260	°C
P <sub>D</sub>	Power Dissipation and Thermal Characteristics (T <sub>A</sub> = +50°C)	0.8	W
θ <sub>JA</sub>	Thermal Resistance (Junction to Ambient) (Note 5)	123	°C/W
θ <sub>JC</sub>	Thermal Resistance (Junction to Case) (Note 5)	19	°C/W
—	ESD (Human Body Model)	2,000	V
	ESD (Machine Model)	200	V

Notes: 4. Stresses greater than those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "Recommended Operating Conditions" is not implied. Exposure to "Absolute Maximum Ratings" for extended periods may affect device reliability.

5. Device mounted on 1"x1" FR-4 substrate PCB, 2oz copper, with minimum recommended pad layout.

## Recommended Operating Conditions (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Symbol	Parameter	Min	Max	Unit
T <sub>A</sub> (Note 6)	Ambient Temperature	-40	+105	°C

Note: 6. The device can operate normally at +125°C ambient temperature under the condition that the junction temperature is less than +150°C.

**Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

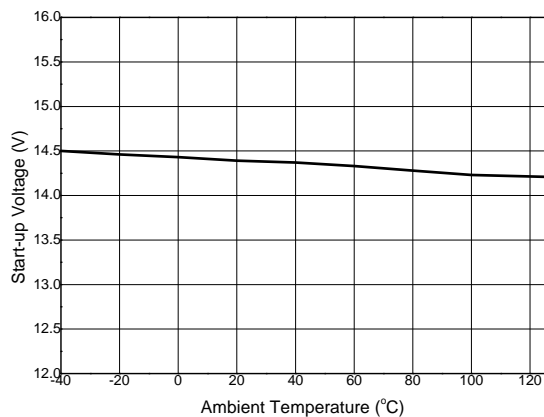
Symbol	Parameter	Condition	Min	Typ	Max	Unit
UVLO						
V <sub>TH(ST)</sub>	Startup Voltage	–	–	14.5	–	V
V <sub>OPR(MIN)</sub>	Minimal Operating Voltage	After Turn On	–	8.5	–	V
V <sub>CC_CLAMP</sub>	V <sub>CC</sub> Clamp Voltage	I <sub>CC</sub> = 1mA	–	16	–	V
Standby Current						
I <sub>ST</sub>	Start-Up Current	V <sub>CC</sub> = V <sub>TH(ST)</sub> -0.5V, Before Start Up	–	150	–	μA
I <sub>CC(OPR)</sub>	Operating Current	Switching Frequency at 5kHz	–	120	–	μA
Internal High Voltage MOSFET						
R <sub>DS(ON)</sub>	Drain-Source On-State Resistance	AL1696-30A	–	–	3	Ω
		AL1696-30BA	–	–	3.4	Ω
		AL1696-20B	–	–	6	Ω
		AL1696-20C	–	–	5.5	Ω
I <sub>DS</sub>	Continuous Drain-Source Current	AL1696-30A	–	–	3.0	A
		AL1696-30BA	–	–	3.0	A
		AL1696-20B	–	–	2.0	A
		AL1696-20C	–	–	2.0	A
V <sub>DS</sub>	Drain-Source Breakdown Voltage	AL1696-30A	300	–	–	V
		AL1696-30BA	400	–	–	V
		AL1696-20B	500	–	–	V
		AL1696-20C	600	–	–	V
I <sub>DSS</sub>	Drain-Source Leakage Current	AL1696-30A @ V <sub>DS</sub> = 300V, V <sub>GS</sub> = 0V	–	–	1	μA
		AL1696-30BA @ V <sub>DS</sub> = 400V, V <sub>GS</sub> = 0V	–	–	1	μA
		AL1696-20B @ V <sub>DS</sub> = 500V, V <sub>GS</sub> = 0V	–	–	1	μA
		AL1696-20C @ V <sub>DS</sub> = 600V, V <sub>GS</sub> = 0V	–	–	1	μA
RT						
V <sub>RT</sub>	Reference Voltage of RT Pin	–	–	0.5	–	V
Current Sense						
V <sub>REF</sub>	Current Sense Reference	–	0.388	0.400	0.412	V
t <sub>ON_MIN</sub>	Minimum t <sub>ON</sub>	–	–	550	–	ns
t <sub>ON_MAX</sub>	Maximum t <sub>ON</sub>	R <sub>T</sub> = 27kΩ	–	8	–	μs
t <sub>OFF_MAX</sub>	Maximum t <sub>OFF</sub>	–	–	180	–	μs
t <sub>OFF_MIN</sub>	Minimum t <sub>OFF</sub> (Note 7)	–	–	6	–	μs
Over Temperature Protection						
–	Shutdown Temperature (Notes 7 & 8)	–	–	+170	–	°C

Notes: 7. These parameters, although guaranteed by design, are not 100% tested in production.

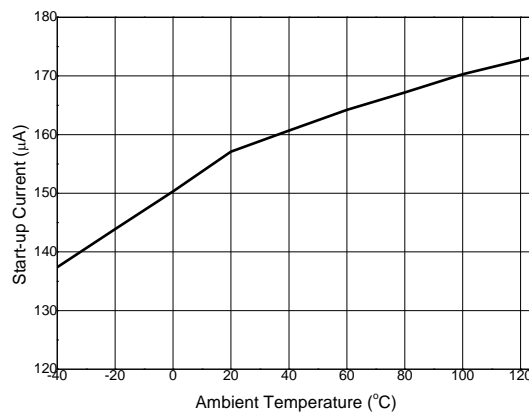
8. The device will latch when OTP happen and the device won't operate constantly at this temperature.

## Performance Characteristics (Note 9)

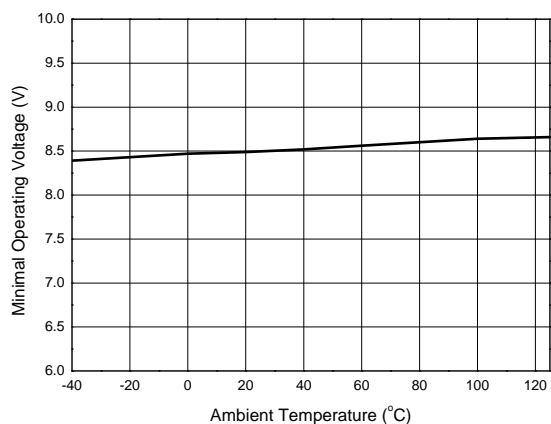
**Start-up Voltage vs. Ambient Temperature**



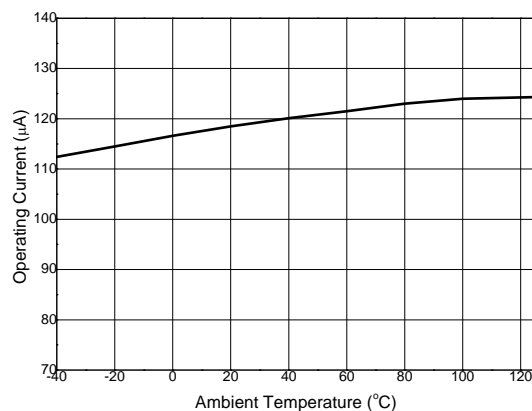
**Start-up Current vs. Ambient Temperature**



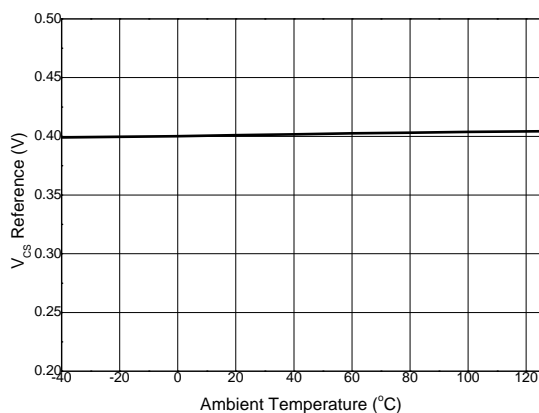
**Minimal Operating Voltage vs. Ambient Temperature**



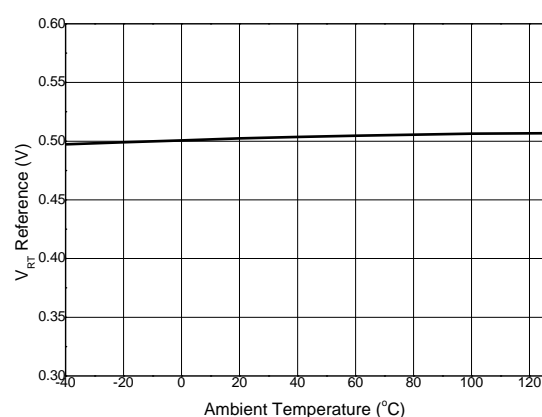
**Operating Current vs. Ambient Temperature**



**V<sub>CS</sub> Reference vs. Ambient Temperature**

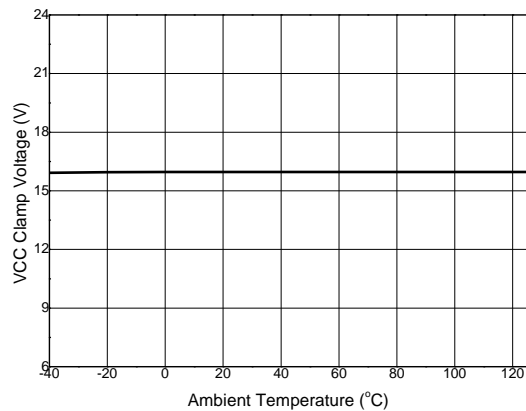


**V<sub>RT</sub> Reference vs. Ambient Temperature**



## Performance Characteristics (Cont.) (Note 9)

### VCC Clamp Voltage vs. Ambient Temperature



Note: 9. These electrical characteristics are tested under DC condition. The ambient temperature is equal to the junction temperature of the device.

## Functional Description and Application Information

### Converter Operation

The AL1696 is a single stage, single winding inductor, high efficiency, and high power factor LED driver solution for mains input phase-cutting dimmable application. It is available for four internal MOSFET options (300V/3A, 400V/3A, 500V/2A and 600V/2A) which helps to reduce the overall LED driver solution's size and optimize born cost. The three different MOSFET options can cover most of 3~12W dimmable applications.

The AL1696 internal MOSFET's on time is limited by  $t_{ON\_MAX}$  which is set through RT pin and internal 0.4V reference. The MOSFET will be turned off either its on-time triggers  $t_{ON\_MAX}$  or input voltage on CS pin triggers internal 0.4V reference voltage. So if the  $t_{ON\_MAX}$  is set to a very small value, the system will operate in constant on-time ( $t_{ON\_MAX}$ ) mode during the whole rectified mains cycle. It will result in a good power factor, but the line regulation will be worse. Normally, a recommended  $t_{ON\_MAX}$  should make the system operate in constant on-time ( $t_{ON\_MAX}$ ) mode at valley of input voltage and peak current mode at the crest. A trade-off between PF and line regulation need to be done when setting  $t_{ON\_MAX}$ .

The AL1696 adopts source-driver technology to decrease the system operating current. Besides, it uses a novel  $t_{OFF}$  time detection method without auxiliary winding. The AL1696 operates in boundary conduction mode (BCM) which can ease EMI design. All of them help AL1696 to have an extremely low bill of material (BOM).

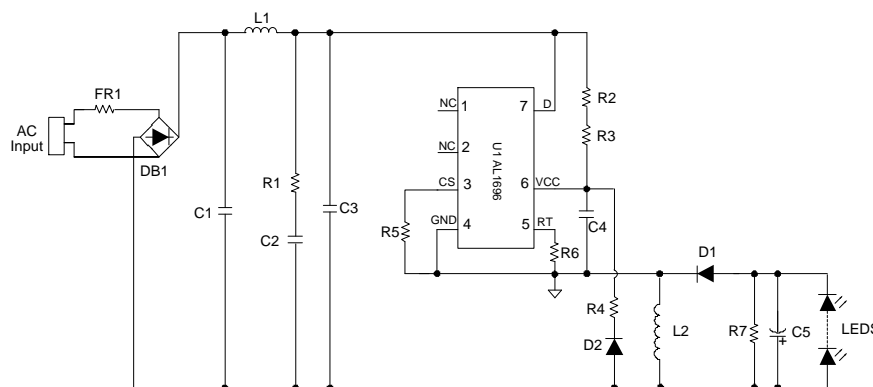


Figure 1. Typical Buck-Boost Application Circuit

### Start-up and Supply Voltage

During start-up, the VCC capacitor C4 is charged through startup resistors (R2, R3) from the rectified mains input until the start-up voltage is reached, the AL1696 starts switching. In normal operation, the VCC supply is provided from two paths: one is from start-up resistors (R2, R3) and the other is from output voltage ( $V_{OUT}$ ) through one diode (D2). In this way the system can provide sufficient VCC supply at low dimming angle.

The AL1696 has internal VCC clamp, the typical voltage is 16V. VCC voltage needs to be between  $V_{OPR(MIN)}$  and  $V_{CC\_CLAMP}$  during normal operation.

When VCC voltage drops below the under voltage lockout (UVLO), IC will stop switching. The IC will restart once the voltage on VCC pin exceeds the startup voltage ( $V_{TH(ST)}$ ) again.

### Protections

#### Under Voltage Lockout (UVLO)

When the voltage on the VCC pin drops below  $V_{OPR(MIN)}$ , the IC will stop switching. The IC will restart until the VCC voltage exceeds the startup voltage ( $V_{TH(ST)}$ ) again.

#### Leading-Edge Blanking (LEB)

To prevent false detection of the peak current through MOSFET, a blanking time following switch-on is designed. When the internal switch turns on, a short current spike may appear because of the discharge of the parasitic capacitor over MOSFET's drain and source. It will be ignored during the LEB time ( $t_{ON\_MIN}$ ).

#### Cycle-by-cycle Over Current Protection (OCP)

The AL1696 has a built-in peak current detector. The R5 connected to the CS pin is used to sense the current through MOSFET and will be one of the inputs. The detection circuit is activated after the LEB time. When the voltage on CS pin reaches  $V_{REF}$ , the IC will turn off the switch to limit the output current.

## Functional Description and Application Information (Cont.)

The peak current ( $I_{PEAK}$ ) of the MOSFET can be set as below:

$$I_{PEAK} = \frac{V_{REF}}{R5}$$

It automatically provides a cycle-by-cycle protection of maximum current through MOSFET during operation. A propagation delay exists between over current detection and actual source-switch off, so the actual peak current will be a little higher than the OCP level set by R5.

### Output-Short Protection (OSP)

When LED is shorted, the device can't detect the  $t_{OFF}$  time, and the system will work with low switching frequency of 5kHz.

### Over-Temperature Protection (OTP)

The AL1696 has OTP protection function. When the junction temperature reaches +170°C typical, it will trigger an over-temperature protection which makes IC shut down and latched. Once OTP is triggered, the system will only restart after the system's power supply powers off and on again.

## Design Parameters Based On Buck-Boost Topology

### Setting the Current Sense Resistor R5

In AL1696 typical application, a  $t_{ON\_MAX}$  is recommended to make the system operate at constant on-time ( $t_{ON\_MAX}$ ) mode at valley of input voltage and peak current mode at the crest. In most of cases, the  $t_{ON\_MAX}$  will set to be 1.3 times of the  $t_{ON}$  at crest, and the critical angle of two operation modes will be:

$$\theta = a \sin\left(\frac{1}{1.3}\right)$$

Then the output current can be calculated as below:

$$I_{O\_MEAN} = \frac{V_{REF}}{2\pi \cdot R5} \left( \int_0^{\theta} 1.3 \cdot \sin(\theta) \frac{\sqrt{2}V_{IN\_RMS} \sin(\theta)}{\sqrt{2}V_{IN\_RMS} \sin(\theta) + V_O} dt + \int_{\theta}^{\pi-\theta} \frac{\sqrt{2}V_{IN\_RMS} \sin(\theta)}{\sqrt{2}V_{IN\_RMS} \sin(\theta) + V_O} dt + \int_{\pi-\theta}^{\pi} 1.3 \cdot \sin(\theta) \frac{\sqrt{2}V_{IN\_RMS} \sin(\theta)}{\sqrt{2}V_{IN\_RMS} \sin(\theta) + V_O} dt \right)$$

Where,

$V_{REF}$  is the internal reference, typical 0.4V.

R5 is the current sense resistor.

$V_{IN\_RMS}$  is the input voltage's RMS value.

$V_O$  is the system output voltage.

### Inductance Selection (L2)

The peak current of the MOSFET is calculated as below:

$$I_{PEAK} = \frac{V_{REF}}{R5}$$

The AL1696 is operating in boundary conduction mode which results in a variable operating frequency. The minimum switching frequency  $f_{MIN}$  should be set at the crest of the minimum AC input voltage. Inductance should be calculated according to the chosen  $f_{MIN}$ :

$$L2 = \frac{\sqrt{2}V_{IN\_RMS} \cdot V_O}{I_{PEAK} \cdot (\sqrt{2}V_{IN\_RMS} + V_O) \cdot f_{MIN}}$$



## Functional Description and Application information (Cont.)

According to the Faraday's Law of Induction, the winding number of the inductance can be calculated by:

$$N_{L2} = \frac{L2 \cdot I_{PEAK}}{A_e \cdot B_m}$$

Where,

$A_e$  is the core effective area.

$B_m$  is the maximum magnetic flux density.

### $t_{ON\_MAX}$ Setting

In order to get a high power factor and good dimmer compatibility, the system should operate in constant on time mode at valley of input voltage. It can be realized by setting the maximum on time, which is set by on external resistor connect to RT pin.

And the  $t_{ON}$  time has the below equation:

$$t_{ON\_MAX} = \frac{25 \cdot C_{REF} \cdot R6}{V_{RT\_REF}}$$

Where,

$V_{RT\_REF}$  is the internal RT pin 0.5V's reference.

$C_{REF}$  is the internal 6pF capacitor.

## Dimmer Compatibility

### Passive Bleeder Design

The passive bleeder is designed to supply latching and holding current to get rid of dimmer's misfire and flicker.

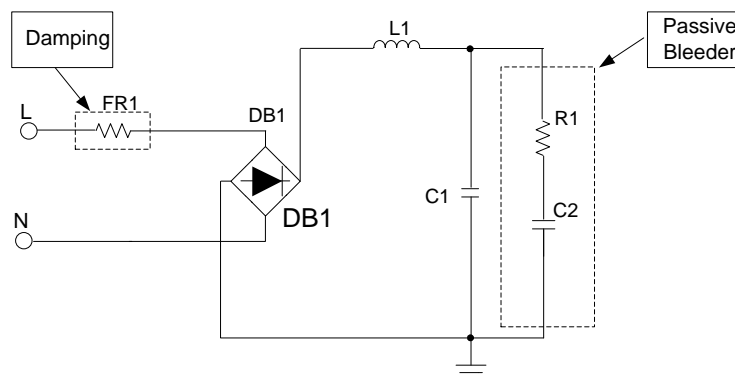


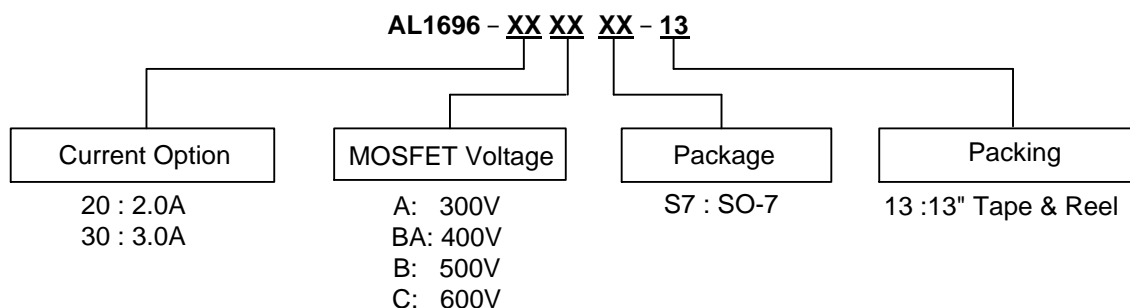
Figure 2. LED Driver Schematic with Passive Bleeder

The passive bleeder includes a capacitor ( $C2$ , in hundreds of nF) to provide latching current and a resistor ( $R1$ ) to limit the current spike. Because a large  $C2$  will affect the PF, THD and efficiency negatively, the value of the capacitor ( $C2$ ) should be selected carefully. Generally, a capacitance from 100nF/400V to 330nF/400V is recommended.  $R1$  is used to limit the latching current. If  $R1$  is too big, the latching current will be not enough and the TRIAC dimmer will be misfired, resulting in LED flicker. If  $R1$  is too small, it will result in greater power dissipation. Generally, a 200Ω to 2KΩ resistor is recommended for  $R1$ .

### Passive Damping Design

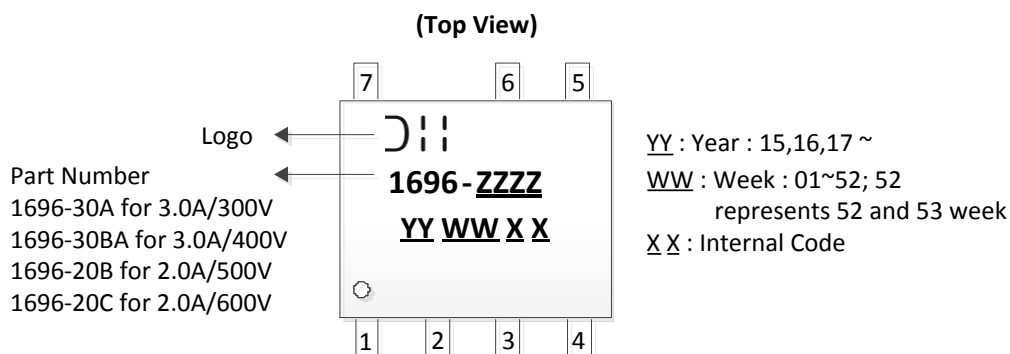
$FR1$  is to limit the spike current caused by quick charging of  $C2$  when dimmer on. Normally,  $FR1$  will be chosen from 20Ω to 100Ω for low line like 120V<sub>AC</sub> application, and 51Ω to 200Ω for high line like 230V<sub>AC</sub> application.

## Ordering Information



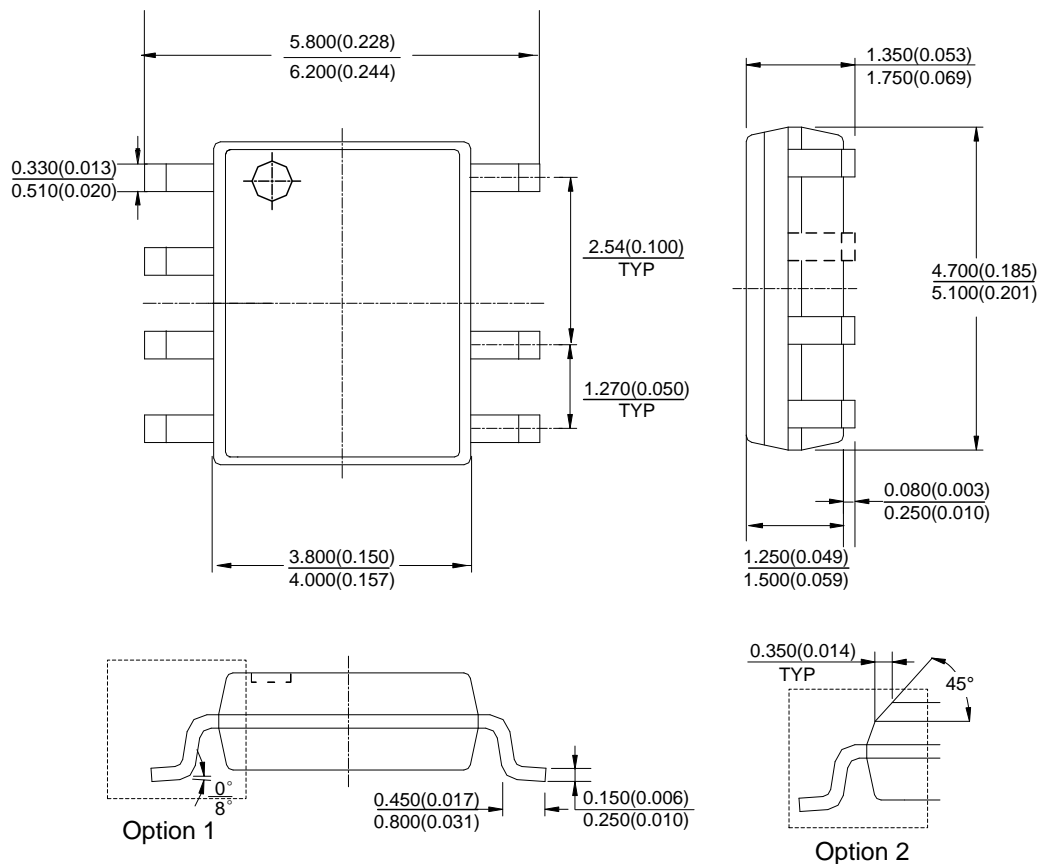
Part Number	Package Code	Package	13" Tape and Reel	
			Quantity	Part Number Suffix
AL1696-30AS7-13	S7	SO-7	4000/Tape & Reel	-13
AL1696-30BAS7-13	S7	SO-7	4000/Tape & Reel	-13
AL1696-20BS7-13	S7	SO-7	4000/Tape & Reel	-13
AL1696-20CS7-13	S7	SO-7	4000/Tape & Reel	-13

## Marking Information



# Package Outline Dimensions (All dimensions in mm.)

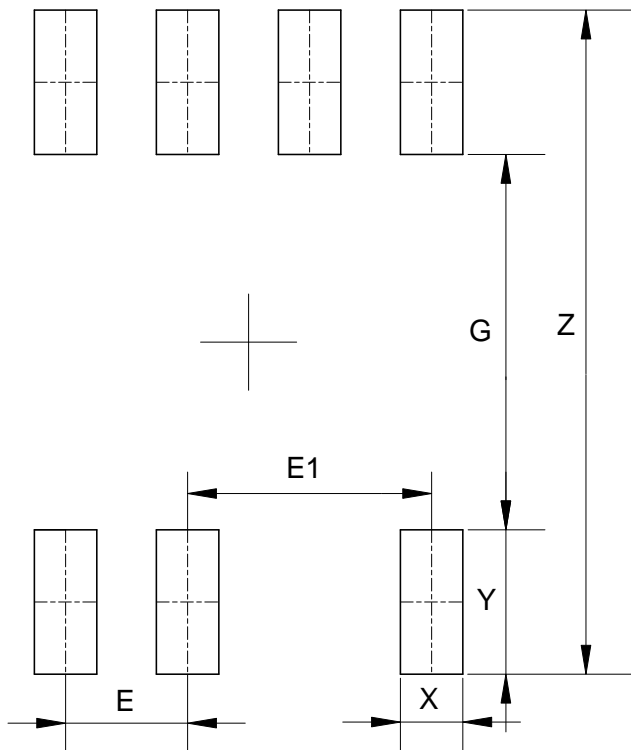
## (1) Package Type: SO-7



Note: Eject hole, oriented hole and mold mark is optional.

## Suggested Pad Layout

(1) Package Type: SO-7



Dimensions	Z (mm)/(inch)	G (mm)/(inch)	X (mm)/(inch)	Y (mm)/(inch)	E (mm)/(inch)	E1 (mm)/(inch)
Value	6.900/0.272	3.900/0.154	0.650/0.026	1.500/0.059	1.270/0.050	2.540/0.100

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