

Selected Electrical Specifications

Parameter	Symbol	Test Condition	Min	Typical	Max	Unit
Ambient Temperature	T_A	F-Grade	0	25	70	°C
		G-Grade	-40	25	85	°C
Supply Voltage, Si324x	V_{DD}		3.13	3.3	3.47	V
Supply Voltage, Si3206/3	V_{DD}		3.13	3.3	3.47	V
High Battery Voltage, Si3206/3	V_{BATH}		-9	—	-135/-110	V
Low Battery Voltage	V_{BATL}		-9	—	V_{BATH}	V
Maximum Loop Resistance (loop + load)	R_{LOOP}	$I_{LOOP}=18$ mA, $V_{BAT} = -48$ V	—	—	2000	Ω
DC Differential Output Resistance	R_{DO}	$I_{LOOP} < I_{LIM}$	160	—	640	Ω
Idle Channel Noise		C-Message weighted	—	8	10	dBrnC
PSRR from V_{DD}		RX and TX, dc to 3.4 kHz	40	—	—	dB
Longitudinal to Metallic/PCM Balance (forward or reverse)		200 Hz to 1 kHz	58	63	—	dB
		1 kHz to 3.4 kHz	53	58	—	dB
Metallic/PCM to Longitudinal Balance		200 Hz to 3.4 kHz	40	—	—	dB
Longitudinal Impedance		200 Hz to 3.4 kHz at TIP or RING	—	50	—	Ω
Longitudinal Current per Pin		Active off-hook 200 Hz to 3.4 kHz	—	—	30	mApk
DC Current		$V_{TR} = 0$ V	—	—	50	mA
2-Wire Return Loss		200 Hz to 3.4 kHz	26	30	—	dB
Transhybrid Balance		300 Hz to 3.4 kHz	26	30	—	dB
Thermal Resistance, (eTQFP-100)	θ_{JA}		—	25	—	°C/W
Continuous Power Dissipation, (eTQFP-100)	P_D	$T_A = 85$ °C	—	1.6	—	W
Thermal Resistance (QFN-40)	θ_{JA}		—	32	—	°C/W
Continuous Power Dissipation (QFN-40)	P_D	$T_A = 85$ °C	—	1.7	—	W

Package Information

