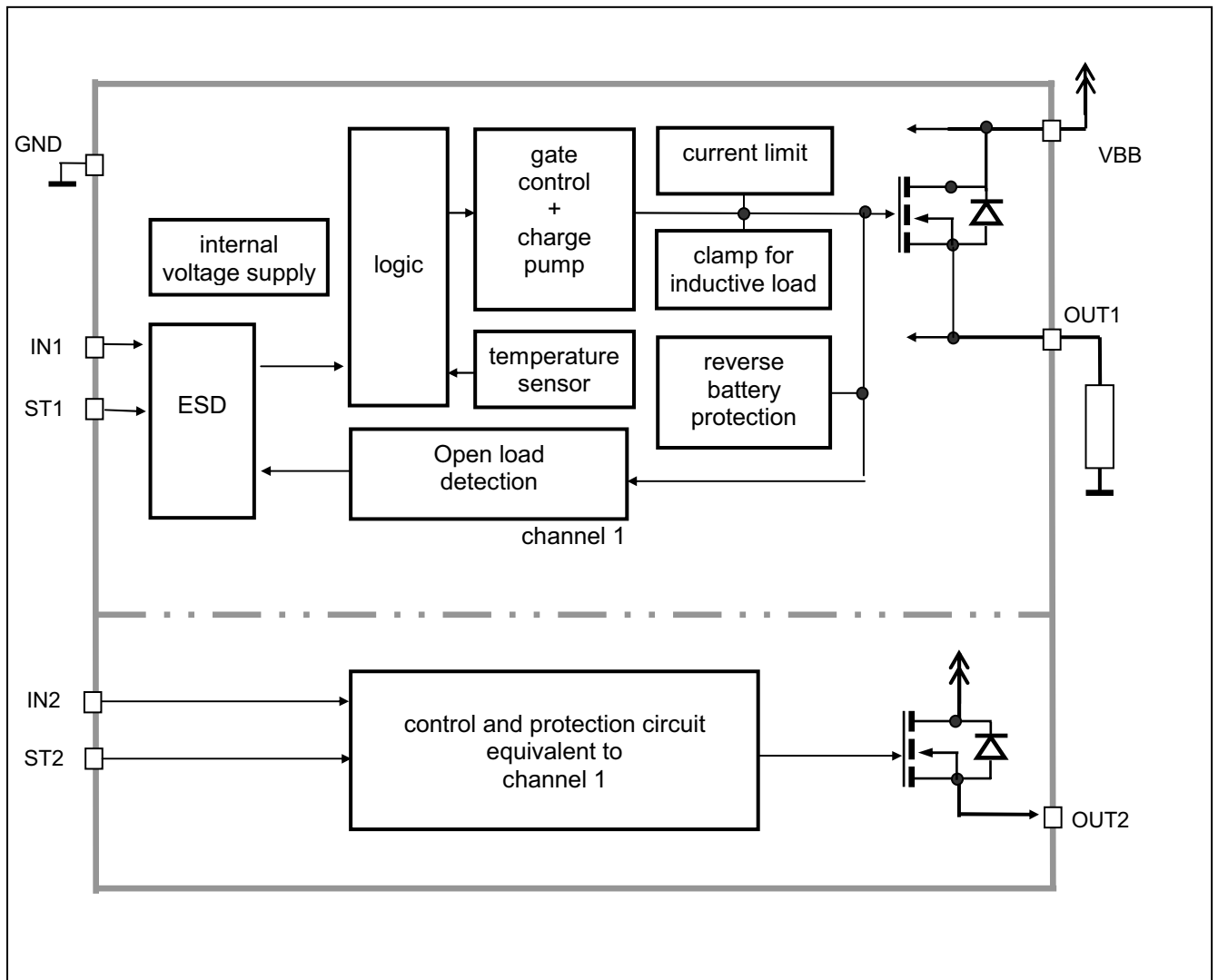


Functional diagram

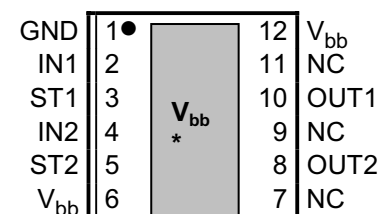


Pin Definitions and Functions

Pin	Symbol	Function
1	GND	Ground of chip
2	IN1	Input 1,2 activates channel 1,2 in case of logic high signal
4	IN2	
3	ST1	Diagnostic feedback 1 & 2 of channel 1,2 open drain, low on failure
5	ST2	
6,12, heat slug	V _{bb}	Positive power supply voltage. Design the wiring for the simultaneous max. short circuit currents from channel 1 to 2 and also for low thermal resistance
7,9,11	NC	Not Connected
8	OUT2	Output 1,2 protected high-side power output of channel 1 and 2. Design the wiring for the max. short circuit current
10	OUT1	

Pin configuration

(top view)



* heat slug

Maximum Ratings at $T_j = 25^\circ\text{C}$ unless otherwise specified

Parameter	Symbol	Values	Unit
Supply voltage (overvoltage protection see page 5)	V_{bb}	43	V
Supply voltage for full short circuit protection $T_{j,start} = -40 \dots +150^\circ\text{C}$	V_{bb}	36	V
Load current (Short-circuit current, see page 6)	I_L	self-limited	A
Load dump protection ¹⁾ $V_{LoadDump} = V_A + V_s$, $V_A = 13.5\text{ V}$ $R_l^{2)}$ = 2 Ω , $t_d = 400\text{ ms}$; IN = low or high, each channel loaded with $R_L = 13.5\text{ }\Omega$,	$V_{Load\ dump}^{3)}$	60	V
Junction temperature	T_j	+150	$^\circ\text{C}$
Operating temperature range	T_a	-30 ... +85	
Storage temperature range	T_{stg}	-40 ... +105	
Power dissipation (DC) ⁴⁾ $T_a = 25^\circ\text{C}$: (all channels active) $T_a = 85^\circ\text{C}$:	P_{tot}	3.1 1.6	W
Maximal switchable inductance, single pulse $V_{bb} = 12\text{V}$, $T_{j,start} = 150^\circ\text{C}^{4)}$, see diagrams on page 9 $I_L = 3.5\text{ A}$, $E_{AS} = 178\text{ mJ}$, 0 Ω one channel: $I_L = 7.0\text{ A}$, $E_{AS} = 337\text{ mJ}$, 0 Ω two parallel channels:	Z_L	21.3 10	mH
Electrostatic discharge capability (ESD) IN: (Human Body Model) ST: out to all other pins shorted: acc. MIL-STD883D, method 3015.7 and ESD assn. std. S5.1-1993 R=1.5k Ω ; C=100pF	V_{ESD}	1.0 4.0 8.0	kV
Input voltage (DC) see internal circuit diagram page 8	V_{IN}	-10 ... +16	V
Current through input pin (DC)	I_{IN}	± 0.3	mA
Pulsed current through input pin ⁵⁾	I_{INp}	± 5.0	
Current through status pin (DC)	I_{ST}	± 5.0	

1) Supply voltages higher than $V_{bb(AZ)}$ require an external current limit for the GND and status pins (a 150 Ω resistor for the GND connection is recommended).

2) R_l = internal resistance of the load dump test pulse generator

3) $V_{Load\ dump}$ is setup without the DUT connected to the generator per ISO 7637-1 and DIN 40839

4) Device on 50mm*50mm*1.5mm epoxy PCB FR4 with 6cm² (one layer, 70 μm thick) copper area for V_{bb} connection. PCB is vertical without blown air. See page 13

5) only for testing

Thermal Characteristics

Parameter and Conditions	Symbol	Values			Unit
		min	typ	max	
Thermal resistance junction - Case ⁶⁾ junction – ambient ⁶⁾ @ 6 cm ² cooling area	each channel: R_{thjC} R_{thja} one channel active: all channels active:	-- -- -- --	-- -- 45 40	5 -- -- --	K/W

Electrical Characteristics

Parameter and Conditions, each of the four channels at $T_j = -40...+150^{\circ}\text{C}$, $V_{bb} = 12\text{ V}$ unless otherwise specified	Symbol	Values			Unit
		min	typ	max	

Load Switching Capabilities and Characteristics

On-state resistance (V_{bb} to OUT); $I_L = 2\text{ A}$ each channel, $T_j = 25^{\circ}\text{C}$: $T_j = 150^{\circ}\text{C}$: two parallel channels, $T_j = 25^{\circ}\text{C}$: see diagram, page 10	R_{ON}	-- -- --	70 140 35	90 180 45	mΩ
Nominal load current one channel active: two parallel channels active: Device on PCB ⁶⁾ , $T_a = 85^{\circ}\text{C}$, $T_j \leq 150^{\circ}\text{C}$	$I_{L(NOM)}$	3.7 7.4	4.7 9.5	--	A
Output current while GND disconnected or pulled up ⁷⁾ ; $V_{bb} = 32\text{ V}$, $V_{IN} = 0$, see diagram page 8	$I_{L(GNDhigh)}$	--	--	2	mA
Turn-on time ⁸⁾ IN  to 90% V_{OUT} :	t_{on}	--	100	250	μs
Turn-off time IN  to 10% V_{OUT} : $R_L = 12\text{ Ω}$	t_{off}	--	100	270	μs
Slew rate on ⁸⁾ 10 to 30% V_{OUT} , $R_L = 12\text{ Ω}$:	dV/dt_{on}	0.2	--	1.0	V/μs
Slew rate off ⁸⁾ 70 to 40% V_{OUT} , $R_L = 12\text{ Ω}$:	$-dV/dt_{off}$	0.2	--	1.1	V/μs

⁶⁾ Device on 50mm*50mm*1.5mm epoxy PCB FR4 with 6cm² (one layer, 70μm thick) copper area for V_{bb} connection. PCB is vertical without blown air. See page 13

⁷⁾ not subject to production test, specified by design

⁸⁾ See timing diagram on page 11.

Parameter and Conditions, each of the four channels at T _j = -40...+150°C, V _{bb} = 12 V unless otherwise specified	Symbol	Values			Unit
		min	typ	max	
Operating Parameters					
Operating voltage	V _{bb(on)}	5.5	--	40	V
Undervoltage switch off ⁹⁾ T _j =-40°C...25°C:	V _{bb(u so)}	--	--	4.5	V
T _j =125°C:		--	--	4.5 ¹⁰⁾	
Overvoltage protection ¹¹⁾ I _{bb} = 40 mA	V _{bb(AZ)}	41	47	52	V
Standby current ¹²⁾ T _j =-40°C...25°C:	I _{bb(off)}	--	4.5	10	μA
V _{IN} = 0; see diagram page 11 T _j =150°C:		--	--	15	
T _j =125°C:		--	--	10 ¹⁰⁾	
Off-State output current (included in I _{bb(off)}) V _{IN} = 0; each channel	I _{L(off)}	--	1	5	μA
Operating current ¹³⁾ , V _{IN} = 5V, one channel on: all channels on:	I _{GND}	-- --	0.6 1.2	1.2 2.4	mA
Protection Functions ¹⁴⁾					
Current limit, V _{out} = 0V, (see timing diagrams, page 11) T _j =-40°C: T _j =25°C: T _j =+150°C:	I _{L(lim)}	-- -- 9	-- 15 --	23 ----	A
Repetitive short circuit current limit, T _j = T _{jt} each channel two channels (see timing diagrams, page 11)	I _{L(SCr)}	-- --	12 12	-- --	A
Initial short circuit shutdown time T _{j,start} =25°C: V _{out} = 0V (see timing diagrams on page 11)	t _{off(SC)}	--	2	--	ms
Output clamp (inductive load switch off) ¹⁵⁾ at V _{ON(CL)} = V _{bb} - V _{OUT} , I _L = 40 mA	V _{ON(CL)}	41	47	52	V
Thermal overload trip temperature	T _{jt}	150	--	--	°C
Thermal hysteresis	ΔT _{jt}	--	10	--	K

9) is the voltage, where the device doesn't change it's switching condition for 15ms after the supply voltage falling below the lower limit of $V_{bb(\text{on})}$

10) not subject to production test, specified by design

11) Supply voltages higher than $V_{bb(\text{AZ})}$ require an external current limit for the GND and status pins (a 150 Ω resistor for the GND connection is recommended). See also $V_{\text{ON}(\text{CL})}$ in table of protection functions and circuit diagram on page 8.

12) Measured with load; for the whole device; all channels off

13) Add I_{ST} , if $I_{\text{ST}} > 0$

14) Integrated protection functions are designed to prevent IC destruction under fault conditions described in the data sheet. Fault conditions are considered as "outside" normal operating range. Protection functions are not designed for continuous repetitive operation.

15) If channels are connected in parallel, output clamp is usually accomplished by the channel with the lowest $V_{\text{ON}(\text{CL})}$

Parameter and Conditions, each of the four channels at $T_j = -40...+150^{\circ}\text{C}$, $V_{bb} = 12\text{ V}$ unless otherwise specified	Symbol	Values			Unit
		min	typ	max	

Reverse Battery

Reverse battery voltage ¹⁶⁾	$-V_{bb}$	--	--	32	V
Drain-source diode voltage ($V_{out} > V_{bb}$) $I_L = -2.0\text{ A}$, $T_j = +150^{\circ}\text{C}$	$-V_{ON}$	--	600	--	mV

Diagnostic Characteristics

Open load detection voltage	$V_{OUT(OL)}$	1.7	2.8	4.0	V
-----------------------------	---------------	-----	-----	-----	---

Input and Status Feedback¹⁷⁾

Input resistance (see circuit page 8)	R_I	2.5	4.0	6.0	k Ω
Input turn-on threshold voltage 	$V_{IN(T+)}$	--	--	2.5	V
Input turn-off threshold voltage 	$V_{IN(T-)}$	1.0	--	--	V
Input threshold hysteresis	$\Delta V_{IN(T)}$	--	0.2	--	V
Status change after positive input slope ¹⁸⁾ with open load	$t_{d(STon)}$	--	10	20	μs
Status change after positive input slope ¹⁸⁾ with overload	$t_{d(STon)}$	30	--	--	μs
Status change after negative input slope with open load	$t_{d(SToff)}$	--	--	500	μs
Status change after negative input slope ¹⁸⁾ with overtemperature	$t_{d(SToff)}$	--	--	20	μs
Off state input current $V_{IN} = 0.4\text{ V}$:	$I_{IN(off)}$	5	--	20	μA
On state input current $V_{IN} = 5\text{ V}$:	$I_{IN(on)}$	10	35	60	μA
Status output (open drain)					
Zener limit voltage $I_{ST} = +1.6\text{ mA}$:	$V_{ST(high)}$	5.4	--	--	V
ST low voltage $I_{ST} = +1.6\text{ mA}$:	$V_{ST(low)}$	--	--	0.6	

¹⁶⁾ Requires a 150 Ω resistor in GND connection. The reverse load current through the intrinsic drain-source diode has to be limited by the connected load. Power dissipation is higher compared to normal operating conditions due to the voltage drop across the drain-source diode. The temperature protection is not active during reverse current operation! Input and Status currents have to be limited (see max. ratings page 3 and circuit page 8).

¹⁷⁾ If ground resistors R_{GND} are used, add the voltage drop across these resistors.

¹⁸⁾ not subject to production test, specified by design

Truth Table

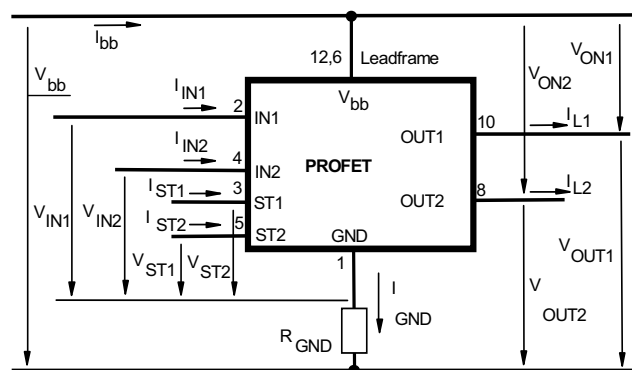
(each channel)

	IN	OUT	ST
Normal operation	L	L	H
	H	H	H
Open load	L	Z	L ¹⁹⁾
	H	H	H
Overtemperature	L	L	H
	H	L	L

L = "Low" Level X = don't care Z = high impedance, potential depends on external circuit
H = "High" Level Status signal valid after the time delay shown in the timing diagrams

Parallel switching of channel 1 and 2 is easily possible by connecting the inputs and outputs in parallel (see truth table). If switching channel 1 to 2 in parallel, the status outputs ST1 and ST2 have to be configured as a 'Wired OR' function with a single pull-up resistor.

Terms

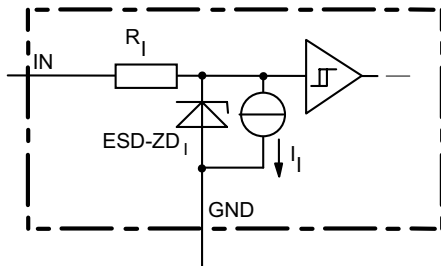


Leadframe (V_{bb}) is connected to pin 6,12

External R_{GND} optional; single resistor $R_{GND} = 150 \Omega$ for reverse battery protection up to the max. operating voltage.

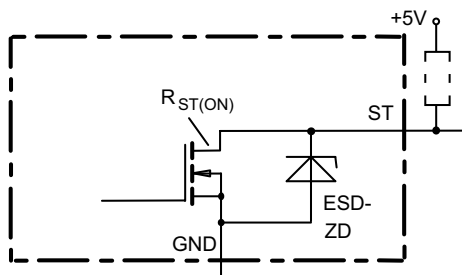
¹⁹⁾ L, if potential at the Output exceeds the OpenLoad detection voltage

Input circuit (ESD protection), IN1 or IN2



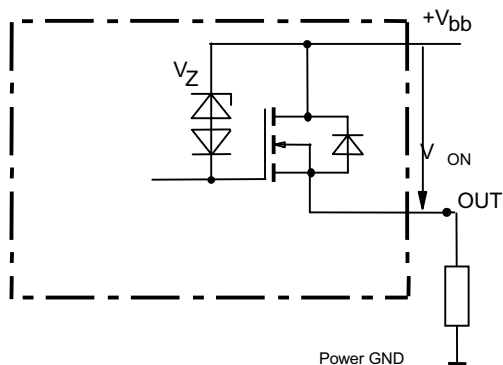
The use of ESD zener diodes as voltage clamp at DC conditions is not recommended.

Status output, ST1 or ST2



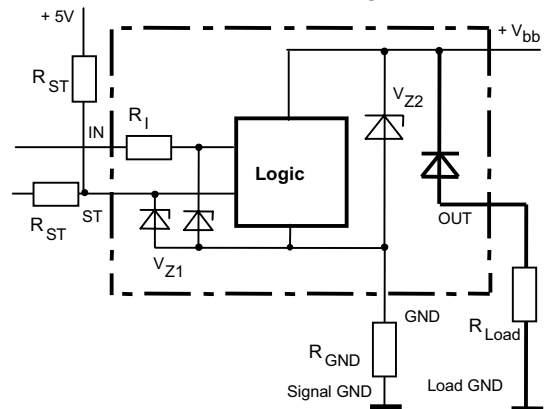
ESD-Zener diode: 6.1 V typ., max 0.3 mA; $R_{ST(ON)} < 375 \Omega$ at 1.6 mA. The use of ESD zener diodes as voltage clamp at DC conditions is not recommended.

Inductive and overvoltage output clamp, OUT1 or OUT2



V_{ON} clamped to $V_{ON(CL)} = 47 \text{ V typ.}$

Overvolt. and reverse batt. protection

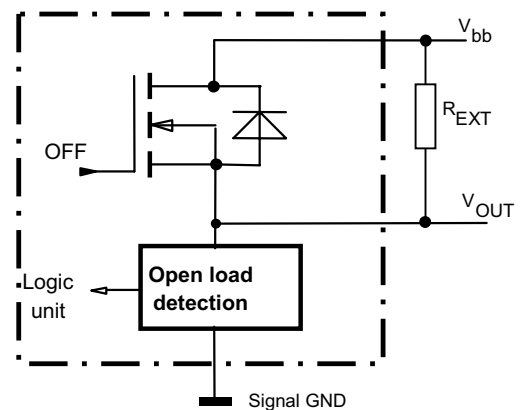


$V_{Z1} = 6.1 \text{ V typ.}$, $V_{Z2} = 47 \text{ V typ.}$, $R_{GND} = 150 \Omega$, $R_{ST} = 15 \text{ k}\Omega$, $R_I = 3.5 \text{ k}\Omega \text{ typ.}$
In case of reverse battery the load current has to be limited by the load. Temperature protection is not active

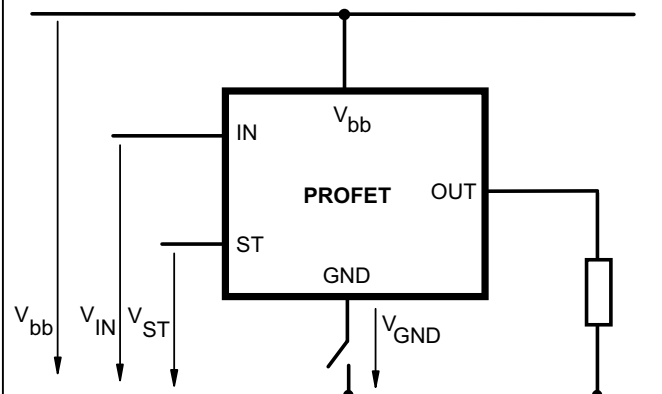
Open-load detection, OUT1 or OUT2

OFF-state diagnostic condition:

Open Load, if $V_{OUT} > 3 \text{ V typ.}$; IN low

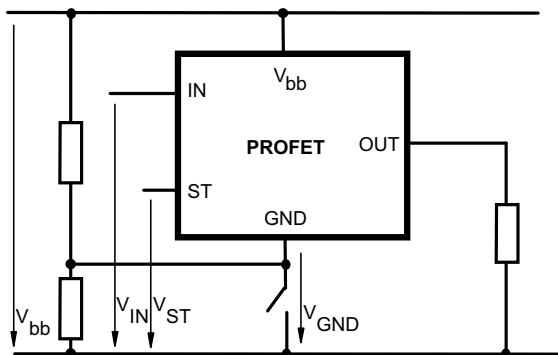


GND disconnect



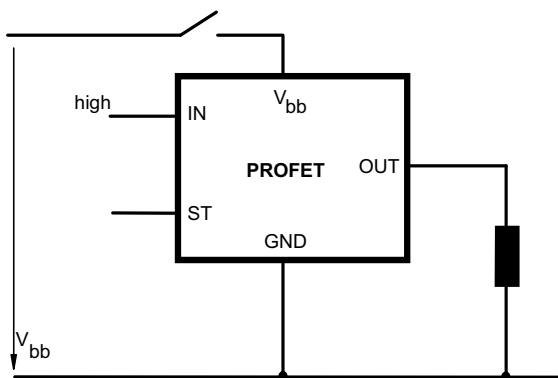
Any kind of load. In case of IN = high is $V_{OUT} \approx V_{IN} - V_{IN(T+)}$. Due to $V_{GND} > 0$, no $V_{ST} = \text{low signal}$ available.

GND disconnect with GND pull up



Any kind of load. If $V_{GND} > V_{IN} - V_{IN(T+)}$ device stays off
Due to $V_{GND} > 0$, no V_{ST} = low signal available.

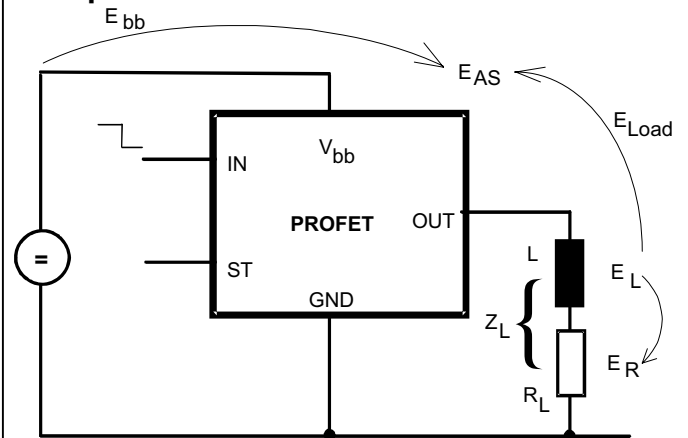
Vbb disconnect with energized inductive load



For inductive load currents up to the limits defined by Z_L (max. ratings and diagram on page 9) each switch is protected against loss of V_{bb} .

Consider at your PCB layout that in the case of V_{bb} disconnection with energized inductive load all the load current flows through the GND connection.

Inductive load switch-off energy dissipation



Energy stored in load inductance:

$$E_L = \frac{1}{2} \cdot L \cdot I_L^2$$

While demagnetizing load inductance, the energy dissipated in PROFET is

$$E_{AS} = E_{bb} + E_L - E_R = \int V_{ON(CL)} \cdot i_L(t) dt,$$

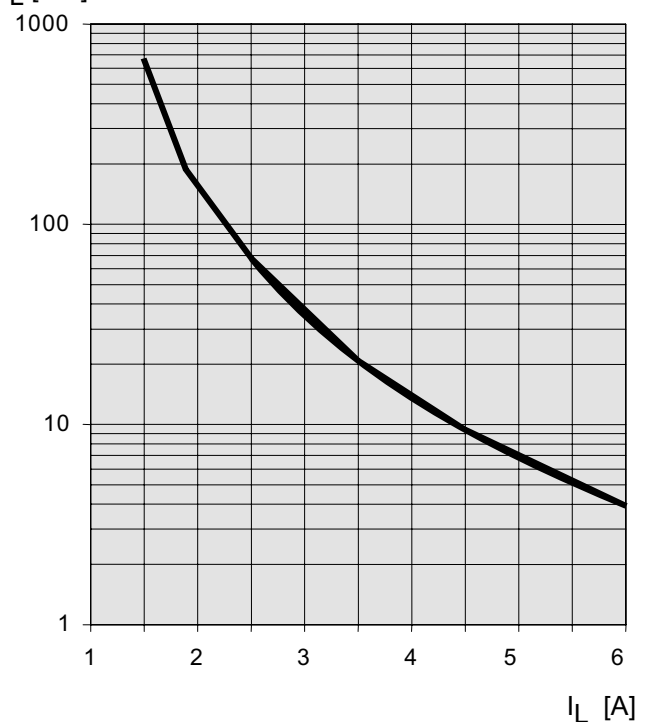
with an approximate solution for $R_L > 0 \Omega$:

$$E_{AS} = \frac{I_L \cdot L}{2 \cdot R_L} (V_{bb} + |V_{OUT(CL)}|) \ln \left(1 + \frac{I_L \cdot R_L}{|V_{OUT(CL)}|} \right)$$

Maximum allowable load inductance for a single switch off (one channel)⁴⁾

$L = f(I_L)$; $T_{j,start} = 150^\circ\text{C}$, $V_{bb} = 12\text{ V}$, $R_L = 0 \Omega$

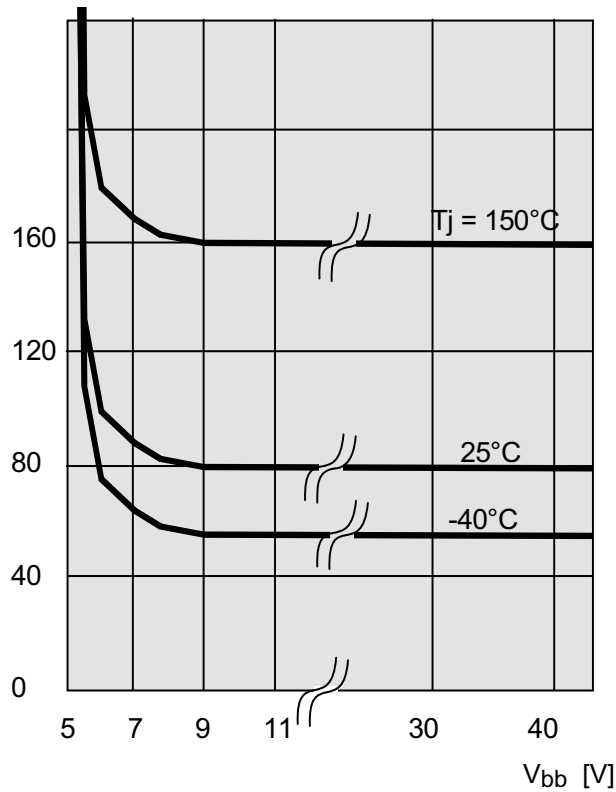
Z_L [mH]



Typ. on-state resistance

$R_{ON} = f(V_{bb}, T_j)$; $I_L = 2\text{ A}$, $I_N = \text{high}$

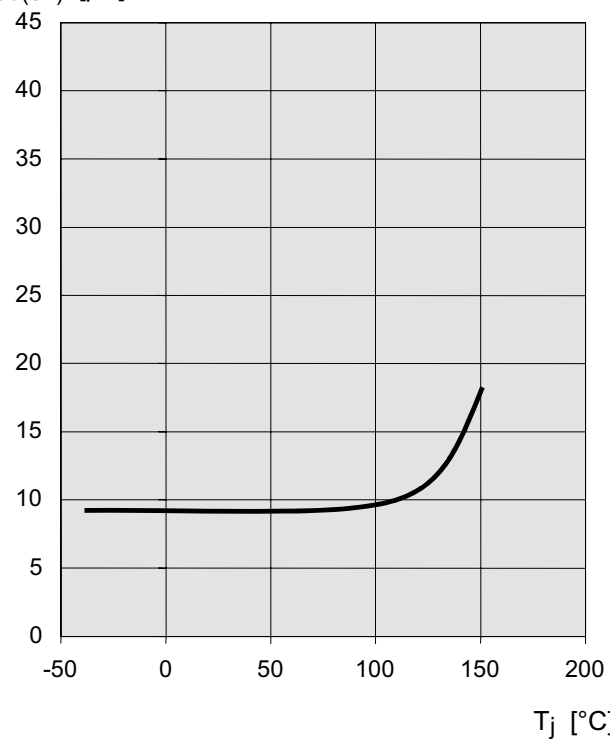
R_{ON} [mOhm]



Typ. standby current

$I_{bb(off)} = f(T_j)$; $V_{bb} = 9\text{...}34\text{ V}$, $I_{N1,2} = \text{low}$

$I_{bb(off)}$ [μA]



Timing diagrams

Both channels are symmetric and consequently the diagrams are valid for channel 1 and channel 2

Figure 1a: V_{bb} turn on:

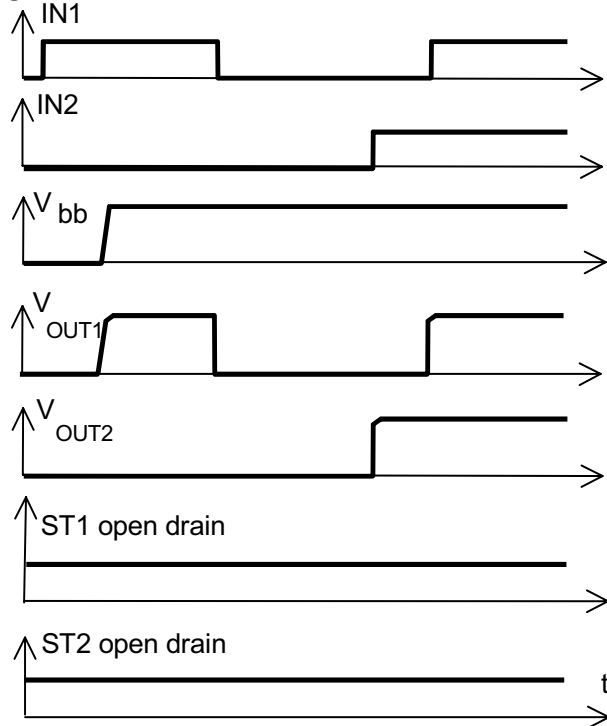


Figure 2a: Switching a resistive load, turn-on/off time and slew rate definition:

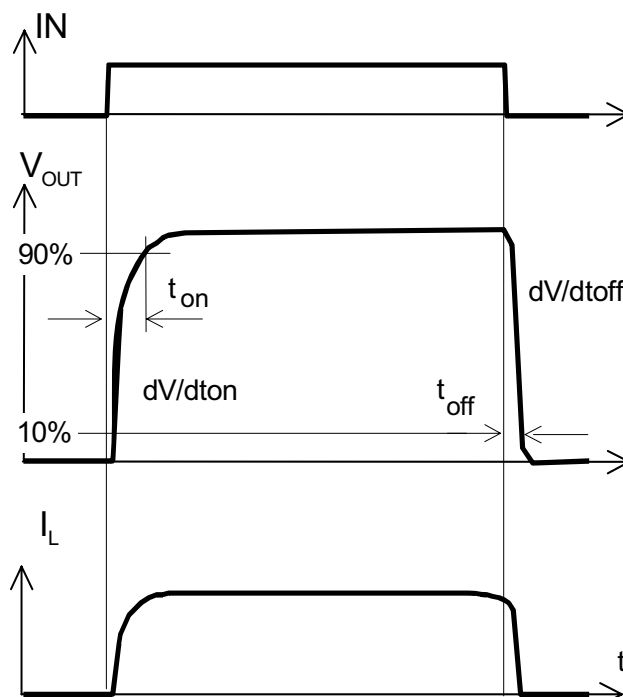


Figure 2b: Switching a lamp:

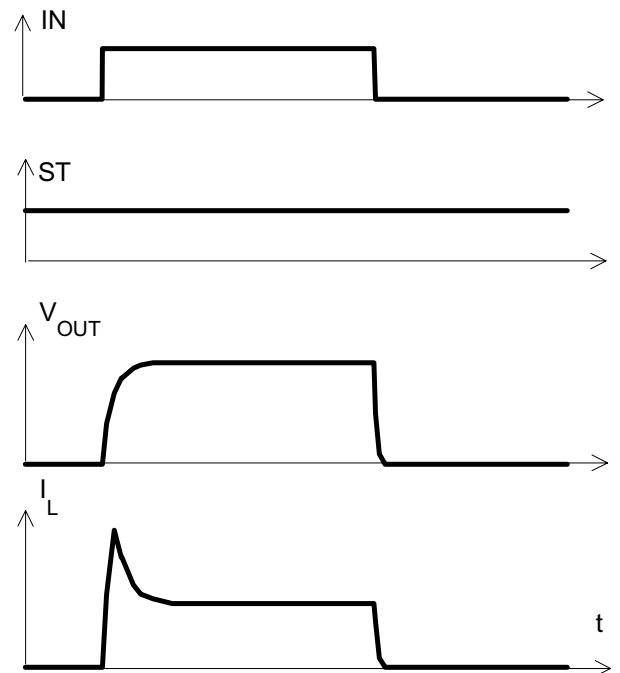
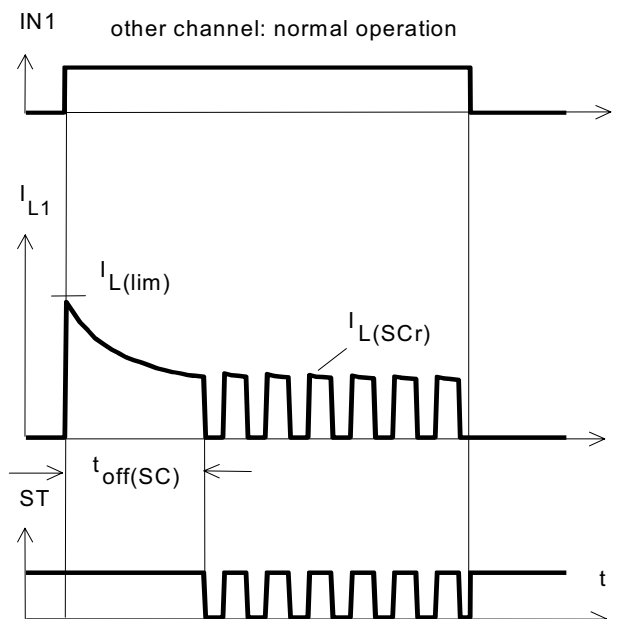
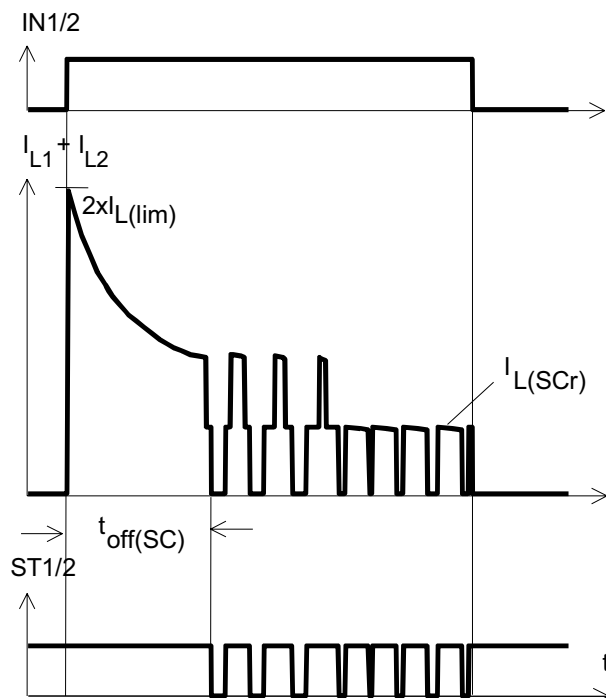


Figure 3a: Turn on into short circuit: shut down by overtemperature, restart by cooling



Heating up of the chip may require several milliseconds, depending on external conditions

Figure 3b: Turn on into short circuit:
shut down by overtemperature, restart by cooling
(two parallel switched channels 1 and 2)



ST1 and ST2 have to be configured as a 'Wired OR' function
ST1/2 with a single pull-up resistor.

Figure 4a: Overtemperature:
Reset if $T_j < T_{jt}$

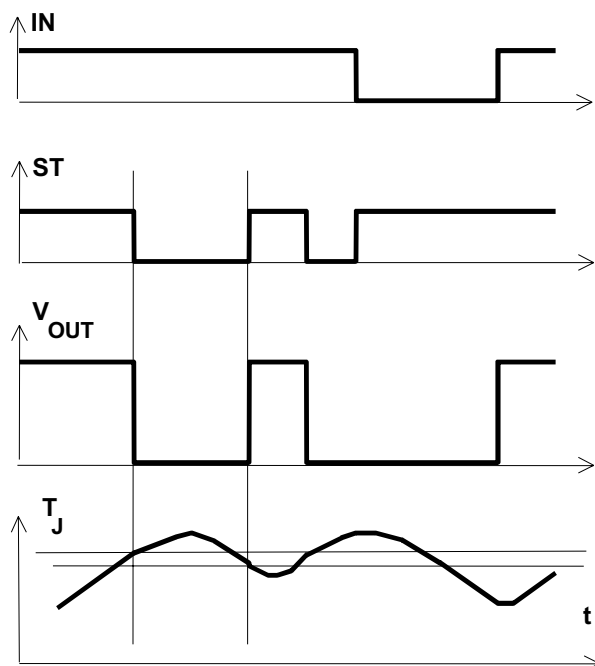


Figure 5a: Open load: detection in OFF-state, turn
on/off to open load
Open load of channel 1; other channels normal
operation

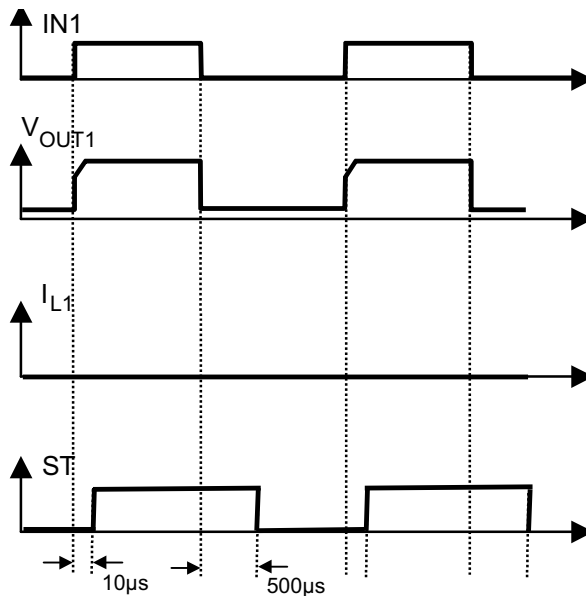
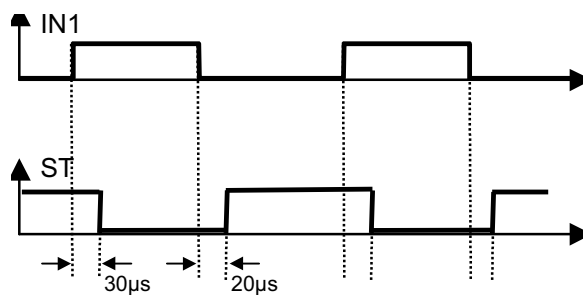


Figure 6a: Status change after, turn on/off to
overtemperature
Overtemperature of channel 1; other channels normal
operation



1 Package Outlines

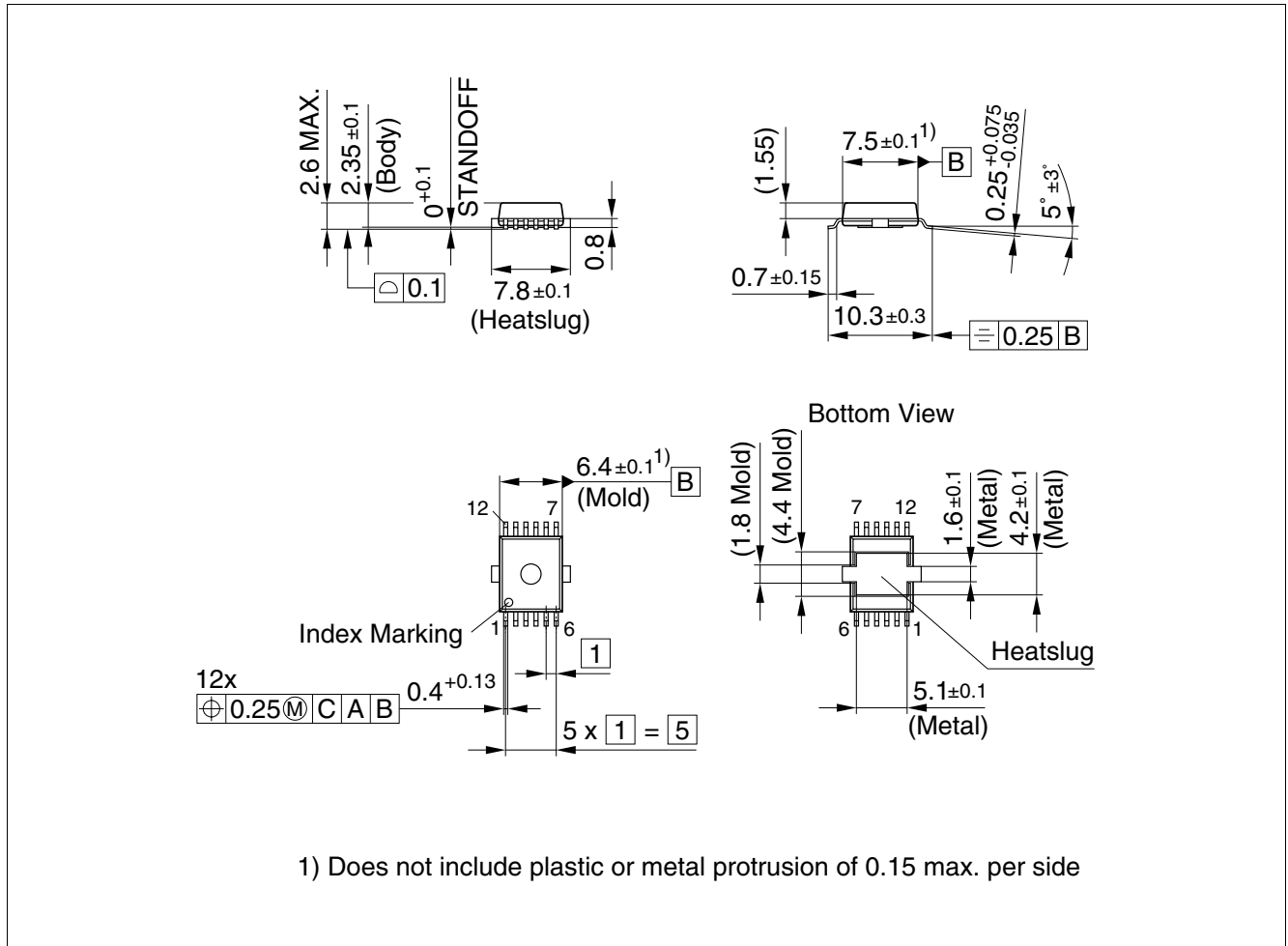


Figure 1 PG-DSO-12-9

Green Product (RoHS compliant)

To meet the world-wide customer requirements for environmentally friendly products and to be compliant with government regulations the device is available as a green product. Green products are RoHS-Compliant (i.e Pb-free finish on leads and suitable for Pb-free soldering according to IPC/JEDEC J-STD-020).

For further information on alternative packages, please visit our website:

<http://www.infineon.com/packages>.

Dimensions in mm

2 Revision History

Revision	Date	Changes
1.1	2008-10-07	RoHS-compliant DSO package version of the ITS724G All pages: Infineon logo updated Page 1: Added "RoHS" logo, added "Green Product (RoHS compliant)" statement to feature list, package names changed to RoHS compliant versions. Page 14: Package names changed to RoHS compliant versions, added "Green Product" description page 15: added Revision History page 16: added Legal Disclaimer

Edition 2008-10-07

**Published by
Infineon Technologies AG
81726 Munich, Germany**

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