

**Maximum Ratings** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V <sub>DSS</sub>	-12	V
Gate-Source Voltage			V <sub>GSS</sub>	±8	V
Continuous Drain Current V <sub>GS</sub> = -4.5V (Note 6)	Steady State	T <sub>A</sub> = +25°C T <sub>A</sub> = +70°C	I <sub>D</sub>	-5.5 -4.3	A
	t < 5s	T <sub>A</sub> = +25°C T <sub>A</sub> = +70°C		-6.5 -5.1	A
Maximum Continuous Body Diode Forward Current (Note 6)			I <sub>S</sub>	-2.2	A
Pulsed Drain Current (10μs pulse, duty cycle = 1%)			I <sub>DM</sub>	-25	A

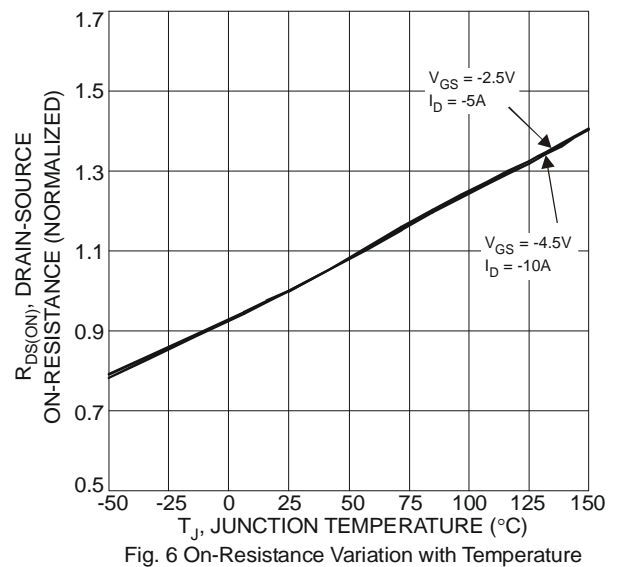
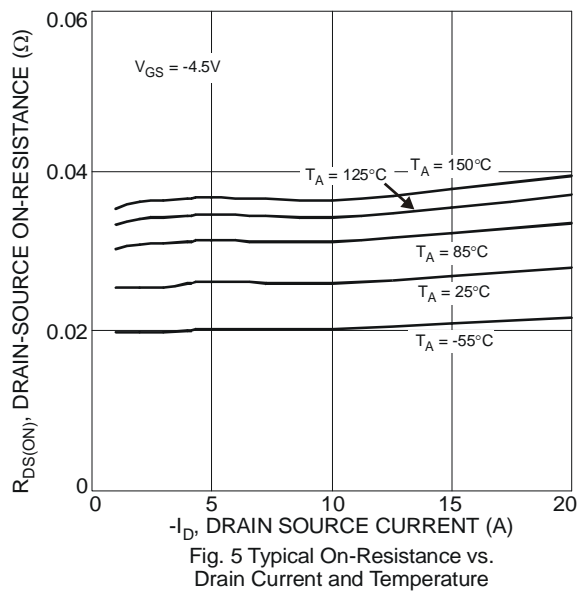
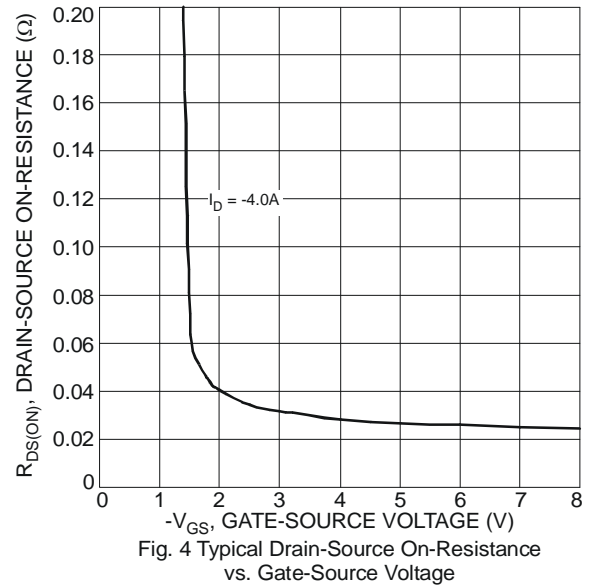
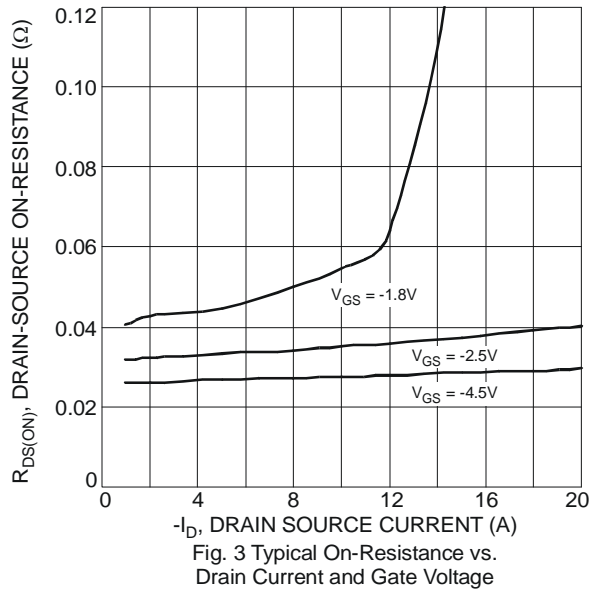
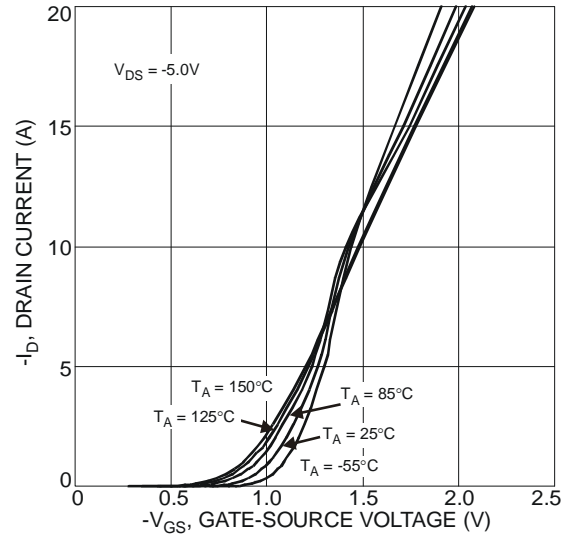
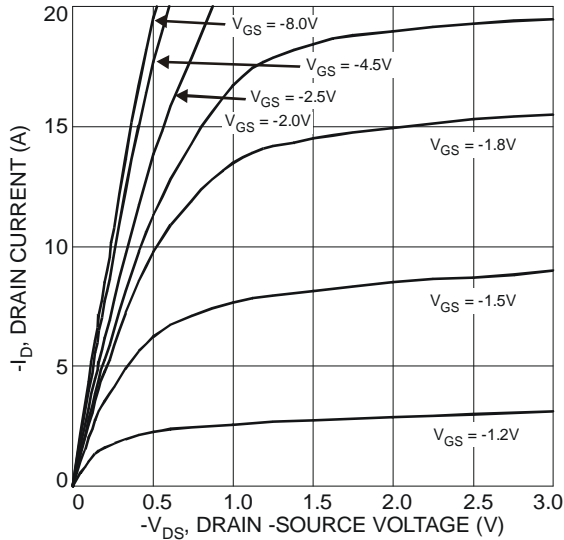
**Thermal Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Power Dissipation (Note 5)	T <sub>A</sub> = +25°C	P <sub>D</sub>	0.7	W
	T <sub>A</sub> = +70°C		0.4	
Thermal Resistance, Junction to Ambient (Note 5)	Steady state	R <sub>θJA</sub>	193	°C/W
	t < 5s		135	
Power Dissipation (Note 6)	T <sub>A</sub> = +25°C	P <sub>D</sub>	1.7	W
	T <sub>A</sub> = +70°C		1.1	
Thermal Resistance, Junction to Ambient (Note 6)	Steady state	R <sub>θJA</sub>	73	°C/W
	t < 5s		52	
Thermal Resistance, Junction to Case (Notes 6)	Steady state	R <sub>θJC</sub>	17	
Operating and Storage Temperature Range		T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

**Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
<b>OFF CHARACTERISTICS (Note 7)</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	-12	-	-	V	V <sub>GS</sub> = 0V, I <sub>D</sub> = -250μA
Zero Gate Voltage Drain Current T <sub>J</sub> = 25°C	I <sub>DSS</sub>	-	-	-1.0	μA	V <sub>DS</sub> = -12V, V <sub>GS</sub> = 0V
Gate-Source Leakage	I <sub>GSS</sub>	-	-	±10	μA	V <sub>GS</sub> = ±8V, V <sub>DS</sub> = 0V
<b>ON CHARACTERISTICS (Note 7)</b>						
Gate Threshold Voltage	V <sub>GS(th)</sub>	-0.3	-0.55	-1.0	V	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = -250μA
Static Drain-Source On-Resistance	R <sub>DS(on)</sub>	-	26	32	mΩ	V <sub>GS</sub> = -4.5V, I <sub>D</sub> = -4.0A
			31	45		V <sub>GS</sub> = -2.5V, I <sub>D</sub> = -3.5A
			51	75		V <sub>GS</sub> = -1.8V, I <sub>D</sub> = -2.7A
Forward Transfer Admittance	Y <sub>fs</sub>	-	12	-	S	V <sub>DS</sub> = -5V, I <sub>D</sub> = -4A
Diode Forward Voltage	V <sub>SD</sub>	-	-0.6	-	V	V <sub>GS</sub> = 0V, I <sub>S</sub> = -1A
<b>DYNAMIC CHARACTERISTICS (Note 8)</b>						
Input Capacitance	C <sub>iss</sub>	-	1291	-	pF	V <sub>DS</sub> = -10V, V <sub>GS</sub> = 0V f = 1.0MHz
Output Capacitance	C <sub>oss</sub>	-	266	-	pF	
Reverse Transfer Capacitance	C <sub>rss</sub>	-	242	-	pF	
Gate Resistnace	R <sub>g</sub>	-	13	-	Ω	V <sub>DS</sub> = 0V, V <sub>GS</sub> = 0V, f = 1.0MHz
<b>SWITCHING CHARACTERISTICS (Note 8)</b>						
Total Gate Charge (V <sub>GS</sub> = -8V)	Q <sub>g</sub>	-	23.7	-	nC	V <sub>DS</sub> = -10V, I <sub>D</sub> = -4A
Total Gate Charge (V <sub>GS</sub> = -4.5V)	Q <sub>g</sub>	-	14.7	-	nC	
Gate-Source Charge	Q <sub>gs</sub>	-	1.8	-	nC	
Gate-Drain Charge	Q <sub>gd</sub>	-	4.6	-	nC	
Turn-On Delay Time	t <sub>D(on)</sub>	-	14	-	ns	V <sub>DS</sub> = -10V, V <sub>GS</sub> = -4.5V, R <sub>L</sub> = 2.5Ω, R <sub>G</sub> = 3.0Ω
Turn-On Rise Time	t <sub>r</sub>	-	22	-	ns	
Turn-Off Delay Time	t <sub>D(off)</sub>	-	74	-	ns	
Turn-Off Fall Time	t <sub>f</sub>	-	75	-	ns	

- Notes:
- Device mounted on FR-4 PCB with minimum recommended pad layout, single sided.
  - Device mounted on 1" x 1" FR-4 PCB with high coverage 2oz. Copper, single sided.
  - Short duration pulse test used to minimize self-heating effect.
  - Guaranteed by design. Not subject to production testing.



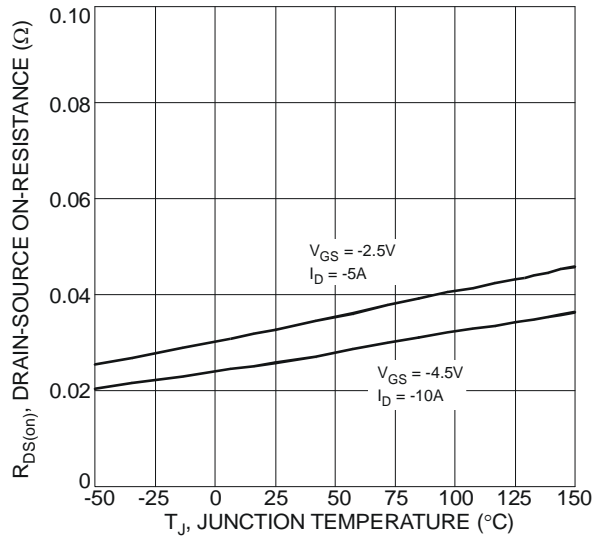


Fig. 7 On-Resistance Variation with Temperature

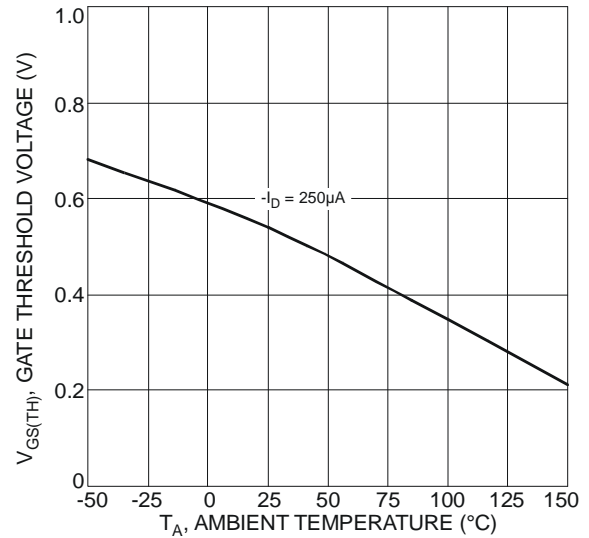


Fig. 8 Gate Threshold Variation vs. Ambient Temperature

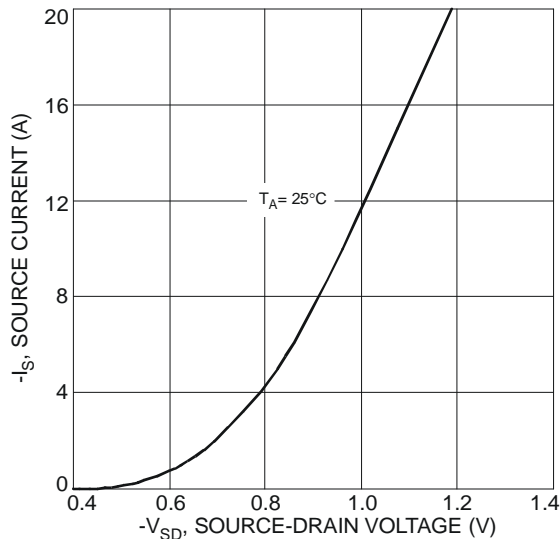


Fig. 9 Diode Forward Voltage vs. Current

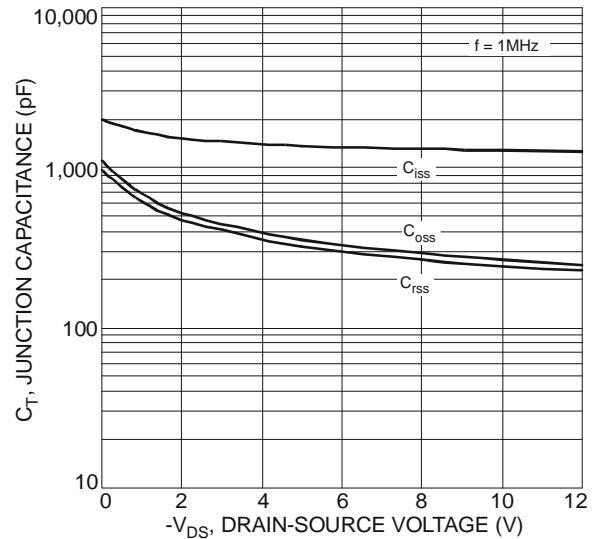


Fig. 10 Typical Junction Capacitance

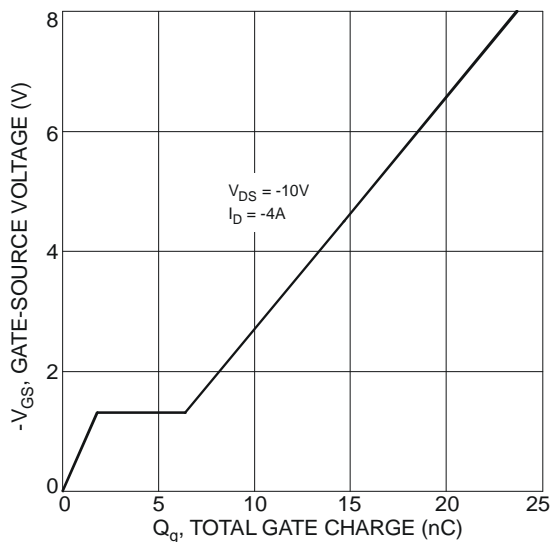


Fig. 11 Gate-Charge Characteristics

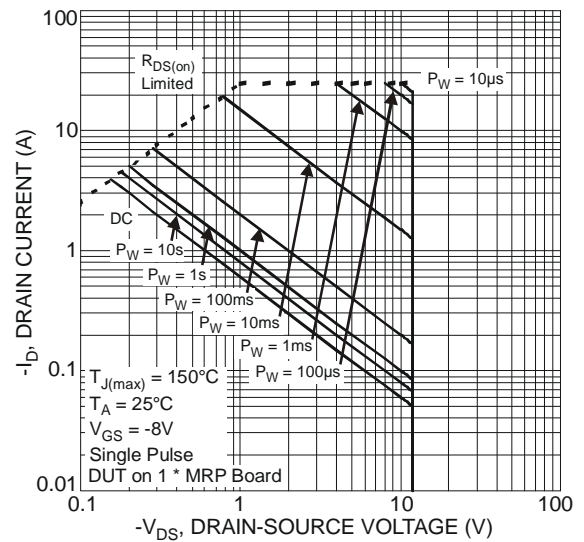
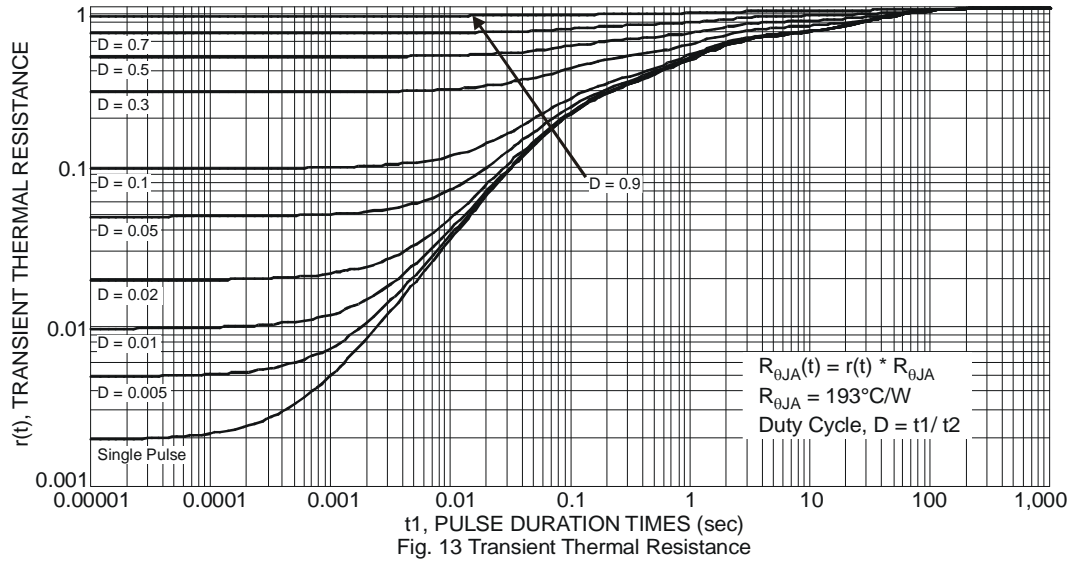
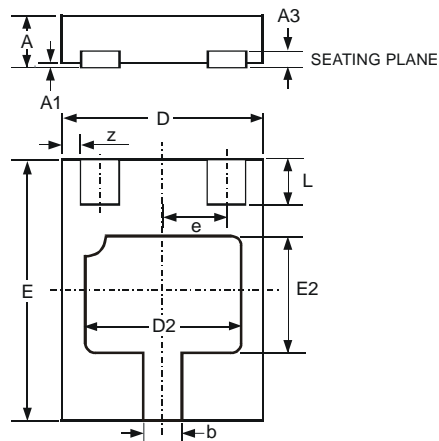


Fig. 12 SOA, Safe Operation Area



## Package Outline Dimensions

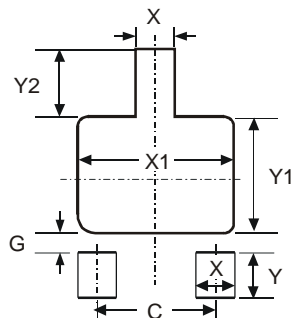
Please see AP02002 at <http://www.diodes.com/datasheets/ap02002.pdf> for latest version.



X2-DFN2015-3			
Dim	Min	Max	Typ
A	—	0.40	—
A1	0	0.05	0.02
A3	—	—	0.13
b	0.20	0.30	0.25
D	1.45	1.575	1.50
D2	1.00	1.20	1.10
e	—	—	0.50
E	1.95	2.075	2.00
E2	0.70	0.90	0.80
L	0.25	0.35	0.30
z	—	—	0.125
All Dimensions in mm			

## Suggested Pad Layout

Please see AP02001 at <http://www.diodes.com/datasheets/ap02001.pdf> for the latest version.



Dimensions	Value (in mm)
C	1.00
G	0.15
X	0.31
X1	1.30
Y	0.50
Y1	1.00
Y2	0.65

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