

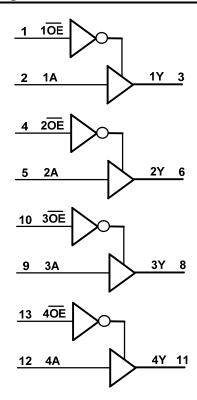
## **Pin Descriptions**

Pin Number	Pin Name	Function			
1	10E	Data Enable Input (active low)			
2	1A	Data Input			
3	1Y	Data Output			
4	2 <del>OE</del>	Data Enable Input (active low)			
5	2A	Data Input			
6	2Y	Data Output			
7	GND	Ground			
8	3Y	Data Output			
9	3A	Data Input			
10	3OE	Data Enable Input (active low)			
11	4Y	Data Output			
12	4A	Data Input			
13	4 <del>0E</del>	Data Enable Input (active low)			
14	V <sub>CC</sub>	Supply Voltage			

## **Function Table**

Inp	Output	
ŌĒ	Α	Y
L	Н	Н
L	L	L
Н	X	Z

# **Logic Diagram**



## Absolute Maximum Ratings (Note 4) (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Symbol	Description	Rating	Unit
ESD HBM	Human Body Model ESD Protection	2	KV
ESD CDM	Charged Device Model ESD Protection	1	KV
ESD MM	Machine Model ESD Protection	200	V
V <sub>CC</sub>	Supply Voltage Range	-0.5 to +7.0	V
Vı	Input Voltage Range	-0.5 to +7.0	V
I <sub>IK</sub>	Input Clamp Current V <sub>I</sub> < -0.5V	-20	mA
I <sub>OK</sub>	Output Clamp Current V <sub>O</sub> < -0.5V	-20	mA
I <sub>OK</sub>	Output Clamp Current V <sub>O</sub> > V <sub>CC</sub> + 0.5V	25	mA
lo	Continuous Output Current -0.5V < V <sub>O</sub> V <sub>CC</sub> +0.5V	+/- 25	mA
Icc	Continuous Current Through V <sub>CC</sub>	75	mA
I <sub>GND</sub>	Continuous Current Through GND	-75	mA
TJ	Operating Junction Temperature	-40 to +150	°C
T <sub>STG</sub>	Storage Temperature	-65 to +150	°C
P <sub>TOT</sub>	Total Power Dissipation	500	mW

Note: 4. Stresses beyond the absolute maximum may result in immediate failure or reduced reliability. These are stress values and device operation should be within recommend values.



## Recommended Operating Conditions (Note 5) (@TA = +25°C, unless otherwise specified.)

Symbol	Parameter	Conditions	Min	Max	Unit
V <sub>CC</sub>	Supply Voltage		2.0	5.5	V
VI	Input Voltage		0	5.5	V
Vo	Output Voltage		0	Vcc	V
Λ+/Λ\ <i>/</i>	Input Transition Dies or Fall Date	V <sub>CC</sub> = 3.0V to 3.6V		100	no/\/
Δt/ΔV	Input Transition Rise or Fall Rate	V <sub>CC</sub> = 4.5V to 5.5V		20	ns/V
T <sub>A</sub>	Operating Free-Air Temperature		-40	+125	°C

Note: 5. Unused inputs should be held at  $V_{\text{CC}}$  or Ground.

# **Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Comple ed	Parameter	Test Conditions	v	T <sub>A</sub> = -40°C to +85°C		T <sub>A</sub> = -40°C to +125°C		Unit
Symbol	Parameter	rest Conditions	V <sub>CC</sub>	Min	Max	Min	Max	Unit
			2.0V	1.5		1.5		
$V_{IH}$	High-Level Input Voltage		3.0V	2.1		2.1		V
			5.5V	3.85		3.85		
			2.0V		0.5		0.5	
$V_{IL}$	Low-Level Input Voltage		3.0V		0.9		0.9	V
			5.5V		1.65		1.65	
		I <sub>OH</sub> = -50μA	2.0V	1.9		1.9		
	High-Level Output Voltage	I <sub>OH</sub> = -50μA	3.0V	2.9		2.9		V
$V_{OH}$		I <sub>OH</sub> = -50μA	4.5V	4.4		4.4		
		I <sub>OH</sub> = -4mA	3.0V	2.48		2.40		
		I <sub>OH</sub> = -8mA	4.5V	3.80		3.70		
		I <sub>OL</sub> = 50μA	2.0V		0.1		0.1	V
		I <sub>OL</sub> = 50μA	3.0V		0.1		0.1	
$V_{OL}$	Low-Level Output Voltage	I <sub>OL</sub> = 50μA	4.5V		0.1		0.1	
		I <sub>OL</sub> = 4mA	3.0V		0.44		0.55	
		I <sub>OL</sub> = 8mA	4.5V		0.44		0.55	
l <sub>OZ</sub>	Z State Leakage Current	$V_{O} = 0 \text{ to } 5.5V$ $V_{I} = \text{GND or } 5.5V$	5.5V		±2.5		±10	μΑ
lı	Input Current	V <sub>I</sub> = GND to 5.5V	3.6V		±1		±2	μA
Icc	Supply Current	$V_I = GND \text{ or } V_{CC}, I_O = 0$	3.6V		20		40	μΑ



# **Operating Characteristics**

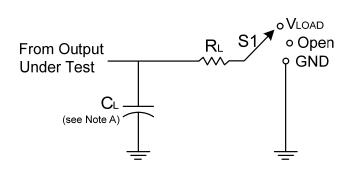
	Parameter	Test Conditions	V <sub>CC</sub> = 2.0V Typ	V <sub>CC</sub> = 3.3V Typ	V <sub>CC</sub> = 5V Typ	Unit
C <sub>pd</sub>	Power Dissipation Capacitance per Gate	f = 1MHz	10.1	13.1	15	pF
Ci	Input Capacitance	$V_i = V_{CC} - \text{or GND}$	4.0	4.0	4.0	pF

# **Switching Characteristics**

Comple ed	Domenication	Test	Test		T <sub>A</sub> = +25°(	C	-40°C to	+85°C	-40°C t	o +125°C	11:4:4		
Symbol	Parameter	Conditions	Vcc	Min	Тур	Max	Min	Max	Min	Max	Unit		
		Figure 1	3.0V to 3.6V	0.5	4.4	8.0	0.5	9.5	0.5	11.5			
	Propagation	$C_L = 15pF$	4.5V to 5.5V	0.5	3.0	5.5	0.5	6.5	0.5	7.0			
t <sub>PD</sub>	Delay A <sub>N</sub> to Y <sub>N</sub>	Figure 1	3.0V to 3.6V	0.5	6.2	11.5	0.5	13.0	0.5	14.5	ns		
		$C_L = 50pF$	4.5V to 5.5 V	0.5	4.3	7.5	0.5	8.5	0.5	9.5			
	Enable Time  TEN  TEN  TEN  TEN  TEN	Enable Time	Enable Time	Figure 1	3.0V to 3.6V	0.5	4.7	8.0	0.5	9.5	0.5	11.5	
				ime C <sub>L</sub> = 15pF	4.5V to 5.5V	0.5	3.3	5.1	0.5	6.0	0.5	7.5	1
τEN		DE <sub>N</sub> to Y <sub>N</sub> Figure 1	3.0V to 3.6V	0.5	6.8	11.5	0.5	13.0	0.5	14.5	ns		
		$C_L = 50pF$	4.5V to 5.5V	0.5	4.7	7.1	0.5	8.0	0.5	9.0			
		Figure 1	3.0V to 3.6V	0.5	6.7	9.7	0.5	11.5	0.5	12.5			
	Disable Time	ole Time C <sub>L</sub> = 15pF	4.5V to 5.5V	0.5	4.8	6.8	0.5	8.0	0.5	8.5	1		
t <sub>DIS</sub>		Figure 1	3.0 V to 3.6V	0.5	9.6	13.2	0.5	15.0	0.5	16.5	ns		
		C <sub>L</sub> =	$C_L = 50pF$	4.5V to 5.5V	0.5	6.8	8.8	0.5	10.0	0.5	11.0		

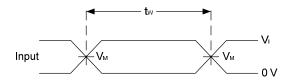


### **Parameter Measurement Information**

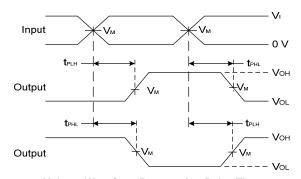


TEST	<b>S1</b>
t <sub>PLH</sub> /t <sub>PHL</sub>	Open
t <sub>PLZ</sub> /t <sub>PZL</sub>	Vload
t <sub>PHZ</sub> /t <sub>PZH</sub>	GND

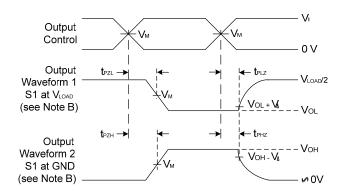
V	Inp	uts	V	V		Б	V/A
Vcc	VI	t <sub>r</sub> /t <sub>f</sub>	V <sub>M</sub>	V <sub>LOAD</sub>	CL	$R_L$	<b>V</b> Δ
3.3V±0.3V	3 V	≤3ns	V <sub>CC</sub> /2	V <sub>CC</sub>	15,50 pF	1ΚΩ	0.3 V
5V±0.5V	V <sub>CC</sub>	≤3ns	V <sub>CC</sub> /2	V <sub>CC</sub>	15,50 pF	1ΚΩ	0.3 V



#### **Voltage Waveform Pulse Duration**



Voltage Waveform Propagation Delay Times Inverting and Non Inverting Outputs



Voltage Waveform Enable and Disable Times Low and High Level Enabling

Figure 1. Load Circuit and Voltage Waveforms

Notes: A. Includes test lead and test apparatus capacitance.

B. All pulses are supplied at pulse repetition rate ≤ 1 MHz.

C. Inputs are measured separately one transition per measurement.

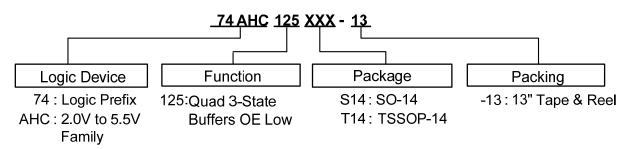
D.  $t_{\text{PLZ}}$  and  $t_{\text{PHZ}}$  are the same as  $t_{\text{dis.}}$ 

E.  $t_{PZL}$  and  $t_{PZH}$  are the same as  $t_{EN0}$ 

F.  $t_{\text{PLH}}$  and  $t_{\text{PHL}}$  are the same as  $t_{\text{PD.}}$ 



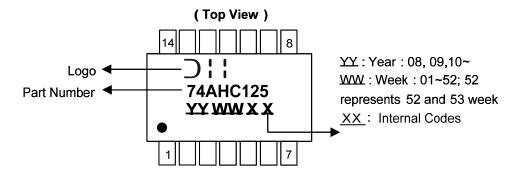
## **Ordering Information**



	Device	Package Code	Dackaging	7" Tape	and Reel
	Device	Fackage Code	Packaging	Quantity	Part Number Suffix
Green	74AHC125S14-13	S14	SO-14	2500/Tape & Reel	-13
areen	74AHC125T14-13	T14	TSSOP-14	2500/Tape & Reel	-13

### **Marking Information**

(1) SO-14, TSSOP-14



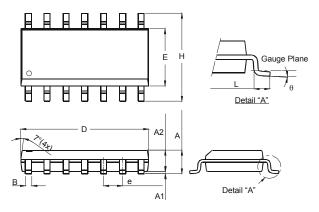
Part Number	Package
74AHC125S14	SO-14
74AHC125T14	TSSOP-14



## Package Outline Dimensions (All dimensions in mm.)

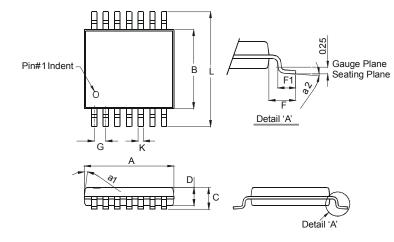
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.

#### Package Type: SO-14



	SO-14			
Dim	Min	Max		
Α	1.47	1.73		
A1	0.10	0.25		
A2	1.45	Тур		
В	0.33	0.51		
D	8.53	8.74		
Е	3.80	3.99		
е	1.27	Тур		
Н	5.80	6.20		
L	0.38	1.27		
θ	0°	8°		
All Dimensions in mm				

### Package Type: TSSOP-14



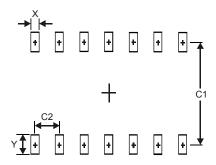
TSSOP-14		
Dim	Min	Max
a1	7° (4X)	
a2	0°	8°
Α	4.9	5.10
В	4.30	4.50
С		1.2
D	8.0	1.05
F	1.00 Typ	
F1	0.45	0.75
G	0.65 Typ	
K	0.19	0.30
L	6.40 Typ	
All Dimensions in mm		



## **Suggested Pad Layout**

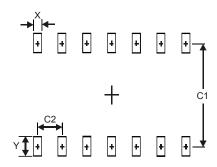
Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.

Package Type: SO-14



Dimensions	Value (in mm)
X	0.60
Y	1.50
C1	5.4
C2	1.27

Package Type: TSSOP-14



Dimensions	Value (in mm)
X	0.45
Υ	1.45
C1	5.9
C2	0.65



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