### Electrical Characteristics at Ta=25°C

Parameter	Cymphol	Conditions	Ratings			Linit
Parameter	Symbol	Conditions	min	typ	max	Unit
Drain-to-Source Breakdown Voltage	V(BR)DSS	ID=1mA, VGS=0V	1500			V
Zero-Gate Voltage Drain Current	IDSS	V <sub>DS</sub> =1200V, V <sub>GS</sub> =0V			100	μΑ
Gate-to-Source Leakage Current	IGSS	V <sub>GS</sub> =16V, V <sub>DS</sub> =0V			±10	μΑ
Cutoff Voltage	VGS(off)	V <sub>DS</sub> =10V, I <sub>D</sub> =1mA	2.5		3.5	V
Forward Transfer Admittance	yfs	V <sub>DS</sub> =20V, I <sub>D</sub> =1A	0.7	1.4		S
Static Drain-to-Source On-State Resistance	R <sub>DS</sub> (on)	I <sub>D</sub> =1A, V <sub>G</sub> S=10V		10	13	Ω
Input Capacitance	Ciss			380		pF
Output Capacitance	Coss	V <sub>DS</sub> =30V, f=1MHz		70		pF
Reverse Transfer Capacitance	Crss			40		pF
Turn-ON Delay Time	t <sub>d</sub> (on)			12		ns
Rise Time	t <sub>r</sub>	Con Fig 2		37		ns
Turn-OFF Delay Time	t <sub>d</sub> (off)	See Fig.2		152		ns
Fall Time	tf			59		ns
Total Gate Charge	Qg			37.5		nC
Gate-to-Source Charge	Qgs	V <sub>DS</sub> =200V, V <sub>GS</sub> =10V, I <sub>D</sub> =2A		2.7		nC
Gate-to-Drain "Miller" Charge	Qgd			20		nC
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =2A, V <sub>GS</sub> =0V		0.88	1.2	V

Fig.1 Avalanche Resistance Test Circuit

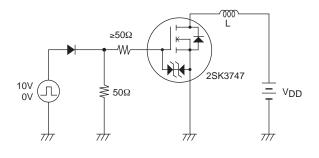
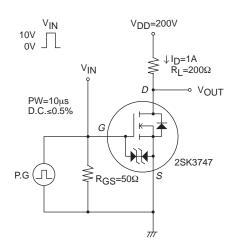
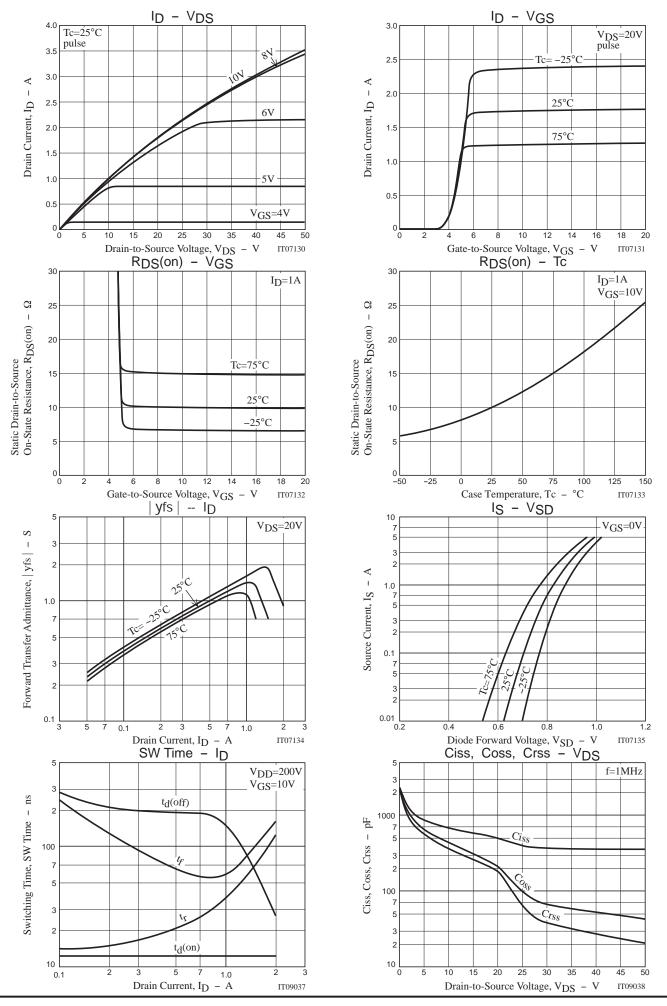


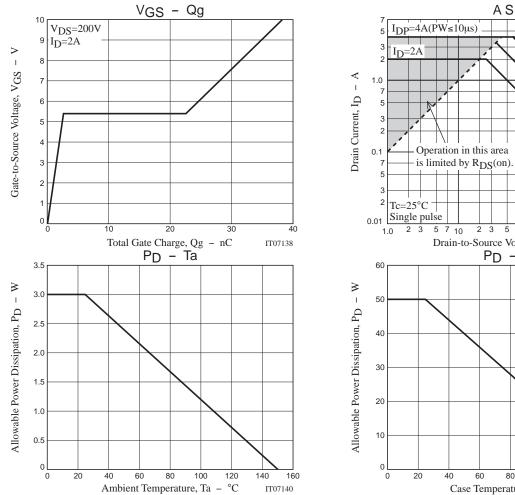
Fig.2 Switching Time Test Circuit

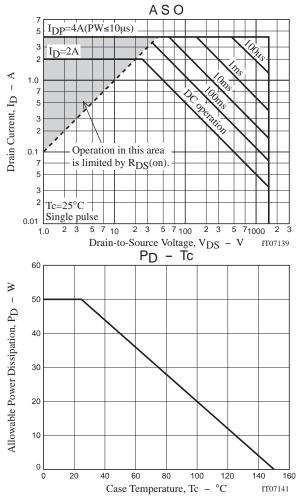


## **Ordering Information**

Device	Package	Shipping	memo	
2SK3747-1E	TO-3PF-3L	30pcs./magazine	Pb Free	







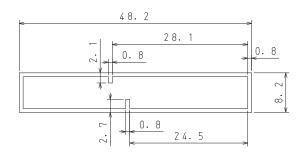
#### Magazine Specification

#### 2SK3747-1E

## 1. Packing Format

	Package Name	Maximum Number of devices contained (pcs)			Packing format		
	I don'd So I (damo	Magazine	Inner box	Outer box	Inner BOX	Outer BOX	
T	O-3PF-3L	30	360	1440		SPD-LV0010 4 inner boxes contained Dimensions:mm (external) 590x225x178	

# 



Tolerance= $\pm 0$ . 2mm Thickness=0.  $8\pm 0$ . 2mm Length =508.  $0\pm 1$ mm Material =PVC or PET (Antistatic treatment)

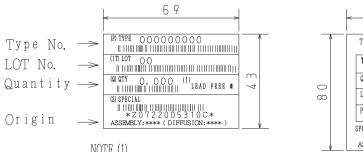
# 3. Storage method to magazine

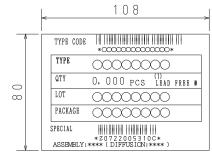


4. Inner box label (unit:mm)



It is a label at the time of factory shipments. The form of a label may change in physical distribution process.



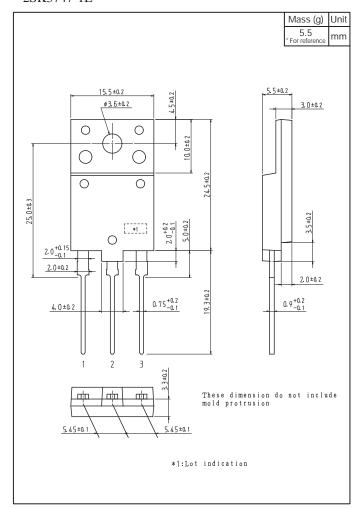


The LEAD FREE \* description shows that the surface treatment of the terminal is lead free.

Label		JEITA Phase
LEAD FREE	3	JEITA Phase 3A

# **Outline Drawing**

2SK3747-1E



Note on usage: Since the 2SK3747 is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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