

**SPECIFICATIONS**  $T_J = 25^\circ\text{C}$ , unless otherwise noted

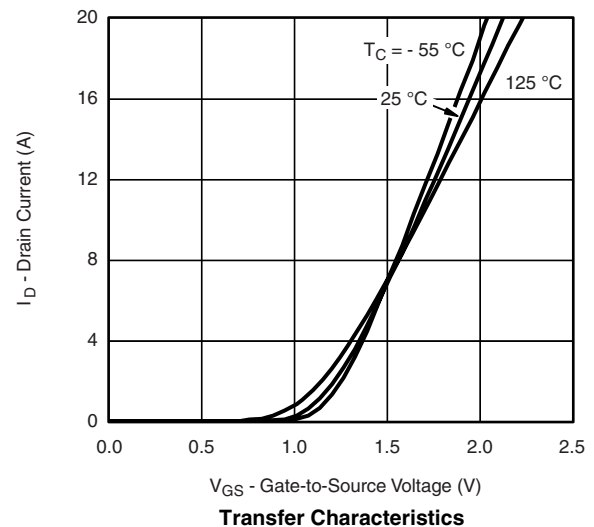
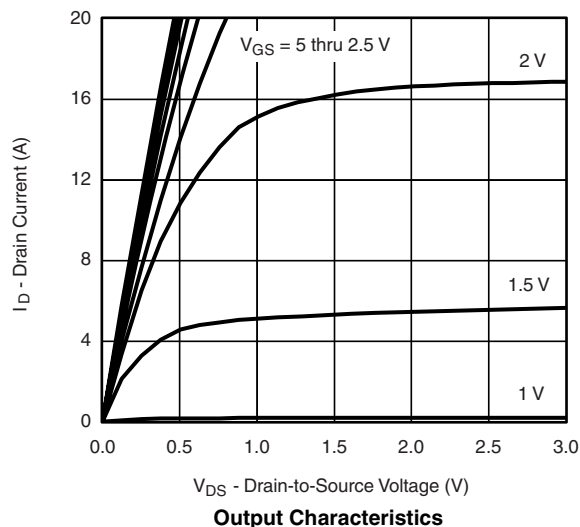
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
<b>Static</b>						
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}$ , $I_D = -250\ \mu\text{A}$	-0.45		-1.0	V
Gate-Body Leakage	$I_{GSS}$	$V_{DS} = 0\ \text{V}$ , $V_{GS} = \pm 8\ \text{V}$			$\pm 100$	nA
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = -8\ \text{V}$ , $V_{GS} = 0\ \text{V}$			-1	$\mu\text{A}$
		$V_{DS} = -8\ \text{V}$ , $V_{GS} = 0\ \text{V}$ , $T_J = 85^\circ\text{C}$			-5	
On-State Drain Current <sup>a</sup>	$I_{D(on)}$	$V_{DS} \leq -5\ \text{V}$ , $V_{GS} = -4.5\ \text{V}$	-20			A
Drain-Source On-State Resistance <sup>a</sup>	$R_{DS(on)}$	$V_{GS} = -4.5\ \text{V}$ , $I_D = -5.2\ \text{A}$		0.027	0.033	$\Omega$
		$V_{GS} = -2.5\ \text{V}$ , $I_D = -4.5\ \text{A}$		0.035	0.043	
		$V_{GS} = -1.8\ \text{V}$ , $I_D = -1.7\ \text{A}$		0.050	0.060	
Forward Transconductance <sup>a</sup>	$g_{fs}$	$V_{DS} = -5\ \text{V}$ , $I_D = -5.2\ \text{A}$		18		S
Diode Forward Voltage <sup>a</sup>	$V_{SD}$	$I_S = -1.1\ \text{A}$ , $V_{GS} = 0\ \text{V}$		-0.8	-1.2	V
<b>Dynamic<sup>b</sup></b>						
Total Gate Charge	$Q_g$	$V_{DS} = -4\ \text{V}$ , $V_{GS} = -4.5\ \text{V}$ , $I_D = -5.2\ \text{A}$		14	21	nC
Gate-Source Charge	$Q_{gs}$			1.8		
Gate-Drain Charge	$Q_{gd}$			3.3		
Gate Resistance	$R_g$	$f = 1\ \text{MHz}$		8		$\Omega$
Turn-On Delay Time	$t_{d(on)}$	$V_{DD} = -4\ \text{V}$ , $R_L = 4\ \Omega$ $I_D \cong -1\ \text{A}$ , $V_{GEN} = -4.5\ \text{V}$ , $R_g = 6\ \Omega$		12	20	ns
Rise Time	$t_r$			22	35	
Turn-Off Delay Time	$t_{d(off)}$			75	115	
Fall Time	$t_f$			50	75	
Source-Drain Reverse Recovery Time	$t_{rr}$	$I_F = -1.1\ \text{A}$ , $dI/dt = 100\ \text{A}/\mu\text{s}$		75	115	ns
Reverse Recovery Charge	$Q_{rr}$			40	60	

Notes:

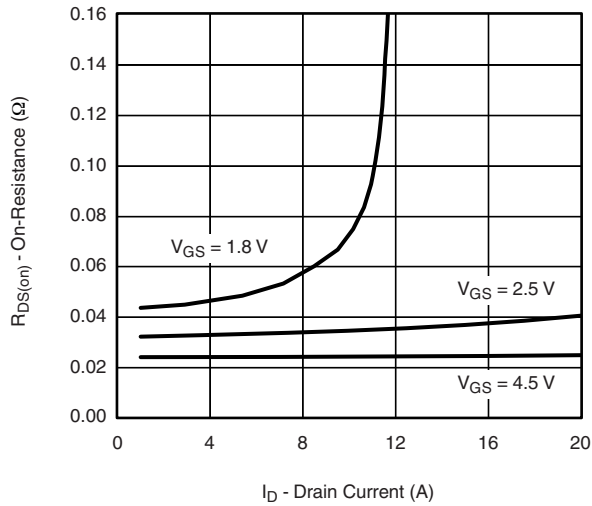
a. Pulse test; pulse width  $\leq 300\ \mu\text{s}$ , duty cycle  $\leq 2\%$ .

b. Guaranteed by design, not subject to production testing.

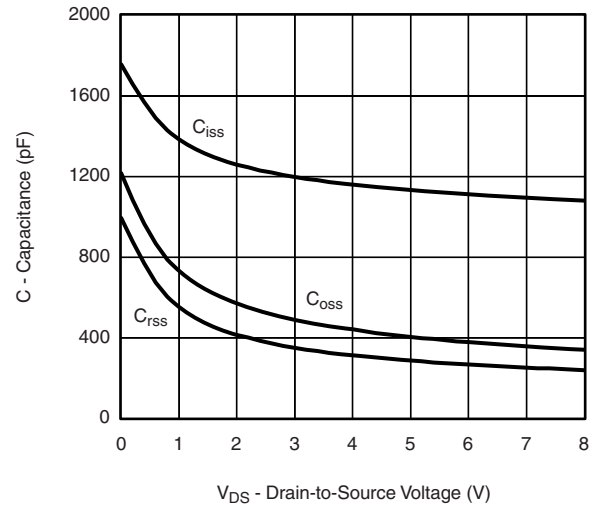
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

**TYPICAL CHARACTERISTICS**  $25^\circ\text{C}$ , unless otherwise noted

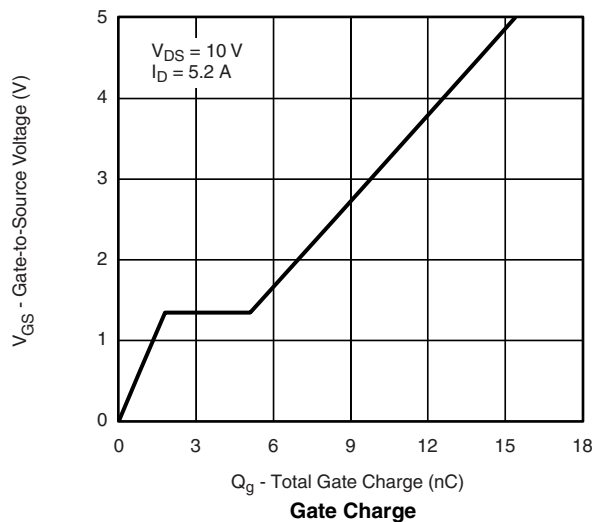
## TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



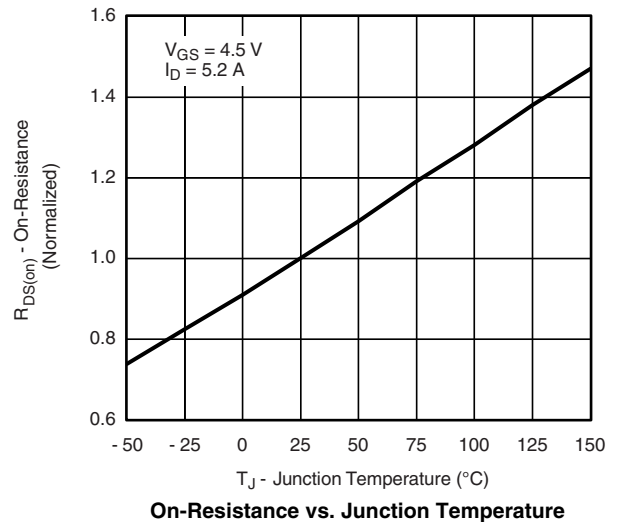
On-Resistance vs. Drain Current



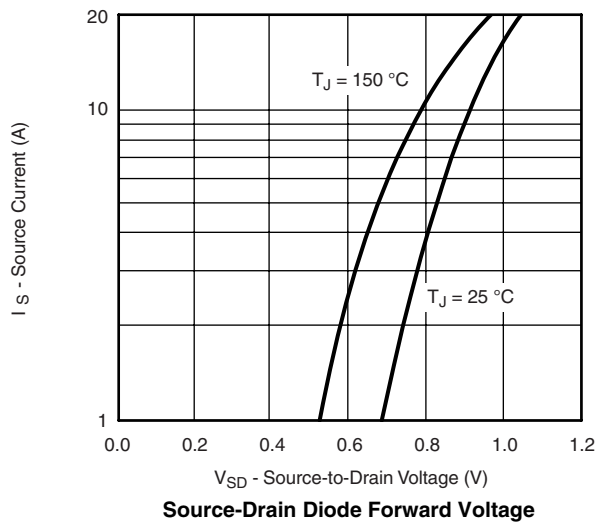
Capacitance



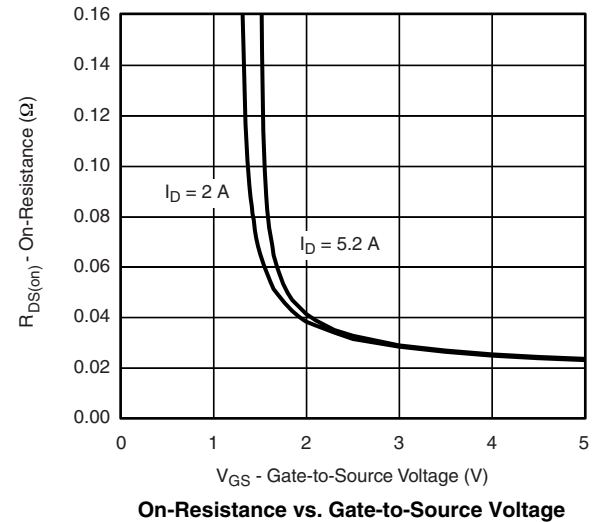
Gate Charge



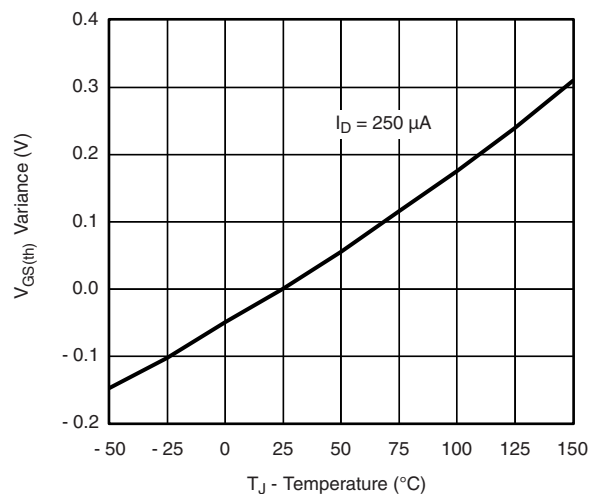
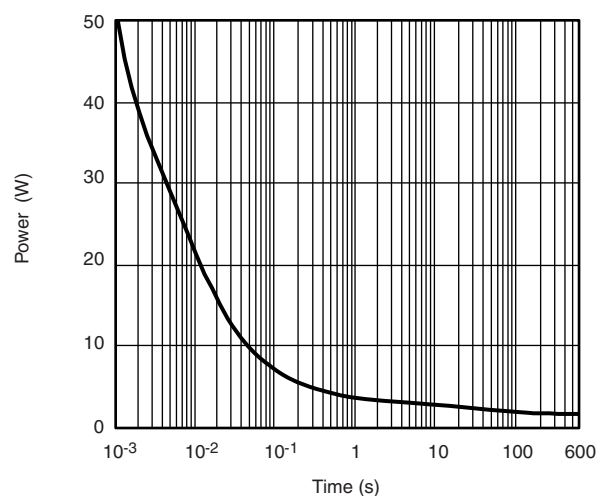
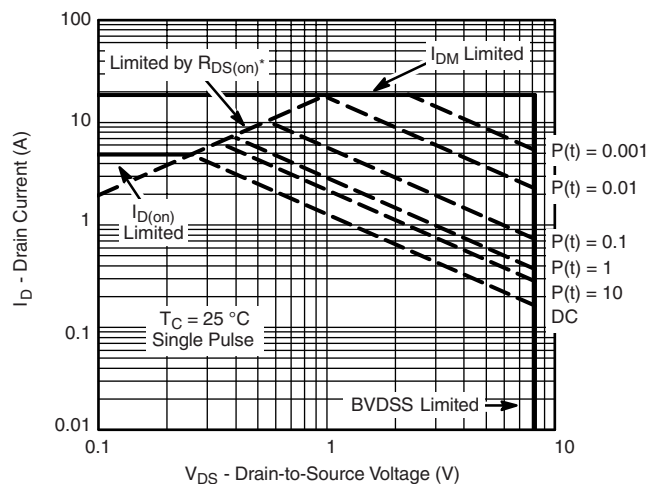
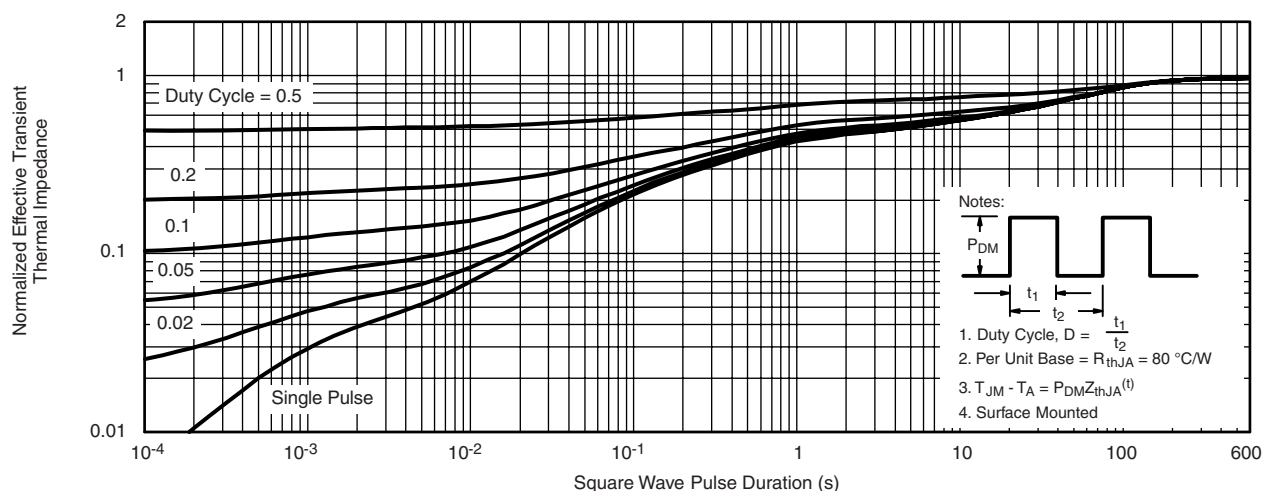
On-Resistance vs. Junction Temperature



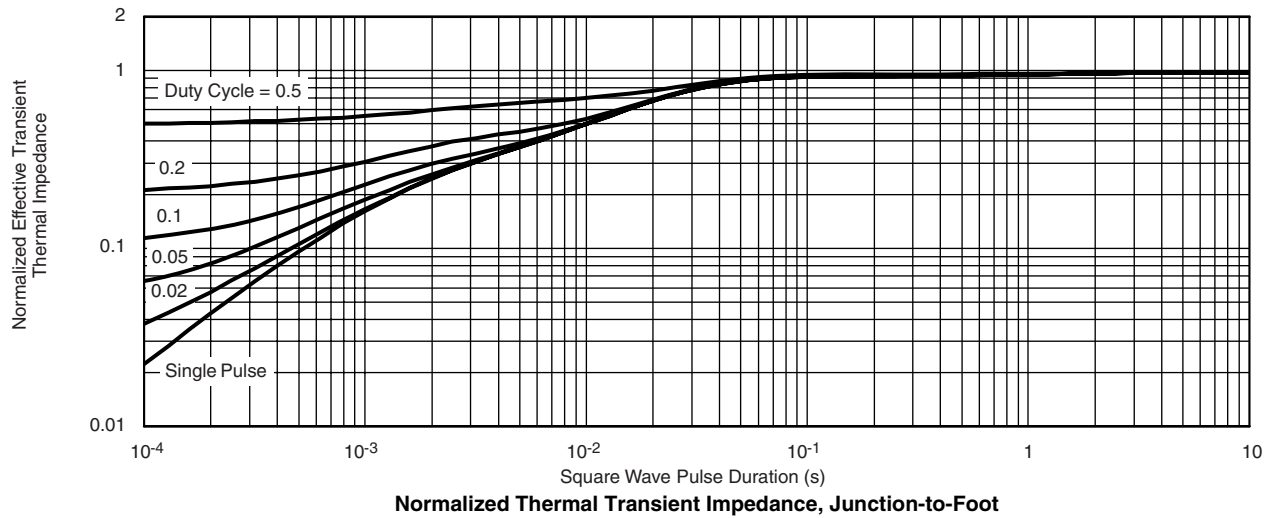
Source-Drain Diode Forward Voltage



On-Resistance vs. Gate-to-Source Voltage

**TYPICAL CHARACTERISTICS** 25 °C, unless otherwise noted**Threshold Voltage****Single Pulse Power**\*  $V_{GS} >$  minimum  $V_{GS}$  at which  $R_{DS(on)}$  is specified**Safe Operating Area****Normalized Thermal Transient Impedance, Junction-to-Ambient**

**TYPICAL CHARACTERISTICS** 25 °C, unless otherwise noted



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