Maximum Ratings

(Above which the useful life may be impaired. For user guidelines, not tested.)

Storage Temperature ....–65°C to +150°C	
Ambient Temperature with Power Applied ....–40°C to +85°C	
Supply Voltage to Ground Potential (Inputs & V_{CC} Only)...–0.5V to +7.0V	
Supply Voltage to Ground Potential (Outputs & D/O Only) –0.5V to +7.0V	
DC Input Voltage ....–0.5V to +7.0V	
DC Output Current..120mA	
Power Dissipation 0.5W	

Note:
Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

DC Electrical Characteristics (Over the Operating Range, T_A = –40°C to +85°C, V_{CC} = 5V ±5%)

Parameters	Description	Test Conditions[1]	Min.	Typ.[2]	Max.	Units
V_{ANALOG}	Analog Signal Range		0		2.0	V
V_{IH}	Input HIGH Voltage	Guaranteed Logic HIGH Level	2.0		-	
V_{IL}	Input LOW Voltage	Guaranteed Logic LOW Level	–0.5		0.8	
I_{IH}	Input HIGH Current	V_{CC} = Max., V_{IN} = V_{CC}			±1	
I_{IL}	Input LOW Current	V_{CC} = Max., V_{IN} = GND			±1	μA
I_O	Analog Output Leakage Current	$0 \leq S1$, S2 or $D \leq V_{CC}$, Switch OFF			±1	
V_{IK}	Clamp Diode Voltage	V_{CC} = Min., I_{IN} = -18mA	-0.7	-1.2		V
V_H	Input Hysteresis at Control Pins			150	-	mV
R_{ON}	Switch On-Resistance[3]	V_{CC} = Min., V_{IN} = 1.0V, I_{ON} = 13mA		5	7	Ω
		V_{CC} = Min., V_{IN} = 2.0V, I_{ON} = 26mA		7	10	

Notes:
1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.
2. Typical values are at V_{CC} = 5.0V, T_A = 25°C ambient and maximum loading.
3. Measured by the voltage drop between S1, S2, and D I/O pins at indicated current through the switch. On-Resistance is determined by the lower of the voltages on the S1, S2, and D I/O pins.

Dynamic Characteristics (Over the Operating Range, T_A = –40°C to +85°C, V_{CC} = 5V ±5%)

Parameters	Description	Test Conditions	Min.	Typ.	Max.	Unit
t_{ON}	Turn On Time	R_L = 75Ω, C_L = 20pF, See Fig. 4		2.5	5	ns
t_{OFF}	Turn Off Time	R_L = 75Ω, C_L = 20pF, See Fig. 4		1.1	5	
B_W[1]	-3 dB Bandwidth	See Fig. 5			570	MHz
X_{TALK}[1]	Crosstalk	10MHz, See Fig. 5, C_L = 0pF		-80		dB
C_{IN}[1]	Input/Enable Capacitance	V_{IN} = 0V, f = 1 MHz			6	pF
C_{OFF}[1]	Capacitance, Switch Off	V_{IN} = 0V, f = 1 MHz			6	
C_{ON}[1]	Capacitance, Switch On	V_{IN} = 0V, f = 1 MHz			9	
O_{IRR}	Off Isolation	10MHz, See Fig 5, C_L = 0pF		-48		dB

Notes:
1. This parameter is determined by device characterization but is not production tested.

Power Supply Characteristics

Parameters	Description	Test Conditions[1]		Min.	Typ[2]	Max.	Units
I_{CC}	Quiescent Power Supply Current	V_{CC} = Max.	IN = GND or V_{CC}		0.1	3.0	μA
Δ_{ICC}	Supply Current per Input @ TTL HIGH	V_{CC} = Max.	IN = 3.4V[3]			2.5	mA
I_{CCD}	Supply Current per Input per MHz[4]	V_{CC} = Max., S1, S2 and D Pins Open $\overline{EN}$ = GND Control Input Toggling 50%Duty Cycle				0.25	mA/ MHz

Notes:
1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device.
2. Typical values are at V_{CC} = 5.0V, +25°C ambient.
3. Per TTL driven input (V_{IN} = 3.4V, control inputs only); S1, S2, and D pins do not contribute to I_{CC}.
4. This current applies to the control inputs only and represent the current required to switch internal capacitance at the specified frequency. The S1, S2, and D I/O pins generate no significant AC or DC currents as they transition. This parameter is not tested, but is guaranteed by design.

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Definitions

| Symbol	Description
R_{ON}	Resistance between source and drain with switch in the ON state.
I_O	Output leakage current measured at S1, S2, and D with the switch OFF.
V_{IN}	Digital voltage at the IN pin that selects between S1 and S2 analog inputs.
V_{EN}	A voltage that ENABLES the chip.
C_{IN}	Capacitance at the digital inputs.
C_{OFF}	Capacitance at analog I/O (S1, S2, D) with switch OFF.
C_{ON}	Capacitance at analog I/O (S1, S2, D) with switch ON.
V_{IH}	Minimum input voltage for logic HIGH.
V_{IL}	Minimum input voltage for logic LOW.
I_{IH} (I_{IL})	Input current of the digital input.
t_{ON}	Propagation delay measured between 50% of the digital input to 90% of the analog output when switch is turned ON. The peak analog voltage is 0.714V.
t_{OFF}	Propagation delay measured between 50% of the digital input to 90% of the analog output when switch is turned OFF. The peak analog voltage is 0.714V.
B_W	Frequency response of the switch in the ON state measured at 3dB down.
X_{TALK}	Unwanted signal coupled from channel to channel, measured in −dB. $X_{TALK} = 20 \, LOG \, V_{OUT}/V_{IN}$. This is non-adjacent crosstalk.
O_{IRR}	Off isolation is the resistance (measured in −dB) between the input and output with the switch off (NO).
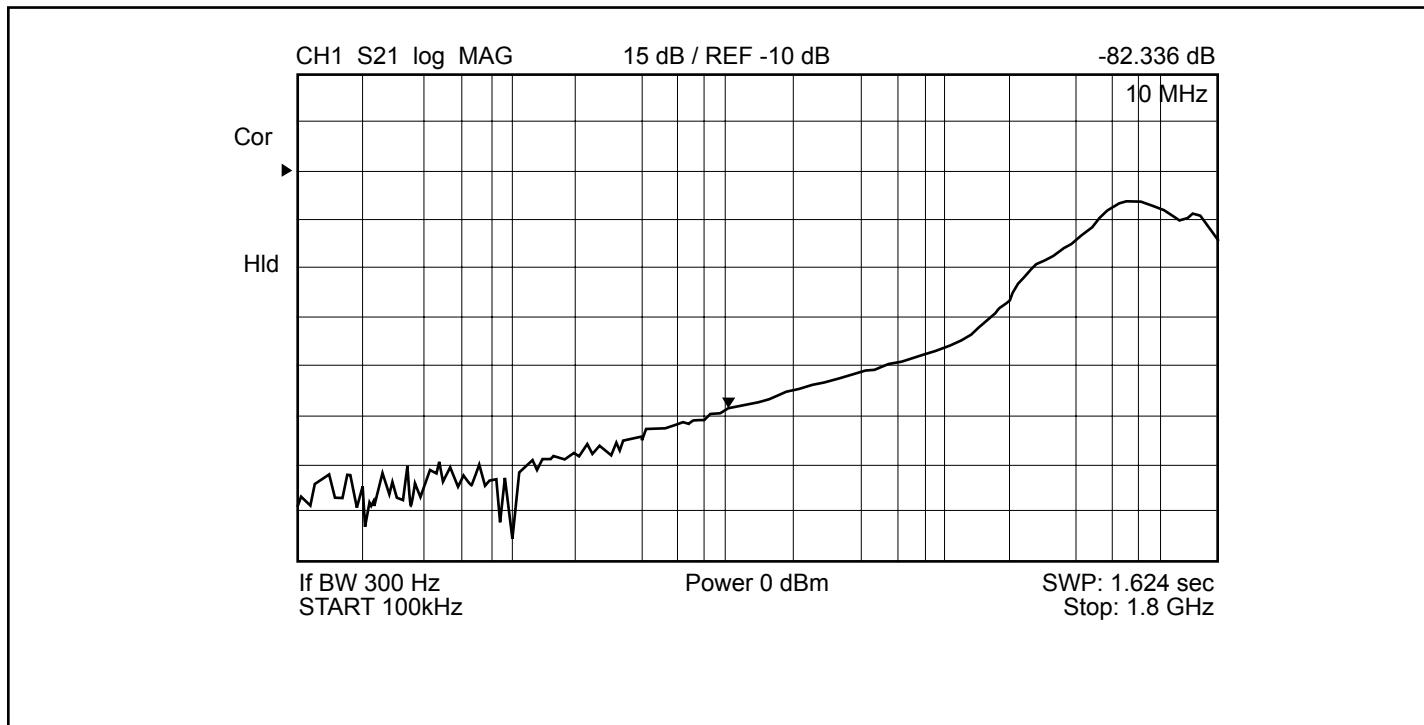
Figure 1. Gain/Phase vs Frequency

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CH1 S21 log MAG   14 dB / REF -10 dB   -49.682 dB

10 MHz

Cor

Hld

If BW 300 Hz   Power 0 dBm   SWP: 1.624 sec
START 100kHz   Stop: 1.8 GHz

Figure 2. Off Isolation vs Frequency

CH1 S21 log MAG   15 dB / REF -10 dB   -82.336 dB

10 MHz

Cor

Hld

If BW 300 Hz   Power 0 dBm   SWP: 1.624 sec
START 100kHz   Stop: 1.8 GHz
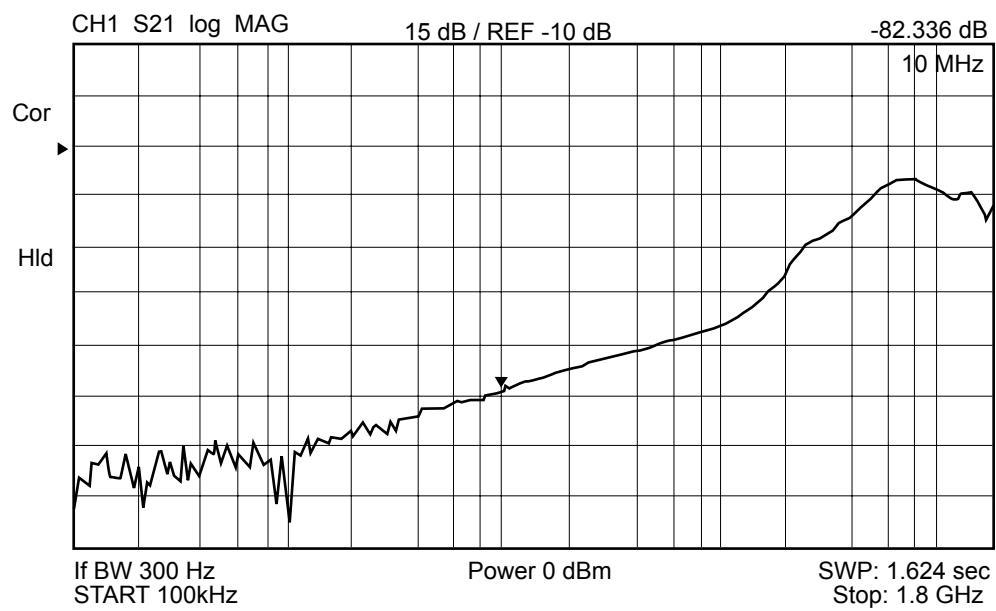
Figure 3. Crosstalk vs Frequency
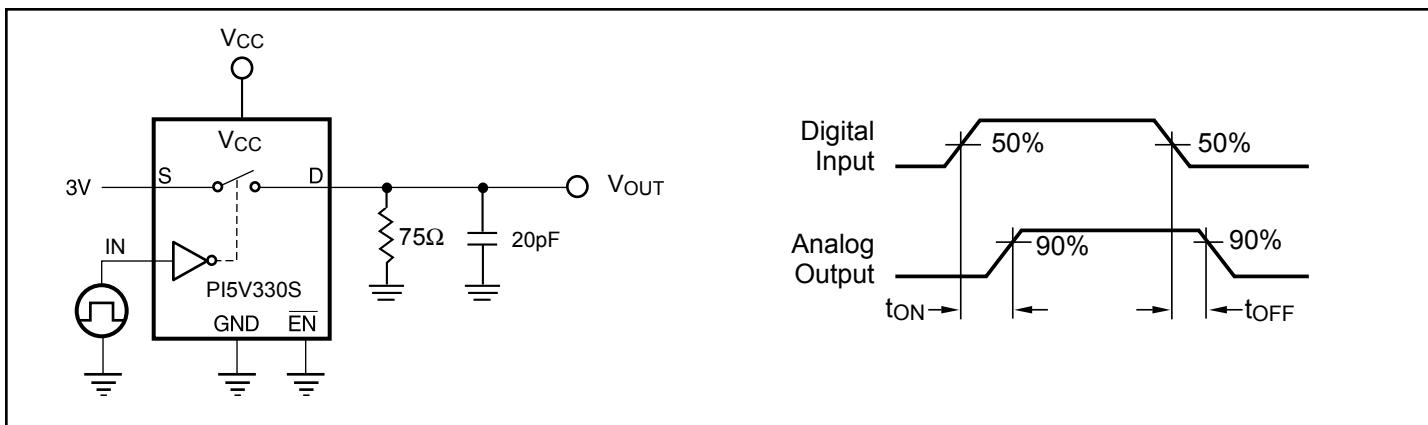
Figure 4. Switching Time
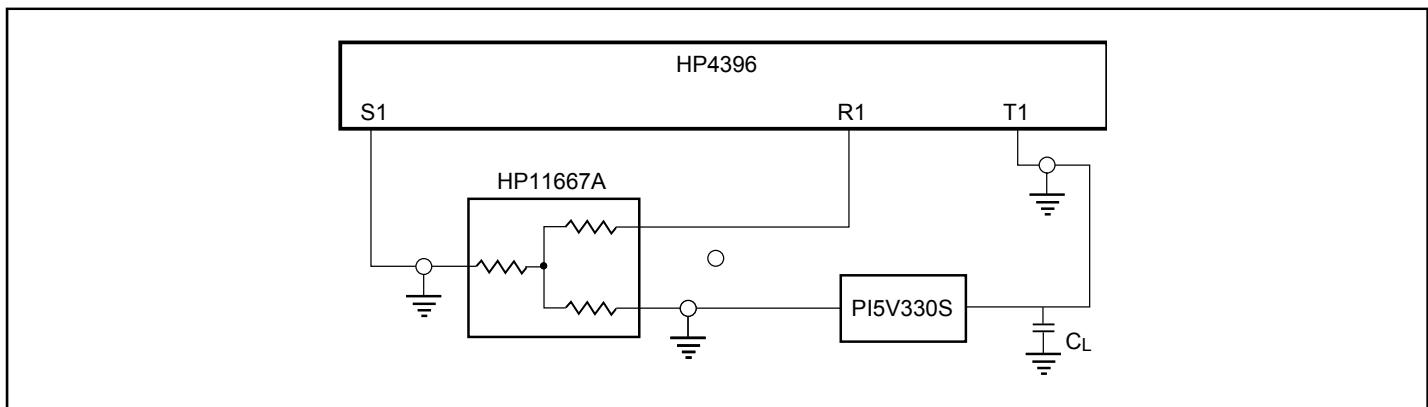
Figure 5. Gain/Phase, Crosstalk, Off-Isolation
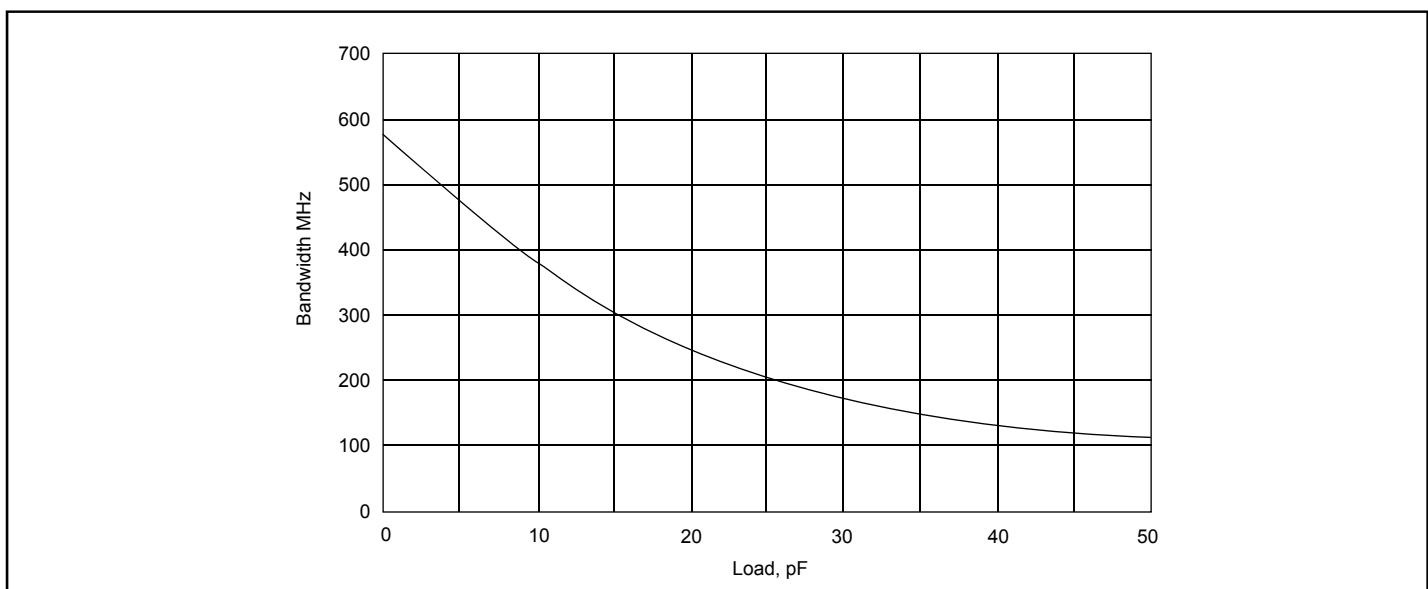
Figure 6. Bandwidth vs Load

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Packaging Mechanical: 16-pin QSOP (Q)
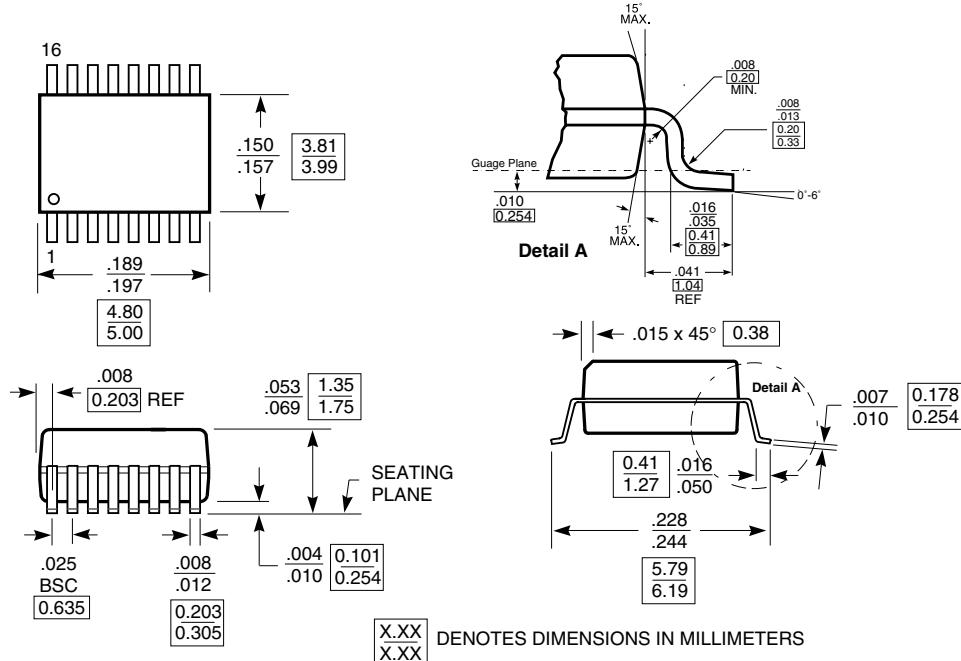
Packaging Mechanical: 16-pin SOIC (W)

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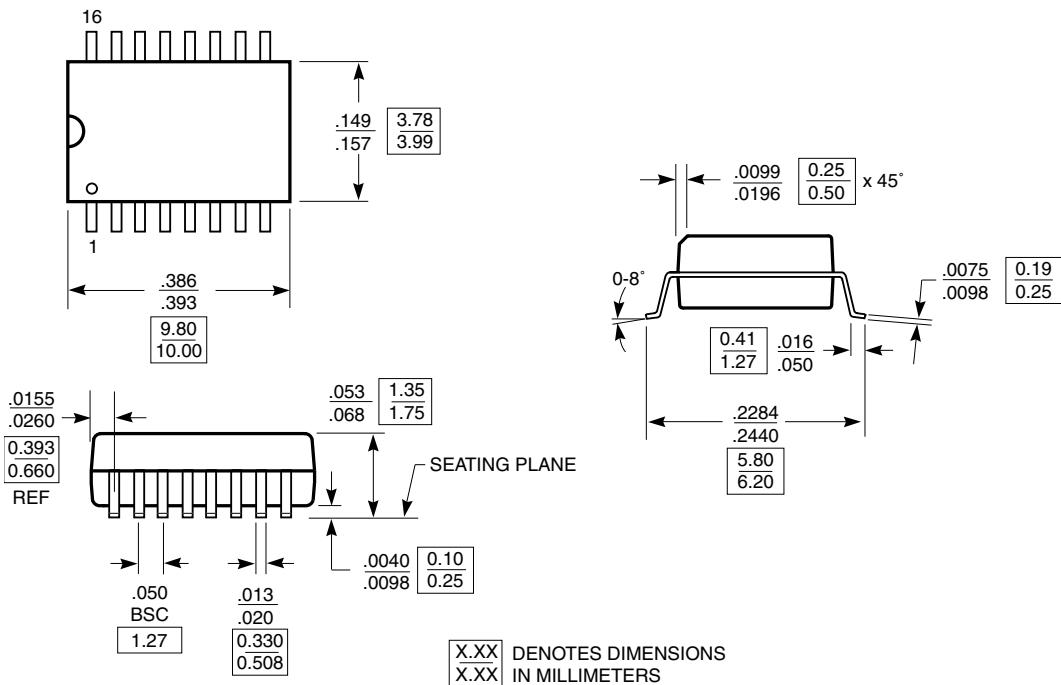
Ordering Information

Ordering Code	Package Code	Package Description
PI5V330SW	W	16-pin 150-mil wide plastic SOIC
PI5V330SWE	W	Pb-free & Green, 16-pin 150-mil wide plastic SOIC
PI5V330SQ	Q	16-pin 150-mil wide plastic QSOP
PI5V330SQE	Q	Pb-free & Green, 16-pin 150-mil wide plastic QSOP

Notes:

1. Thermal characteristics can be found on the company web site at www.pericom.com/packaging/