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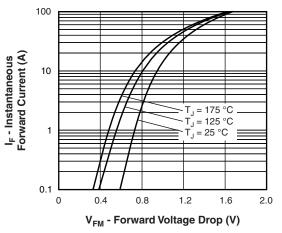
### Vishay Semiconductors

<b>DYNAMIC RECOVERY CHARACTERISTICS</b> ( $T_J = 25$ °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CO	MIN.	TYP.	MAX.	UNITS	
Reverse recovery time		$I_F = 1.0 \text{ A}, \text{ d}I_F/\text{d}t = 50 \text{ A}/\mu\text{s}, V_R = 30 \text{ V}$		-	-	35	
	+	$I_F = 0.5 \text{ A}, I_R = 1.0 \text{ A}, I_{REC} = 0.25 \text{ A}$		-	-	25	
	t <sub>rr</sub>	T <sub>J</sub> = 25 °C	I <sub>F</sub> = 10 A dI <sub>F</sub> /dt = 200 A/μs V <sub>R</sub> = 160 V	-	21	-	ns
		T <sub>J</sub> = 125 °C		-	35	-	
Peak recovery current		T <sub>J</sub> = 25 °C		-	1.9	-	A
	I <sub>RRM</sub>	T <sub>J</sub> = 125 °C		-	4.8	-	
Reverse recovery charge		T <sub>J</sub> = 25 °C		-	25	-	
	Q <sub>rr</sub>	T <sub>J</sub> = 125 °C		-	78	-	nC

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		- 65	-	175	°C
Thermal resistance, per leg	Р		-	-	2.5	
junction to case total device	R <sub>thJC</sub>		-	-	1.25	
Thermal resistance, junction to ambient per leg	R <sub>thJA</sub>		-	-	50	°C/W
Thermal resistance, case to heatsink	R <sub>thCS</sub>	Mounting surface, flat, smooth and greased	-	0.5	-	
Weight			-	2.0	-	g
Weight			-	0.07	-	oz.
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)
Marking device		Case style TO-220AB	MUR2020CT			

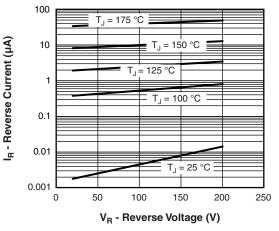
# VS-MUR2020CTPbF, VS-MUR2020CT-N3

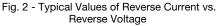




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Fig. 1 - Maximum Forward Voltage Drop Characteristics





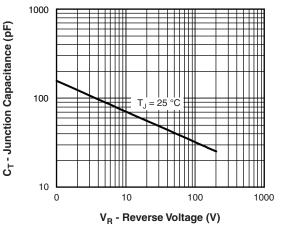


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

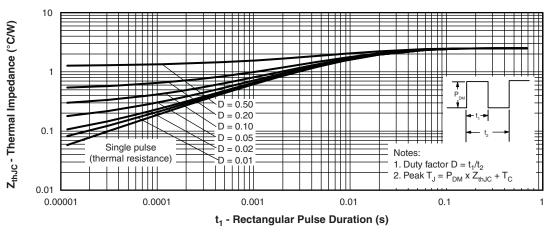
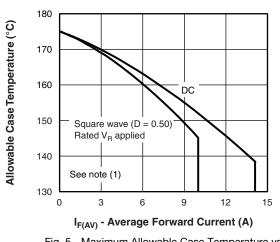


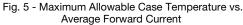
Fig. 4 - Maximum Thermal Impedance  $Z_{thJC}$  Characteristics



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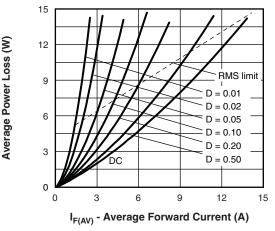
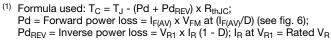
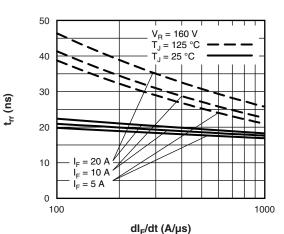


Fig. 6 - Forward Power Loss Characteristics

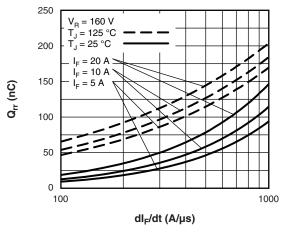
#### Note

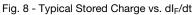












Revision: 11-Aug-11 4 Document Number: 94079 For technical questions within your region: <u>DiodesAmericas@vishay.com</u>, <u>DiodesAsia@vishay.com</u>, <u>DiodesEurope@vishay.com</u> THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT <u>www.vishay.com/doc?91000</u>



## VS-MUR2020CTPbF, VS-MUR2020CT-N3

### **Vishay Semiconductors**

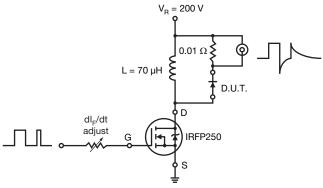
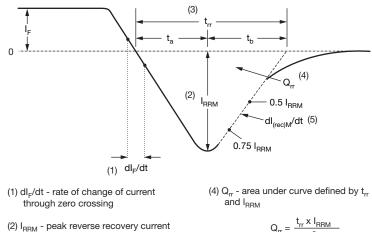


Fig. 9 - Reverse Recovery Parameter Test Circuit



$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

(3) t<sub>rr</sub> - reverse recovery time measured from zero crossing point of negative going  $I_F$  to point where a line passing through 0.75 I<sub>RRM</sub> and 0.50 I<sub>RRM</sub> extrapolated to zero current.

(5)  $dI_{(rec)M}/dt$  - peak rate of change of current during t<sub>b</sub> portion of t<sub>rr</sub>

Fig. 10 - Reverse Recovery Waveform and Definitions

Downloaded from Arrow.com.

Device code VS- MUR 20 20 CT PbF

(6)

- Vishay Semiconductors product

(4)

2 - Ultrafast MUR series

(2)

1

1

3

4

5

6

- Current rating (20 = 20 A)

(3)

- Voltage rating (20 = 200 V)
- CT = Center tap (dual)
- Environmental digit:
  - PbF = Lead (Pb)-free and RoHS compliant

(5)

-N3 = Halogen-free, RoHS compliant and totally lead (Pb)-free

ORDERING INFORMATION (Example)							
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION				
VS-MUR2020CTPbF	50	1000	Antistatic plastic tube				
VS-MUR2020CT-N3	50	1000	Antistatic plastic tube				

LINKS TO RELATED DOCUMENTS					
Dimensions		www.vishay.com/doc?95222			
Part marking information	TO-220ABPbF	www.vishay.com/doc?95225			
	TO-220AB-N3	www.vishay.com/doc?95028			
SPICE model		www.vishay.com/doc?95272			

VS-MUR2020CTPbF, VS-MUR2020CT-N3

**Vishay Semiconductors** 



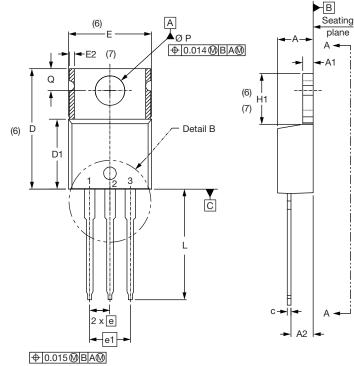
**ORDERING INFORMATION TABLE** 

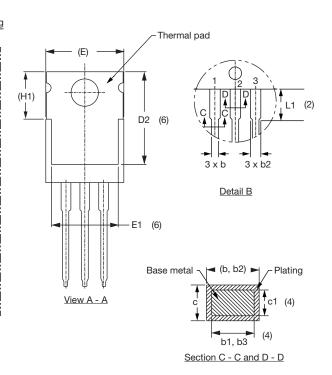


**Vishay Semiconductors** 

**TO-220AB** 

#### **DIMENSIONS** in millimeters and inches





Lead tip 🔨



Lead	assignments

<u>Diodes</u>

3. - Anode

1. - Anode/open 2. - Cathode

SYMBOL	MILLIN	IETERS	INCHES		NOTES
	MIN.	MAX.	MIN.	MAX.	NOTES
A	4.25	4.65	0.167	0.183	
A1	1.14	1.40	0.045	0.055	
A2	2.56	2.92	0.101	0.115	
b	0.69	1.01	0.027	0.040	
b1	0.38	0.97	0.015	0.038	4
b2	1.20	1.73	0.047	0.068	
b3	1.14	1.73	0.045	0.068	4
С	0.36	0.61	0.014	0.024	
c1	0.36	0.56	0.014	0.022	4
D	14.85	15.25	0.585	0.600	3
D1	8.38	9.02	0.330	0.355	
D2	11.68	12.88	0.460	0.507	6

#### Notes

- <sup>(1)</sup> Dimensioning and tolerancing as per ASME Y14.5M-1994
- <sup>(2)</sup> Lead dimension and finish uncontrolled in L1
- <sup>(3)</sup> Dimension D, D1 and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- <sup>(4)</sup> Dimension b1, b3 and c1 apply to base metal only
- <sup>(5)</sup> Controlling dimensions: inches
- $^{\rm (6)}$  Thermal pad contour optional within dimensions E, H1, D2 and E1

Conforms to JEDEC outline TO-220AB

SYMBOL	MILLIN	IETERS	INCHES		NOTES
STWIDOL	MIN.	MAX.	MIN.	MAX.	NOTES
E	10.11	10.51	0.398	0.414	3, 6
E1	6.86	8.89	0.270	0.350	6
E2	-	0.76	-	0.030	7
е	2.41	2.67	0.095	0.105	
e1	4.88	5.28	0.192	0.208	
H1	6.09	6.48	0.240	0.255	6, 7
L	13.52	14.02	0.532	0.552	
L1	3.32	3.82	0.131	0.150	2
ØΡ	3.54	3.73	0.139	0.147	
Q	2.60	3.00	0.102	0.118	
θ	90° t	o 93°	90° to 93°		

- <sup>(7)</sup> Dimensions E2 x H1 define a zone where stamping and singulation irregularities are allowed
- <sup>(8)</sup> Outline conforms to JEDEC TO-220, except A2 (maximum) and D2 (minimum) where dimensions are derived from the actual package outline

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