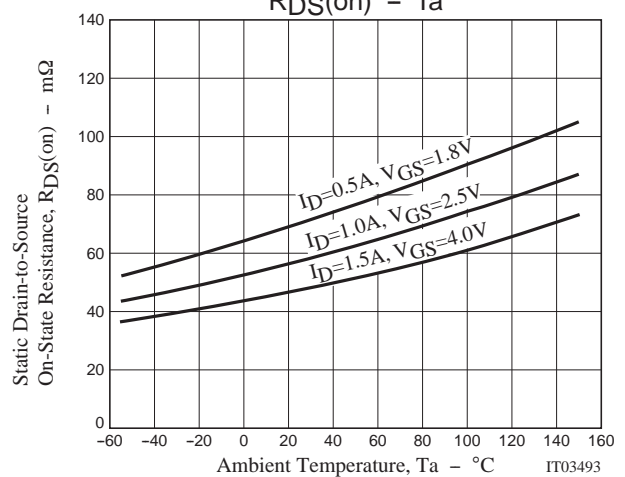
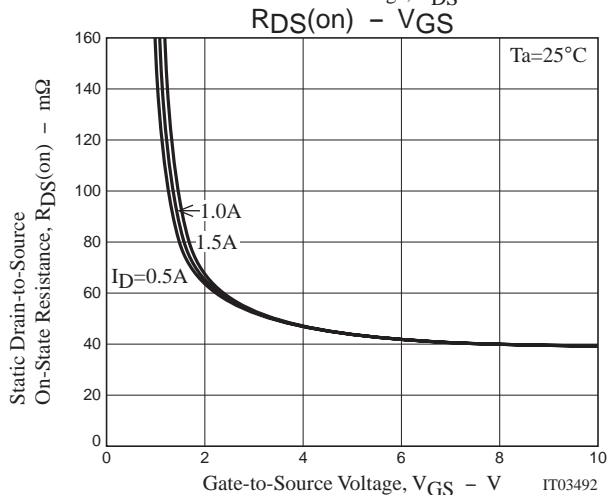
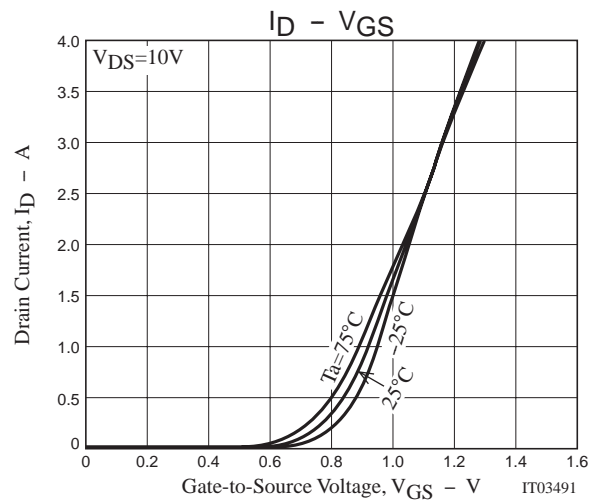
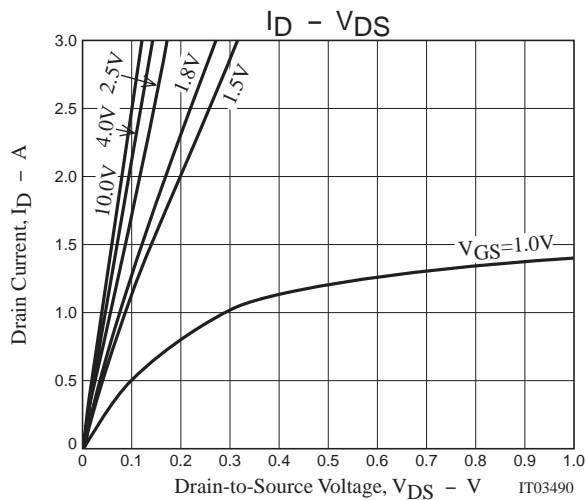
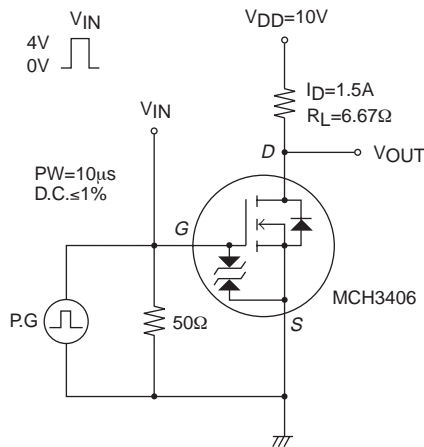
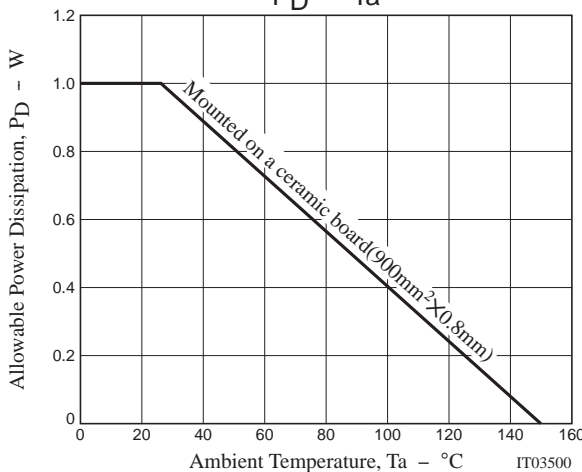
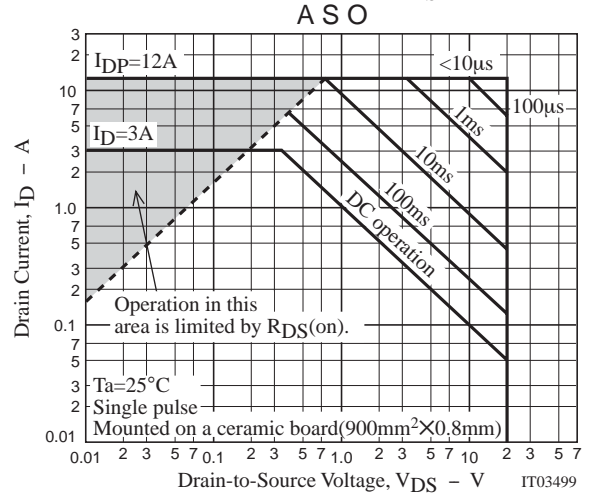
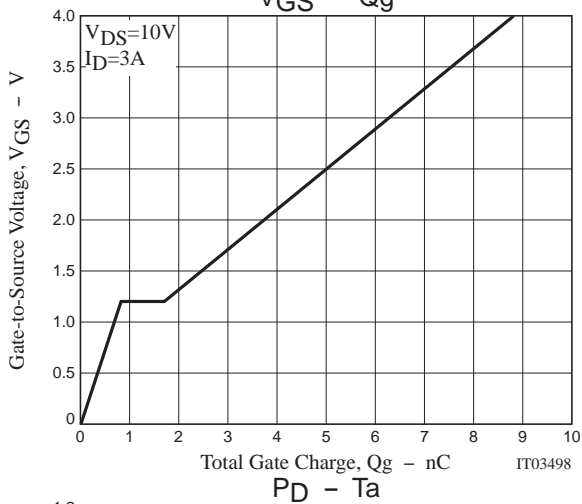
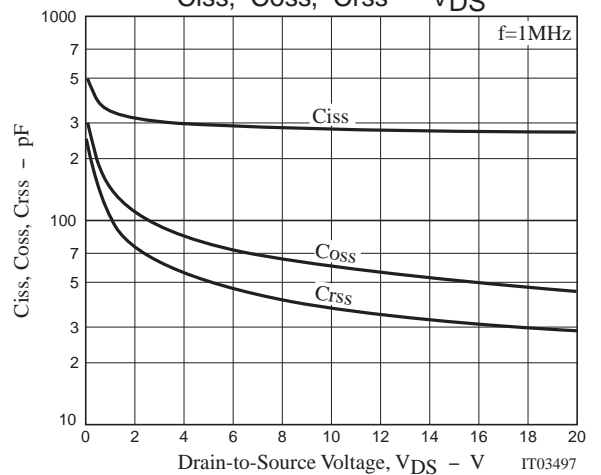
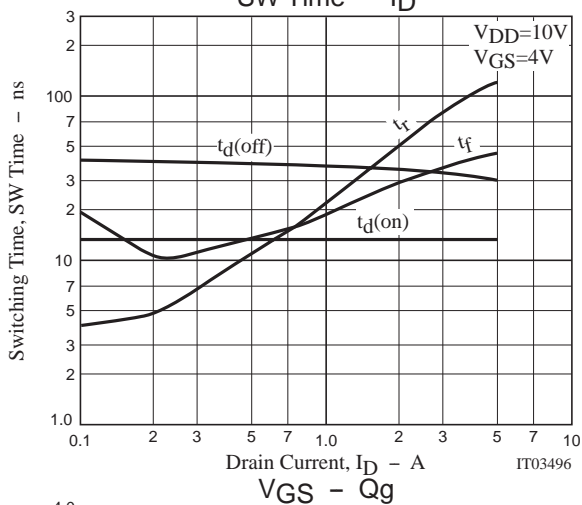
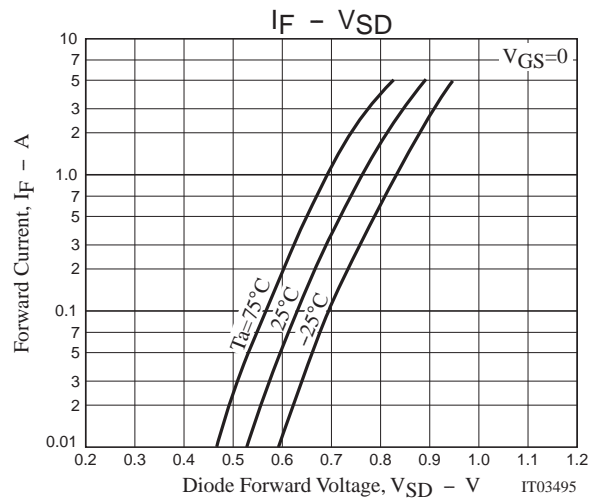
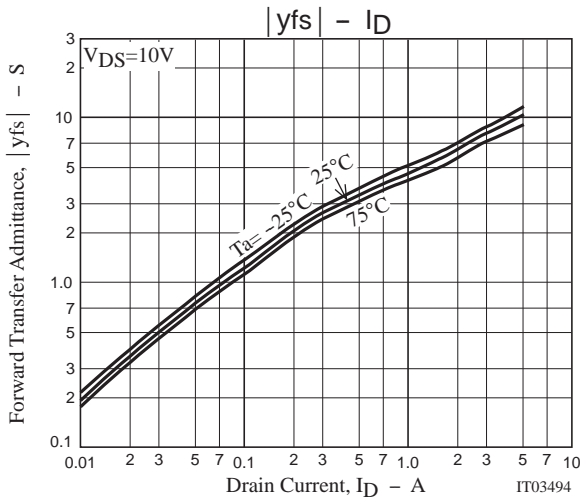


Continued from preceding page.

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Input Capacitance	C_{iss}	$V_{DS}=10V, f=1MHz$		280		pF
Output Capacitance	C_{oss}	$V_{DS}=10V, f=1MHz$		60		pF
Reverse Transfer Capacitance	C_{rss}	$V_{DS}=10V, f=1MHz$		38		pF
Turn-ON Delay Time	$t_{d(on)}$	See specified Test Circuit		13		ns
Rise Time	t_r	See specified Test Circuit		35		ns
Turn-OFF Delay Time	$t_{d(off)}$	See specified Test Circuit		35		ns
Fall Time	t_f	See specified Test Circuit		25		ns
Total Gate Charge	Q_g	$V_{DS}=10V, V_{GS}=4V, I_D=3A$		8.8		nC
Gate-to-Source Charge	Q_{gs}	$V_{DS}=10V, V_{GS}=4V, I_D=3A$		0.85		nC
Gate-to-Drain "Miller" Charge	Q_{gd}	$V_{DS}=10V, V_{GS}=4V, I_D=3A$		0.85		nC
Diode Forward Voltage	V_{SD}	$I_S=3A, V_{GS}=0$		0.82	1.2	V

Switching Time Test Circuit





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