

Ultra-Low Leakage Monolithic CMOS Analog Multiplexers

ABSOLUTE MAXIMUM RATINGS

Voltage Referenced to V-

V+	+44V
GND	+25V
Digital Inputs (Note 1), V _S , V _D	-2V to (V+ + 2V)
Current (Any Terminal, Except S or D)	30mA
Continuous Current, S or D (pulsed at 1ms, 10% duty cycle max)	40mA

Operating Temperature Range

MAX328/329 C _ _	0°C to +70°C
MAX328/329 E _ _	-40°C to +85°C
MAX328/329 M _ _	-55°C to +125°C

Power Dissipation (Package) (Note 1)

16-Pin CERDIP (Note 2)	900mW
16-Pin Plastic DIP (Note 3)	470mW
16-Pin Wide SO (Note 4)	750mW
16-Pin QFN (Note 5)	1538mW
Storage Temperature	-65°C to +150°C

Note 1: All leads soldered or welded to PC board.

Note 2: Derate 12mW/°C above +75°C.

Note 3: Derate 6.3mW/°C above +75°C.

Note 4: Derate 10mW/°C above +75°C.

Note 5: Derate 19.2mW/°C above +75°C.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ELECTRICAL CHARACTERISTICS

(V+ = +15V, V- = -15V, GND = 0V, T_A = +25°C, unless otherwise noted.)

PARAMETER		SYMBOL	TEST CONDITIONS		LIMITS						UNITS
					MAX328M MAX329M			MAX328C/E MAX329C/E			
					MIN	TYP	MAX	MIN	TYP	MAX	
SWITCH											
Analog Signal Range		V _{ANALOG}			±15		±15				V
Drain-Source On-Resistance		R _{DS(ON)}	V _D = 10V, I _S = 100μA	Seq. each switch on, V _{AL} = 0.8V, V _{AH} = 2.4V	1.5 2.5		1.5 3.5				kΩ
			V _D = -10V, I _S = 100μA		1.0 2.5		1.0 3.5				
Greatest Change in Drain-Source On-Resistance Between Channels		ΔR _{DS(ON)}	$R_{DS(ON)} = \frac{R_{DS(ON) \text{ Max}} - R_{DS(ON) \text{ Min}}}{R_{DS(ON) \text{ Ave}}}$		2		2				%
Source Off-Leakage Current (Note 6)		I _{S(OFF)}	V _S = 10V, V _D = -10V	V _{EN} = 0V	0.1 ±10		0.1 ±10				pA
			V _S = -10V, V _D = 10V		0.3 ±10		0.3 ±10				
Drain Off-Leakage Current (Note 6)	MAX328	I _{D(OFF)}	V _D = 10V, V _S = -10V	V _{EN} = 0V	0.3 ±10		0.3 ±10				pA
	MAX329		V _D = -10V, V _S = 10V		1.0 ±10		1.0 ±10				
			V _D = 10V, V _S = -10V		0.3 ±10		0.3 ±10				
			V _D = -10V, V _S = 10V		0.5 ±10		0.5 ±10				
Drain On-Leakage Current (Note 6)	MAX328	I _{D(ON)}	V _S (all) = V _D = 10V	Seq. each switch on, V _{AL} = 0.8V, V _{AH} = 2.4V	3.0 ±10		3.0 ±10				pA
	MAX329		V _S (all) = V _D = -10V		2.0 ±10		2.0 ±10				
			V _S (all) = V _D = 10V		1.5 ±10		1.5 ±10				
			V _S (all) = V _D = -10V		1.0 ±10		1.0 ±10				

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MAX328/MAX329

ELECTRICAL CHARACTERISTICS (continued)

(V₊ = +15V, V₋ = -15V, GND = 0V, T_A = +25°C, unless otherwise noted.)

PARAMETER	SYMBOL	TEST CONDITIONS		LIMITS						UNITS
				MAX328M MAX329M			MAX328C/E MAX329C/E			
				MIN	TYP	MAX	MIN	TYP	MAX	
INPUT										
Address Input Current, Input Voltage High	I _{AH}	V _A = 2.4V		0.001	±1		0.001	±1	μA	
		V _A = 15V		0.001	±1		0.001	±1		
Address Input Current, Input Voltage Low	I _{AL}	V _{EN} = 2.4V	All V _A = 0V	0.001	±1		0.001	±1	μA	
		V _{EN} = 0V		0.001	±1		0.001	±1		
DYNAMIC										
Switching Time of Multiplexer	t _{TRANSITION}	See Figure 1		1.0			1.5			μs
Break-Before-Make Interval	t _{OPEN}	See Figure 2		0.2			0.2			μs
Enable Turn-On Time	t _{ON(EN)}	See Figure 3		1.0			1.5			μs
Enable Turn-Off Time	t _{OFF(EN)}	See Figure 3		0.7			1.0			μs
Off-Isolation	OIRR	V _{EN} = 0V, R _L = 1kΩ, C _L = 15pF, V _S = 7V _{RMS} , f = 500kHz		84			84			dB
Source Off-Capacitance	C _{S(OFF)}	V _S = 0V	V _{EN} = 0V, f = 1MHz	1.8			1.8			pF
Drain Off- Capacitance	MAX328	C _{D(OFF)}	V _D = 0V	8.0			8.0			pF
	MAX329			4.0			4.0			
Charge Injection (Note 8)	Q _(INJ)	V _A = +10V		1			1			pc
		V _A = 0V		2 5			2 5			
		V _A = -10V		4			4			
SUPPLY										
Positive Supply Current	I ₊	V _{EN} = 2.4V	V _A = 0V/5V	4.5 200			4.5 200			μA
Negative Supply Current	I ₋	V _{EN} = 2.4V	V _A = 0V/5V	1 -100			1 -100			μA
Power-Supply Range for Continuous Operation (Note 7)	V _{OP}			±5 ±18			±5 ±18			V

Note 6: All leakage parameters are 100% tested at maximum rated operating temperature, i.e., +70°C, +85°C, +125°C, and guaranteed by correlation at +25°C.

Note 7: Electrical characteristics, such as On-Resistance, change when power supplies other than ±15V are used. Power-supply range is a design characteristic, not production tested.

Note 8: Guaranteed by design.

Typical values are for DESIGN AID ONLY, not guaranteed or subject to production testing.

Ultra-Low Leakage Monolithic CMOS Analog Multiplexers

ELECTRICAL CHARACTERISTICS (Over Temperature)

(V+ = +15V, V- = -15V, GND = 0V, T_A = T_{MIN} to T_{MAX}, unless otherwise noted.)

PARAMETER	SYMBOL	TEST CONDITIONS		LIMITS						UNITS
				MAX328M MAX329M			MAX328C/E MAX329C/E			
				MIN	TYP	MAX	MIN	TYP	MAX	
SWITCH										
Analog Signal Range		V _{ANALOG}				±15		±15		V
Drain-Source On-Resistance		R _{DS(ON)}	V _D = 10V, I _S = 100μA	Seq. each switch on, V _{AL} = 0.8V, V _{AH} = 2.4V	2.2 4		1.9 5		kΩ	
			V _D = -10V, I _S = 100μA		1.5 4		1.2 5			
Source Off-Leakage Current (Note 9)		I _{S(OFF)}	V _S = 10V, V _D = -10V	V _{EN} = 0V	±5		±5		nA	
			V _S = -10V, V _D = 10V		±5		±5			
Drain Off-Leakage Current (Note 9)	MAX328	I _{D(OFF)}	V _D = 10V, V _S = -10V	V _{EN} = 0V	±20		±20		nA	
	MAX329		V _D = -10V, V _S = 10V		±20		±20			
			V _D = 10V, V _S = -10V		±10		±10			
			V _D = -10V, V _S = 10V		±10		±10			
Drain On-Leakage Current (Note 9)	MAX328	I _{D(ON)}	V _S (all) = V _D = 10V	Seq. each switch on, V _{AL} = 0.8V, V _{AH} = 2.4V	±20		±20		nA	
	MAX329		V _S (all) = V _D = -10V		±20		±20			
			V _S (all) = V _D = 10V		±10		±10			
			V _S (all) = V _D = -10V		±10		±10			
INPUT										
Address Input Current, Input Voltage High		I _{AH}	V _A = 2.4V		0.01 ±1		0.01 ±1		μA	
			V _A = 15V		0.01 ±1		0.01 ±1			
Address Input Current, Input Voltage Low		I _{AL}	V _{EN} = 2.4V	All V _A = 0V	0.01 ±1		0.01 ±1		μA	
			V _{EN} = 0V		0.01 ±1		0.01 ±1			

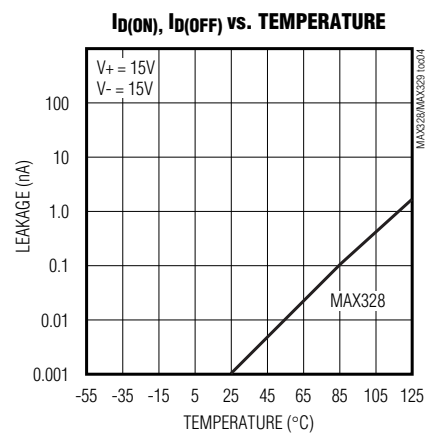
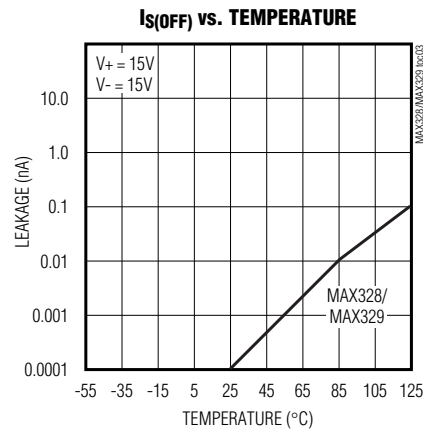
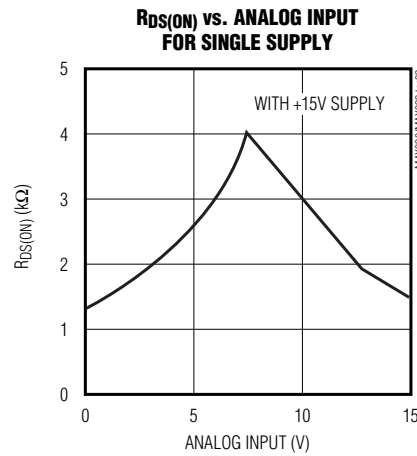
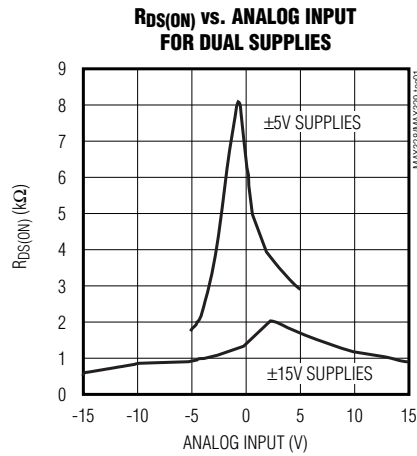
Note 9: Leakage parameters are 100% tested at maximum rated operating temperature, i.e., +70°C, etc.

Ultra-Low Leakage Monolithic CMOS Analog Multiplexers

Typical Operating Characteristics

($T_A = +25^\circ\text{C}$, unless otherwise noted.)

MAX328/MAX329



Ultra-Low Leakage Monolithic CMOS Analog Multiplexers

Pin Description

PIN				NAME	FUNCTION
MAX328		MAX329			
DIP/SO	QFN	DIP/SO	QFN		
1, 15, 16	15, 14, 13	—	—	A0, A2, A1	Address Input
—	—	1, 16	15, 14	A0, A1	Address Input
2	16	2	16	EN	Enable
3	1	3	1	V-	Negative-Supply Voltage Input
4–7	2–5	—	—	S1–S4	Analog Inputs—Bidirectional
—	—	4–7	2–5	S1A–S4A	Analog Inputs—Bidirectional
8	6	—	—	D	Analog Outputs—Bidirectional
—	—	8, 9	6, 7	DA, DB	Analog Outputs—Bidirectional
9–12	7–10	—	—	S8–S5	Analog Inputs—Bidirectional
—	—	10–13	8–11	S4B–S1B	Analog Inputs—Bidirectional
13	11	14	12	V+	Positive-Supply Voltage Input
14	12	15	13	GND	Ground

Truth Table—MAX328

A ₂	A ₁	A ₀	EN	ON SWITCH
X	X	X	0	None
0	0	0	0	1
0	0	1	1	2
0	1	0	1	3
0	1	1	1	4
1	0	0	1	5
1	0	1	1	6
1	1	0	1	7
1	1	1	1	8

Truth Table—MAX329

A ₁	A ₀	EN	ON SWITCH
X	X	0	None
0	0	1	1
0	1	1	2
1	0	1	3
1	1	1	4

Note: Logic “0” = $V_{AL} \leq 0.8V$, Logic “1” = $V_{AH} \geq 2.4V$

Ultra-Low Leakage Monolithic CMOS Analog Multiplexers

MAX328/MAX329

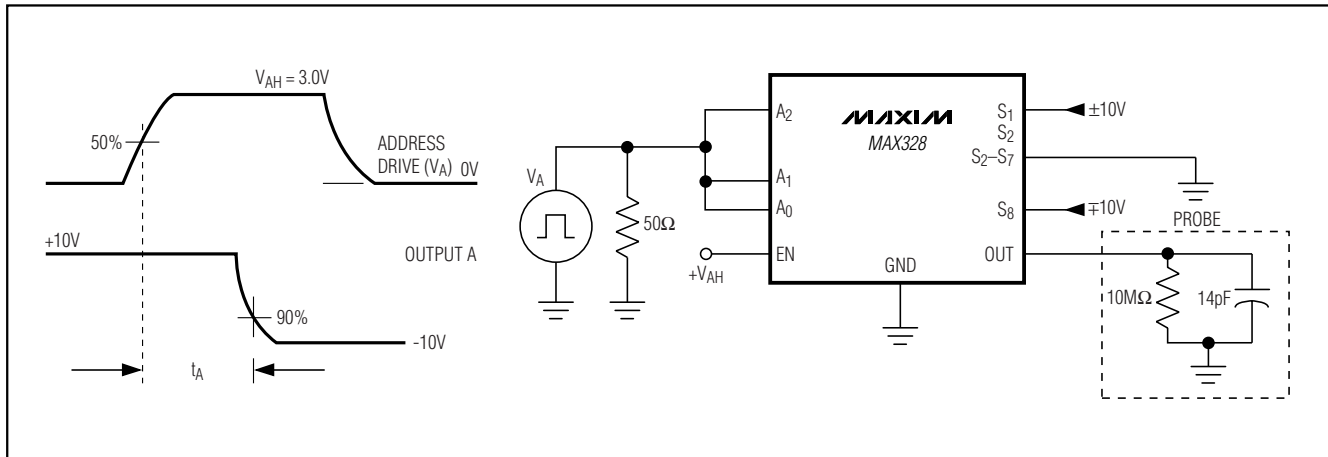


Figure 1. Access Time vs. Logic Level (High)

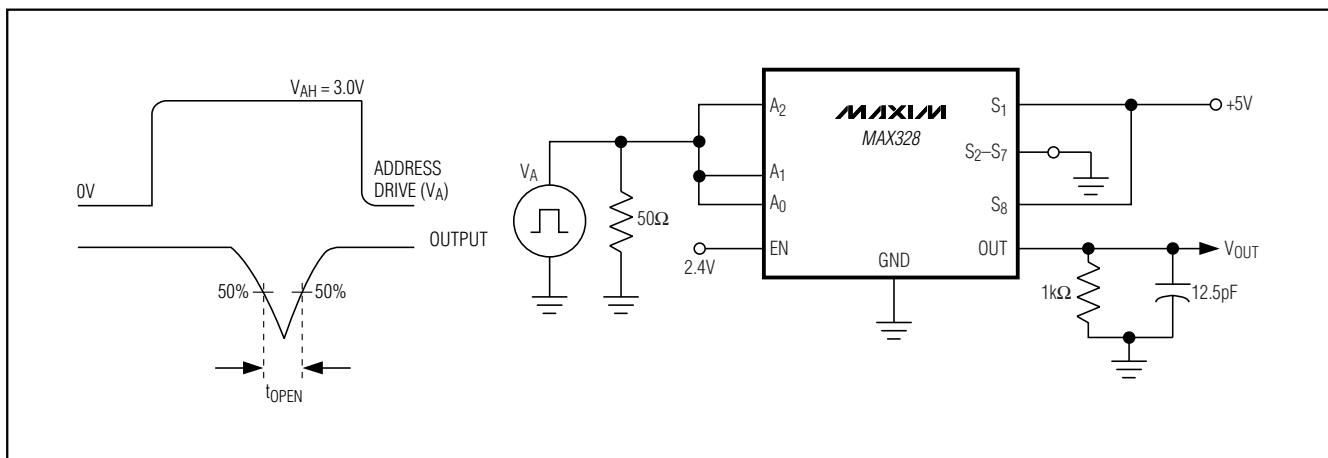


Figure 2. Break-Before-Make Delay (t_{OPEN})

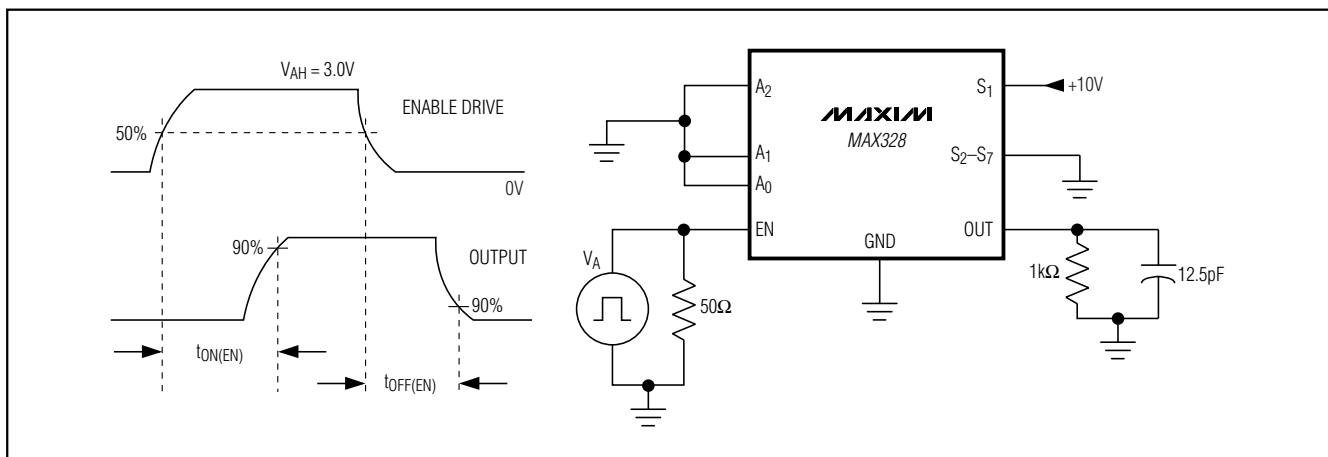


Figure 3. Enable Delay ($t_{ON(EN)}$, $t_{OFF(EN)}$)

Ultra-Low Leakage Monolithic CMOS Analog Multiplexers

Applications Information

Figure 4 is a typical circuit for converting the MAX328/MAX329 into a fault-tolerant mux. In this application, the internal diodes limit the voltage at the MAX328 input to $\pm 15.7V$ ($\pm 15V$ supplies). No external diodes need to be added with the MAX328/MAX329, unlike conventional multiplexers requiring external diodes.

The resistors, R, need to be $39k\Omega$ or higher to limit the power dissipation in the resistor when a 120V AC fault occurs (i.e., power dissipation is $(120-16)^2/39k\Omega$ or 0.28W. This is why a 1/2W resistor is needed). The circuit withstands an indefinite fault to a 120V AC line with no damage to any component.

In addition to allowing fault-protection, the guaranteed low leakage of the MAX328/MAX329 also reduces signal errors. The circuit in Figure 4 produces an error voltage of $10pA$ (max leakage) $\times 39k\Omega$ or $0.39\mu V$ at room temperature and $39\mu V$ at $+125^\circ C$. Therefore, for 10V signals, the MAX328/MAX329 allows 17-bit resolution ($38\mu V = 1LSB$) over the full temperature range.

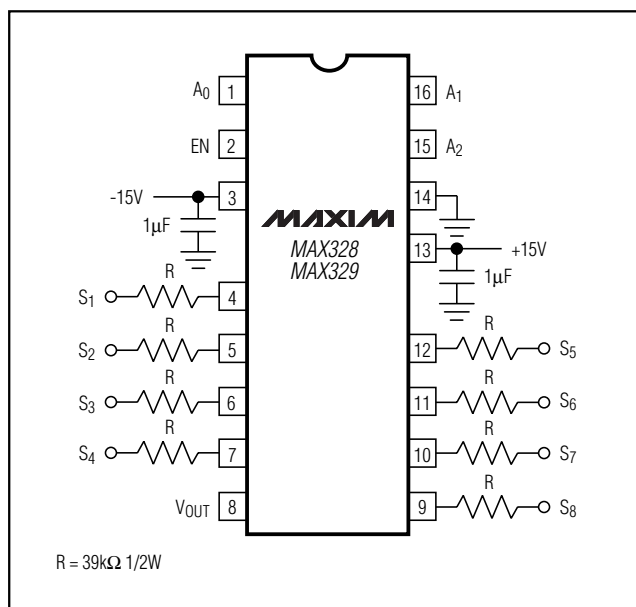
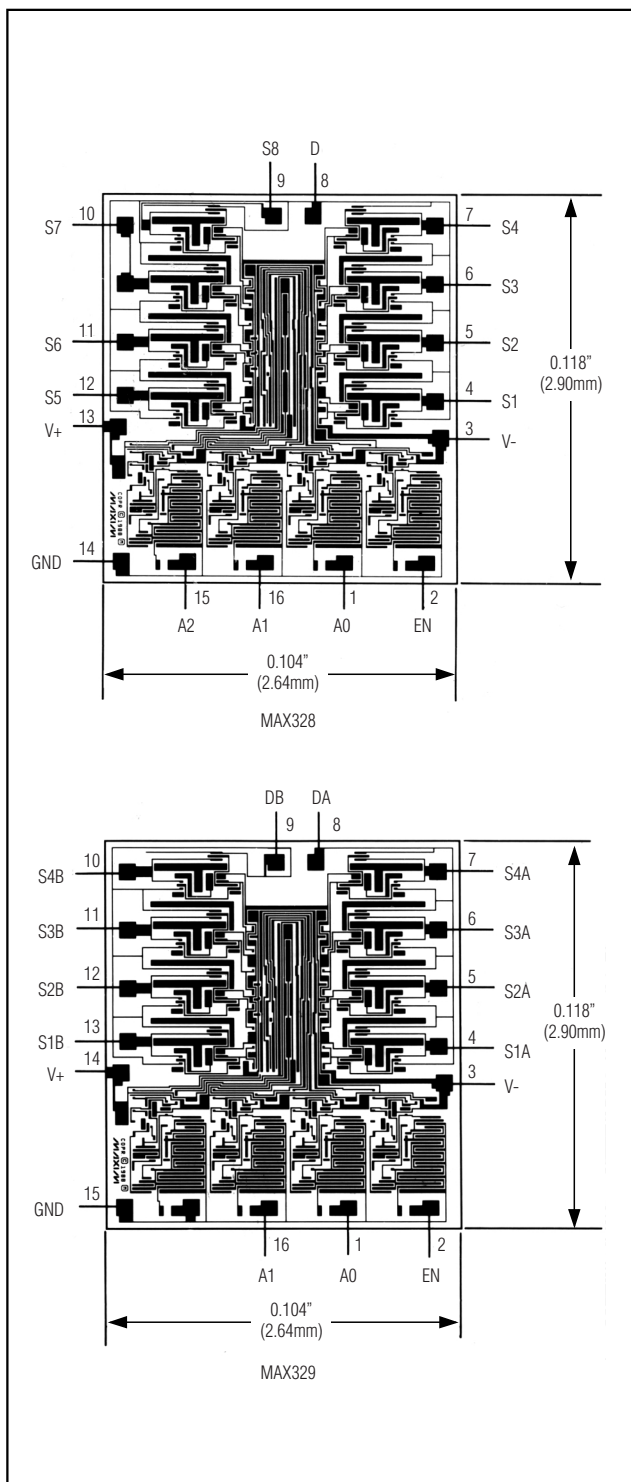


Figure 4. Fault-Tolerant MUX (indefinitely withstands 120V AC fault voltages)

Chip Topographies



Ultra-Low Leakage Monolithic CMOS Analog Multiplexers

MAX328/MAX329

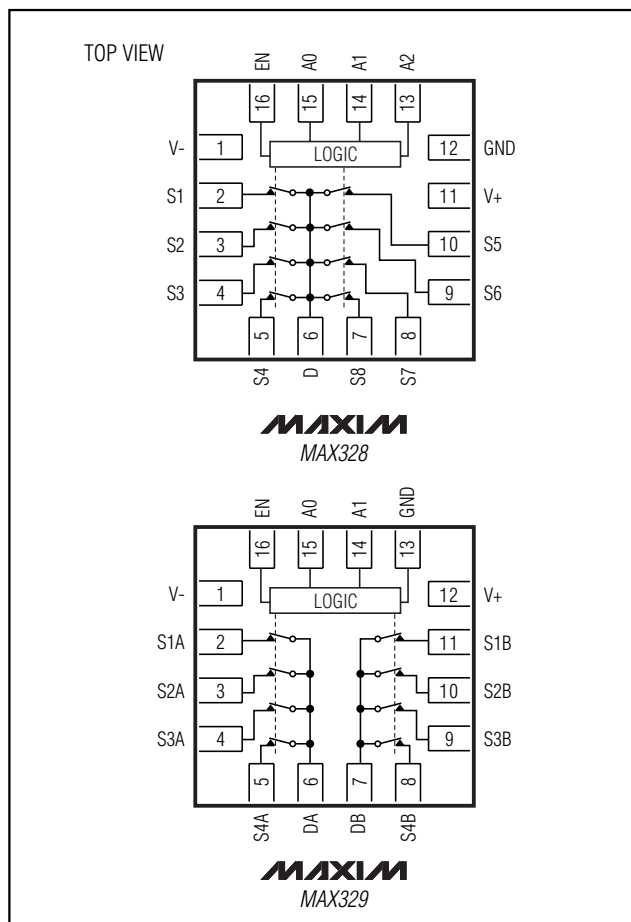
Ordering Information (continued)

PART	TEMP RANGE	PIN-PACKAGE
MAX329CPE	0°C to +70°C	16 Plastic DIP
MAX329CWE	0°C to +70°C	16 Wide SO
MAX329CJE	0°C to +70°C	16 CERDIP
MAX329C/D	0°C to +70°C	Dice*
MAX329EGE	-40°C to +85°C	16 QFN
MAX329EPE	-40°C to +85°C	16 Plastic DIP
MAX329EWE	-40°C to +85°C	16 Wide SO
MAX329EJE	-40°C to +85°C	16 CERDIP**
MAX329MJE	-55°C to +125°C	16 CERDIP**

*Contact factory for dice specifications.

**Contact factory for availability. Substrate may be allowed to float or be connected to V+.

Pin Configurations (continued)



Package Information

For the latest package outline information, go to www.maxim-ic.com/packages.

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