THERMAL CHARACTERISTICS

Characteristic		Value	Unit	
Thermal Resistance, Junction-to-Case	$R_{ heta JC}$	2.2	°C/W	
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	62.5	°C/W	
Maximum Lead Temperature for Soldering Purposes 1/8" from Case for 10 Seconds	TL	260	°C	

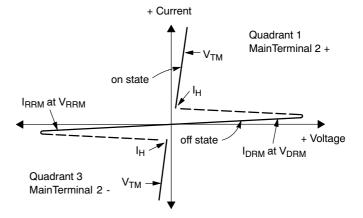
ELECTRICAL CHARACTERISTICS (T_{.J} = 25°C unless otherwise noted; Electricals apply in both directions)

Characteristic	Symbol	Min	Тур	Max	Unit	
OFF CHARACTERISTICS				•		
Peak Repetitive Blocking Current (V_D = Rated V_{DRM} , V_{RRM} ; Gate Open) T_J = 25°C T_J = 125°C			-	-	0.01 2.0	mA
ON CHARACTERISTICS						
Peak On-State Voltage (Note 2), (I _{TM} = ±11 A Peak)	V_{TM}	-	1.2	1.6	V	
Gate Trigger Current (Continuous DC) (V_D = 12 V, R_L = 100 Ω) MT2(+), G(+) MT2(+), G(-) MT2(-), G(-)		l _{GT}	5.0 5.0 5.0	13 16 18	35 35 35	mA
Holding Current, (V _D = 12 V, Gate Open, Initiating Current = ±150 mA)			-	20	40	mA
Latching Current (V_D = 24 V, I_G = 35 mA), MT2(+), G(+); MT2(-), G(-) MT2(+), G(-)		ΙL	-	20 30	50 80	mA
Gate Trigger Voltage (V_D = 12 V, R_L = 100 Ω) MT2(+), G(+) MT2(+), G(-) MT2(-), G(-)		V _{GT}	0.5 0.5 0.5	0.69 0.77 0.72	1.5 1.5 1.5	V
Gate Non-Trigger Voltage (V_D = 12 V, R_L = 100 Ω , T_J = 125°C) MT2(+), G(+); MT2(+), G(-); MT2(-), G(-)	V_{GD}	0.2	-	-	V	
DYNAMIC CHARACTERISTICS		•	•	•		
Rate of Change of Commutating Current See Figure 10.(V_D = 400 V, I_{TM} = 4.4 A, Commutating dv/dt = 18 V/ μ s,Gate Open, T_J = 125°C, f = 250 Hz, No Snubber) C_L = 10 μ F L_L = 40 mH		(di/dt) _c	6.5	-	-	A/ms
Critical Rate of Rise of Off-State Voltage (V_D = Rated V_{DRM} , Exponential Waveform, Gate Open, T_J = 125°C)			250	-	-	V/μs

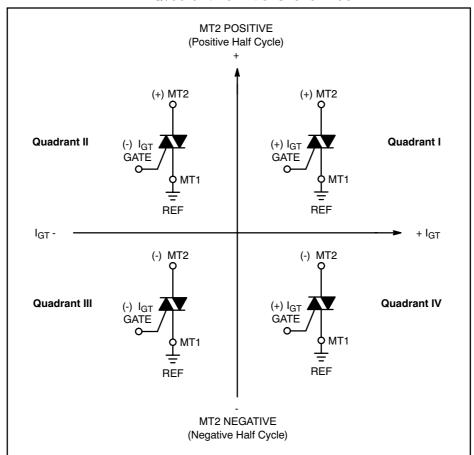
Indicates Pulse Test: Pulse Width ≤ 2.0 ms, Duty Cycle ≤ 2%.

Voltage Current Characteristic of Triacs (Bidirectional Device)

Symbol	Parameter
V_{DRM}	Peak Repetitive Forward Off State Voltage
I _{DRM}	Peak Forward Blocking Current
V_{RRM}	Peak Repetitive Reverse Off State Voltage
I _{RRM}	Peak Reverse Blocking Current
V_{TM}	Maximum On State Voltage
I _H	Holding Current



Quadrant Definitions for a Triac



All polarities are referenced to MT1.

With in-phase signals (using standard AC lines) quadrants I and III are used.

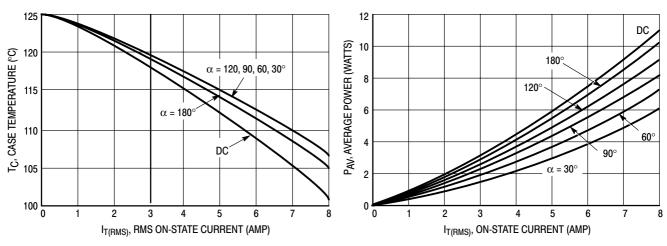
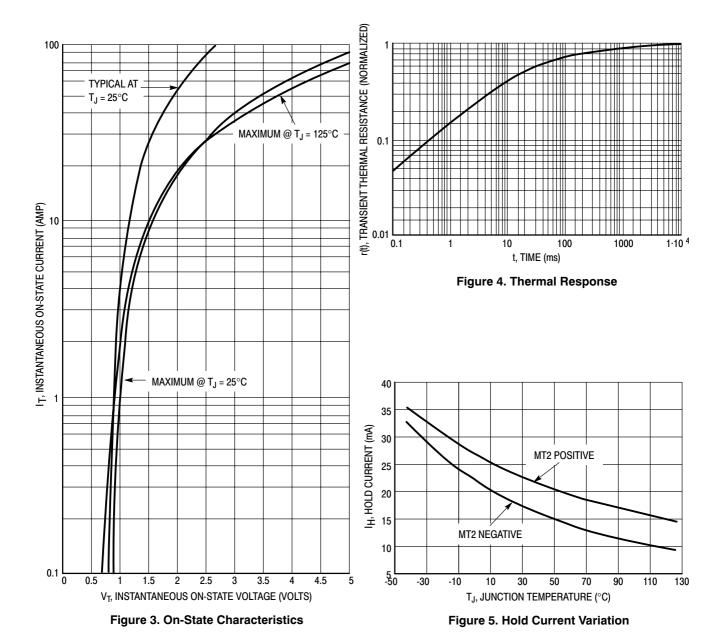
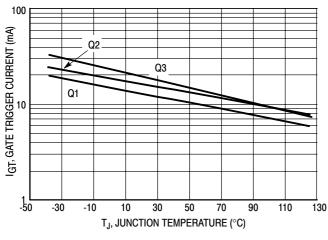


Figure 1. RMS Current Derating

Figure 2. On-State Power Dissipation



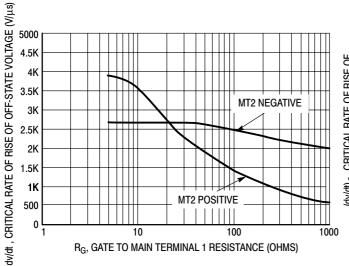
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Q2 0.95 VGT, GATE TRIGGER VOLTAGE (VOLT) 0.9 Q3 0.85 0.8 075 0.7 Q1 0.65 0.6 0.55 0.5 0.45 0.4 **-**-50 -30 50 70 90 110 30 130 T_J, JUNCTION TEMPERATURE (°C)

Figure 6. Gate Trigger Current Variation

Figure 7. Gate Trigger Voltage Variation



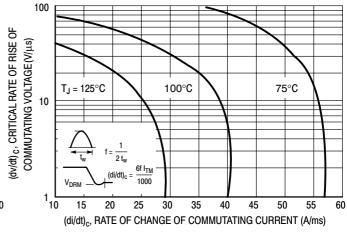
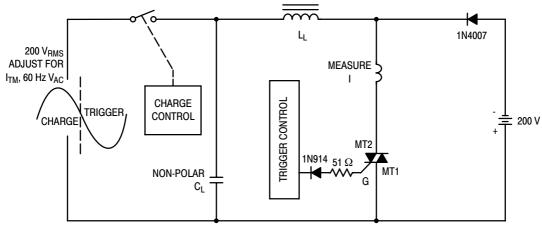


Figure 8. Critical Rate of Rise of Off-State Voltage (Exponential)

Figure 9. Critical Rate of Rise of Commutating Voltage

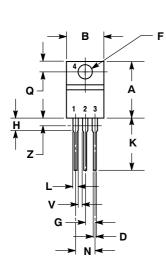


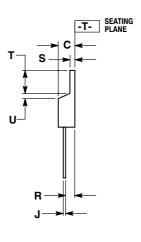
Note: Component values are for verification of rated (di/dt)_c. See AN1048 for additional information.

Figure 10. Simplified Test Circuit to Measure the Critical Rate of Rise of Commutating Current (di/dt)

PACKAGE DIMENSIONS

TO-220 CASE 221A-09 **ISSUE AE**





NOTES:

- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 CONTROLLING DIMENSION: INCH.
- DIMENSION Z DEFINES A ZONE WHERE ALL BODY AND LEAD IRREGULARITIES ARE

	INCHES		MILLIMETERS	
DIM	MIN	MAX	MIN	MAX
Α	0.570	0.620	14.48	15.75
В	0.380	0.405	9.66	10.28
С	0.160	0.190	4.07	4.82
D	0.025	0.035	0.64	0.88
F	0.142	0.161	3.61	4.09
G	0.095	0.105	2.42	2.66
Н	0.110	0.155	2.80	3.93
J	0.014	0.025	0.36	0.64
K	0.500	0.562	12.70	14.27
L	0.045	0.060	1.15	1.52
N	0.190	0.210	4.83	5.33
Q	0.100	0.120	2.54	3.04
R	0.080	0.110	2.04	2.79
S	0.045	0.055	1.15	1.39
T	0.235	0.255	5.97	6.47
U	0.000	0.050	0.00	1.27
٧	0.045		1.15	
Z		0.080		2.04

STYLE 4:

- PIN 1. MAIN TERMINAL 1
 - MAIN TERMINAL 2
 - GATE 3.
 - MAIN TERMINAL 2

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