

# Absolute Maximum Ratings (Note 4) (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Symbol	Parameter		Rating	Unit
		LMV321	4.0	
ESD HBM	Human Body Model ESD Protection	LMV358	4.0	KV
		LMV324	4.5	
		LMV321	350	
ESD MM	Machine Model ESD Protection	LMV358	350	V
		LMV324	250	
	Differential Input Voltage		±Supply Voltage	V
V <sup>+</sup> -V <sup>-</sup>	Supply Voltage		5.5	V
	Output Short Circuit to V <sup>+</sup>		(Note 5)	
	Output Short Circuit to V		(Note 6)	
T <sub>ST</sub>	Storage Temperature		-65 to +150	°C
$T_J$	Maximum Junction Temperature		+150	°C

Notes:

- 5. Shorting output to V+ will adversely affect reliability.
- 6. Shorting output to V- will adversely affect reliability.

### Recommended Operating Conditions (@TA = +25°C, unless otherwise specified.)

Symbol	Parameter	Rating	Unit
V <sup>+</sup> -V <sup>-</sup>	Supply Voltage	2.7 to 5.5	V
T <sub>A</sub>	Operating Ambient Temperature Range	-40 to +125	°C

## **Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

### 2.7V DC Electrical Characteristics

Unless otherwise specified, all limits guaranteed for  $T_A = +25^{\circ}C$ ,  $V^{+} = 2.7V$ ,  $V^{-} = 0V$ ,  $V_{CM} = 1.0V$ ,  $V_{O} = V^{+}/2$  and  $R_L > 1$  M $\Omega$ .

Symbol	Parameter	Test Conditions	Min (Note 8)	Typ (Note 7)	Max (Note 8)	Unit
Vos	Input Offset Voltage			1.7	7	mV
TCVos	Input Offset Voltage Average Drift			5		μV/°C
I <sub>B</sub>	Input Bias Current			10		nA
Ios	Input Offset Current			5	50	nA
CMRR	Common Mode Rejection Ratio	$0V \le V_{CM} \le 1.7V$	50	63		dB
PSRR	Power Supply Rejection Ratio	$2.7V \le V^{+} \le 5V, V_{O} = 1V$	50	60		dB
V	Input Common-Mode Voltage Range	For CMDD > FO4D	0	-0.2		V
V <sub>CMR</sub>	MR Imput Common-wode voltage Range For CWRR	For CMRR ≥ 50dB		1.9	1.7	V
Vo	Output Swing	$R_1 = 10 \text{ k}\Omega \text{ to } 1.35\text{V}$	V <sup>+</sup> - 100	V <sup>+</sup> - 20		mV
VO	Output Swing			20	100	IIIV
		LMV321 Single amplifier		110	140	μΑ
Is	Supply Current	LMV358 Both amplifiers		190	340	μΑ
		LMV324 All four amplifiers		340	680	μΑ
	2.7V AC Electrical Characteristics  Unless otherwise specified, all limits guaranteed for $T_A$ = +25°C, $V^+$ = 2.7V, $V^-$ = 0V, $V_{CM}$ = 1.0V, $V_O$ = $V^+$ /2 and $R_L$ > 1 M $\Omega$ .					
GBWP	Gain-Bandwidth Product	C <sub>L</sub> = 200 pF		1		MHz
ФМ	Phase Margin			60		Deg
Gm	Gain Margin			10		dB
e <sub>n</sub>	Input-Referred Voltage Noise	f > 50 kHz		23		$\frac{nV}{\sqrt{H_z}}$

<sup>4.</sup> Absolute Maximum Ratings indicate limits beyond which damage to the device may occur. Operating Ratings indicate conditions for which the device is intended to be functional, but specific performance is not guaranteed. For guaranteed specifications and the test conditions, see the Electrical Characteristics.



## Electrical Characteristics (cont.) (@TA = +25°C, unless otherwise specified.)

#### **5V DC Electrical Characteristics**

Unless otherwise specified, all limits guaranteed for  $T_A = +25^{\circ}C$ , V' = 5V, V = 0V,  $V_{CM} = 2.0V$ ,  $V_O = V^{+}/2$  and  $R_L > 1$   $M\Omega$ .

Symbol	Parameter		st Condition		Min (Note 8)	Typ (Note 7)	Max (Note 8)	Unit													
.,	land Officet Vallega	T <sub>A</sub> = +25°C				1.7	7	\/													
Vos	Input Offset Voltage	T <sub>A</sub> = full range					9	mV													
TCV <sub>OS</sub>	Input Offset Voltage Average Drift					5		μV/°C													
		T <sub>A</sub> = +25°C				15	250														
lΒ	Input Bias Current	T <sub>A</sub> = full range					500	nA													
		T <sub>A</sub> = +25°C				5	50														
los	Input Offset Current	T <sub>A</sub> = full range					150	nA													
CMRR	Common Mode Rejection Ratio	$0V \le V_{CM} \le 4.0V$			50	65		dB													
DODD	Davis Oversky Daisetter Datie	$2.7V \le V^{+} \le 5V$			50	00		-10													
PSRR	Power Supply Rejection Ratio	$V_{O} = 1V, V_{CM} = 1V$			50	60		dB													
\/	Input Common Made Voltage Bange	For CMRR ≥ 50dB			0	-0.2		V													
V <sub>CMR</sub>	Input Common-Mode Voltage Range	FOI CIVIRR 2 300B				4.2	4.0	V													
۸	Large Signal Voltage Gain	B. = 2 kO (Note 0)	T <sub>A</sub> = +25°C		15	100		\//m\/													
$A_V$	Large Signal Voltage Gain	$R_L = 2 k\Omega \text{ (Note 9)}$ $T_A = \text{full range}$		ge	10			V/mV													
		R <sub>L</sub> = 2 kΩ to 2.5V	I liab laval	T <sub>A</sub> = +25°C	V <sup>+</sup> - 300	V <sup>+</sup> -50		mV													
			High level	T <sub>A</sub> = full range	V <sup>+</sup> - 400																
			Low level	T <sub>A</sub> = +25°C		50	300														
.,				T <sub>A</sub> = full range			400														
Vo	Output Swing	R <sub>L</sub> = 10 kΩ to 2.5V		T <sub>A</sub> = +25°C	V <sup>+</sup> - 100	V <sup>+</sup> -10															
			R <sub>L</sub> = 10 kΩ to 2.5V	R <sub>L</sub> = 10 kΩ to 2.5V	R <sub>L</sub> = 10 kΩ to 2.5V	R <sub>L</sub> = 10 kΩ to 2.5V	R <sub>L</sub> = 10 kΩ to 2.5V	R <sub>L</sub> = 10 kΩ to 2.5V	R <sub>L</sub> = 10 kΩ to 2.5V	R <sub>L</sub> = 10 kΩ to 2.5V	R <sub>L</sub> = 10 kΩ to 2.5V	R <sub>L</sub> = 10 kΩ to 2.5V	R <sub>L</sub> = 10 kΩ to 2.5V	R <sub>L</sub> = 10 kΩ to 2.5V	R <sub>L</sub> = 10 kΩ to 2.5V	High level	T <sub>A</sub> = full range	V <sup>+</sup> - 200			
																		T <sub>A</sub> = +25°C		10	180
			Low level $T_A = \text{full range}$			280															
		Sourcing, V <sub>O</sub> = 0V	1	1	5	60															
lo	Output Short Circuit Current	Sinking, V <sub>O</sub> = 5V			10	90		mA													
		LMV321 Single amp	lifier			110	140														
		111 (050 D II III	•	T <sub>A</sub> = +25°C		190	340														
Is	Supply Current	LMV358 Both amplif	iers	T <sub>A</sub> = full range			600	μA													
				T <sub>A</sub> = +25°C		340	680														
		LMV324 All four am	LMV324 All four amplifiers $T_A = \text{full range}$				1100														
		SOT353 (Note 10)				330		·													
		SOT25 (Note 10)			250																
Δ	Thermal Resistance Junction-to-	TSSOP-14 (Note 10	)			100		°C/W													
$\theta_{JA}$	Ambient	MSOP-8 (Note 10)				203															
		SO-8 (Note 10)				150															
		SO-14 (Note 10)				83															

### 5V AC Electrical Characteristics

Unless otherwise specified, all limits guaranteed for  $T_A = 25^{\circ}C$ ,  $V^{\dagger} = 5V$ , V = 0V,  $V_{CM} = 2.0V$ ,  $V_C = V^{\dagger}/2$  and  $R_L > 1$   $M\Omega$ . **Boldface** limits apply at the temperature extremes

Bolalace	Bolatace illinio apply at the temperature extremes.					
SR	Slew Rate	(Note 11)		1		V/µs
GBWP	Gain-Bandwidth Product	C <sub>L</sub> = 200pF		1		MHz
$\Phi_{m}$	Phase Margin			60		Deg
G <sub>m</sub>	Gain Margin			10		dB
en	Input-Referred Voltage Noise	f > 50 kHz		23		$\frac{\text{nV}}{\sqrt{\text{H}_{\text{z}}}}$

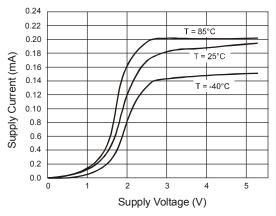
Notes:

<sup>7.</sup> Typical values represent the most likely parametric norm as determined at the time of characterization. Actual typical values may vary over time and will also depend on the application and configuration. The typical values are not tested and are not guaranteed on shipped production material. 8. All limits are guaranteed by testing or statistical analysis.

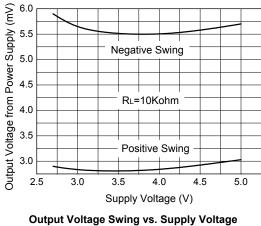
<sup>O. All initials are guaranteed by testing of statistical artistics.
O. R<sub>L</sub> is connected to V-. The output voltage is 0.5V ≤ V<sub>O</sub> ≤ 4.5V.
All numbers are typical, and apply for packages soldered directly onto a PC board in still air.
Connected as voltage follower with 3V step input. Number specified is the slower of the positive and negative slew rates.</sup> 

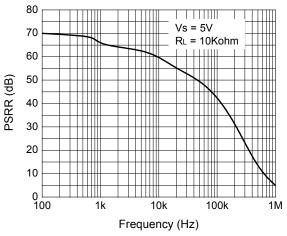


## Typical Performance Characteristics (V<sub>S</sub> = +5V, single supply, @T<sub>A</sub> = +25°C, unless otherwise specified.)

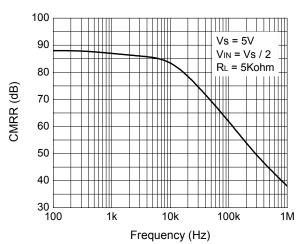


Supply Current vs. Supply Voltage

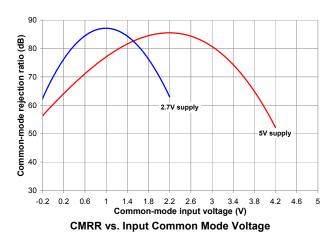


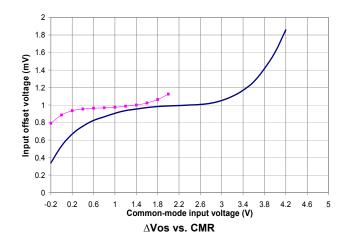


PSRR vs. Frequency

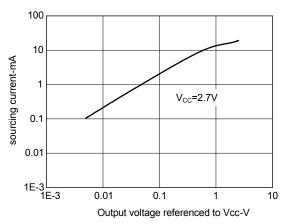


CMRR vs. Frequency

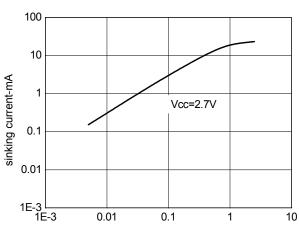




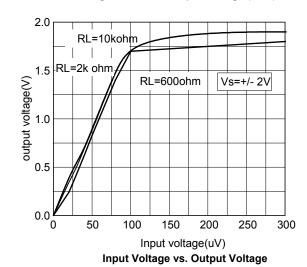




Sourcing Current vs. Output Voltage (2.7V)

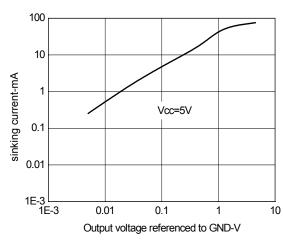


Output voltage referenced to GND-V Sinking Current vs. Output Voltage (2.7V)

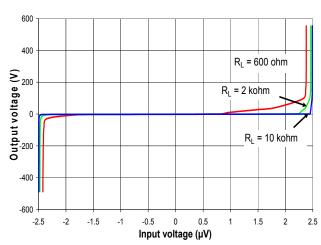


100 10 10 Vcc=5V 0.1 1E-3 1E-3 0.01 0.1 10 0.01 10 0.01 10 0.01 10 0.01 10 0.01 10 0.01 10 0.01 10 0.01 10 0.01 10 0.01 10 0.01 10 0.01 10 0.01 10 0.01 0.0

Sourcing Current vs. Output Voltage (5V)

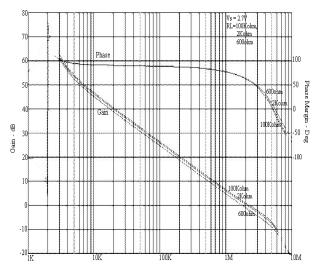


Sinking Current vs. Output Voltage (5V)

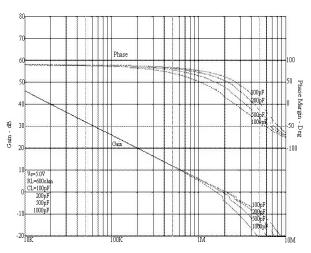


Output Voltage vs. Input Voltage

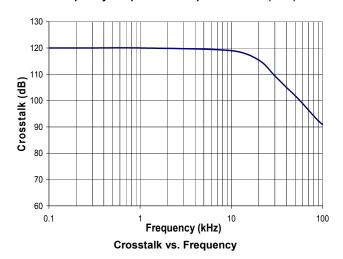


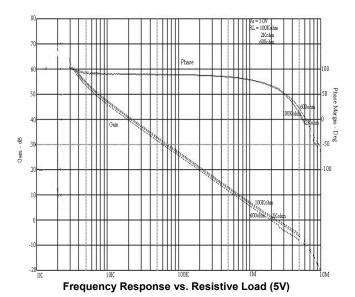


Frequency Response vs. Resistive Load (2.7V)



Frequency Response vs. Capacitive Load (2.7V)

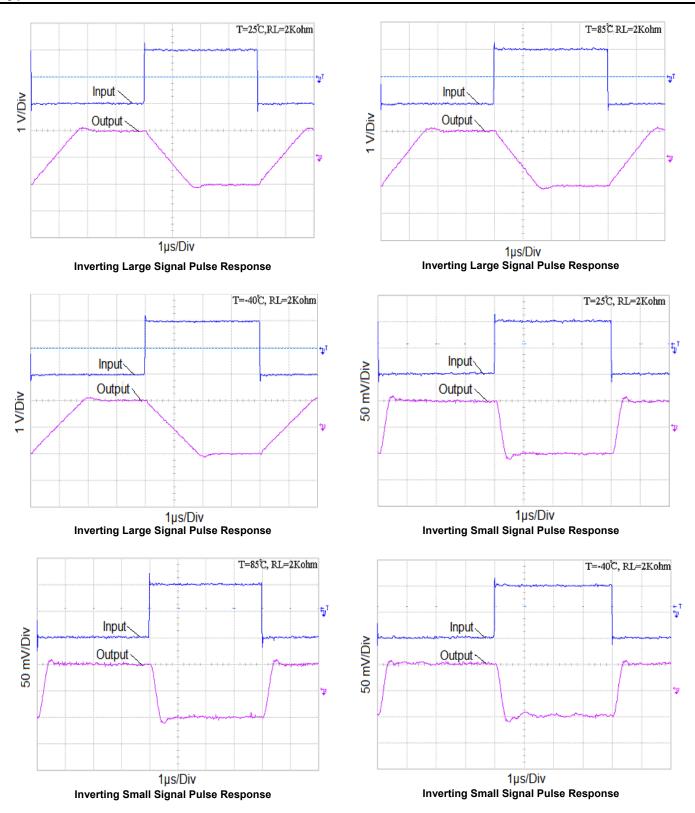




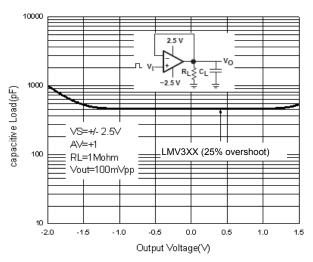
80
70
Phase
60
Phase
100pF
0
100pF
0
100pF

Frequency Response vs. Capacitive Load (5V)

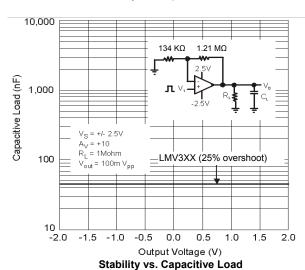








#### Stability vs. Capacitive Load



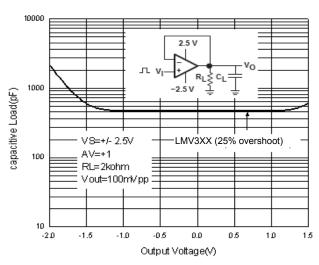
1.4 Rising Edge Av=+1
RL=10kohm
Vin=1Vpp

0.8

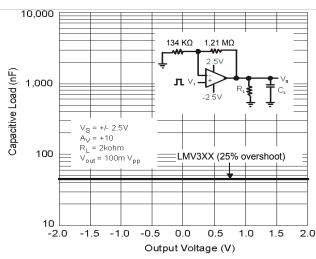
3.0

Supply Voltage (V)

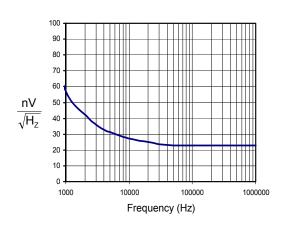
Slew Rate vs. Supply Voltage



Stability vs. Capacitive Load



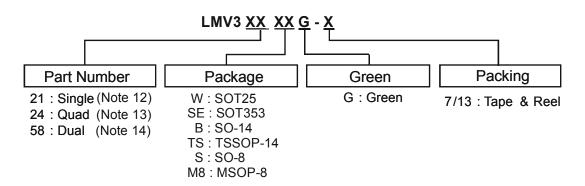
Stability vs. Capacitive Load



Input Voltage Noise



## **Ordering Information**



Ī	Part Number	Package Code	Packaging	7"/13" Ta	ape and Reel
	Part Number	Package Code	Packaging	Quantity	Part Number Suffix
Lead-free Green	LMV321WG-7	W	SOT25	3000/Tape & Reel	-7
Lead free Green	LMV321SEG-7	SE	SOT353	3000/Tape & Reel	-7
Lead free Green	LMV324BG-13	В	SO-14	2500/Tape & Reel	-13
Lead-free Green	LMV324TSG-13	TS	TSSOP-14	2500/Tape & Reel	-13
Pb Lead-free Green	LMV358SG-13	S	SO-8	2500/Tape & Reel	-13
Pb.	LMV358M8G-13	M8	MSOP-8	2500/Tape & Reel	-13

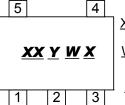
Notes:

- 12. LMV321 is only available for SOT25 and SOT353.
- 13. LMV324 is only available for SO-14 and TSSOP-14.
- 14. LMV358 is only available for SO-8 and MSOP-8.

## **Marking Information**

SOT25/SOT353





XX: Identification Code

Y : Year : 0~9

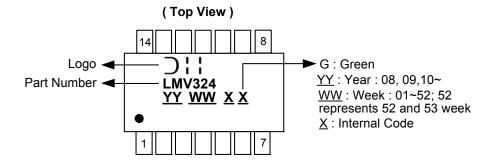
<u>W</u>: Week: A~Z: 1~26 week; a~z: 27~52 week; z represents 52 and 53 week <u>X</u>: A~Z: Green

Device	Package type	Identification Code
LMV321W	SOT25	BX
LMV321SE	SOT353	BY

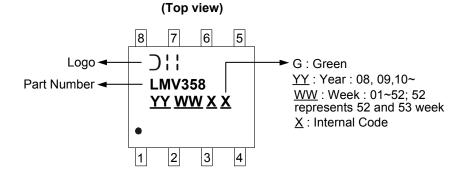


### Marking Information (cont.)

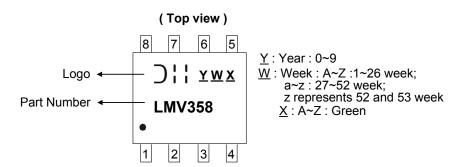
#### SO-14 / TSSOP-14



#### **SO-8**



#### MSOP-8

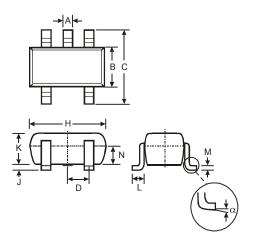




## Package Outline Dimensions (All dimensions in mm.)

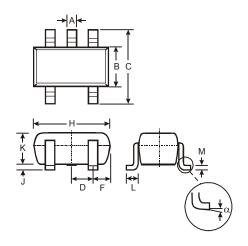
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.

#### SOT25



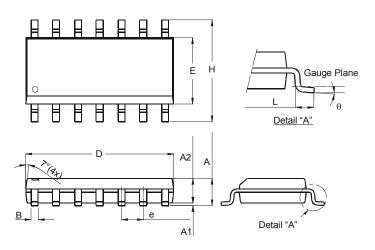
SOT25					
Dim	Min	Max	Тур		
Α	0.35	0.50	0.38		
В	1.50	1.70	1.60		
O	2.70	3.00	2.80		
D		_	0.95		
Н	2.90	3.10	3.00		
J	0.013	0.10	0.05		
K	1.00	1.30	1.10		
J	0.35	0.55	0.40		
M	0.10	0.20	0.15		
N	0.70	0.80	0.75		
α 0° 8° —					
AII D	imensi	ons in	mm		

#### **SOT353**



	SO	Г353	
Dim	Min	Max	Тур
Α	0.10	0.30	0.25
В	1.15	1.35	1.30
၁	2.00	2.20	2.10
D	0	.65 Typ	C
F	0.40	0.45	0.425
I	1.80	2.20	2.15
۲	0	0.10	0.05
Κ	0.90	1.00	1.00
٦	0.25	0.40	0.30
М	0.10	0.22	0.11
α	0°	8°	-
All	Dimens	ions i	n mm

### SO-14



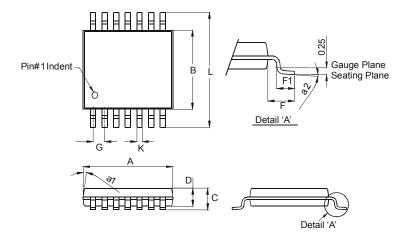
	SO-14				
Dim	Min	Max			
Α	1.47	1.73			
A1	0.10	0.25			
A2	1.45	Тур			
В	0.33	0.51			
D	8.53	8.74			
Е	3.80	3.99			
е	1.27	Тур			
Н	5.80	6.20			
L	0.38	1.27			
θ	0°	8°			
All Di	mensions	s in mm			



### Package Outline Dimensions (cont.) (All dimensions in mm.)

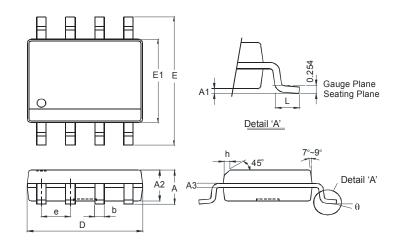
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.

#### TSSOP-14



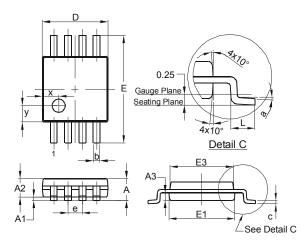
	TSSOP-14				
Dim	Min	Max			
a1	7° (	4X)			
a2	0°	8°			
Α	4.9	5.10			
В	4.30	4.50			
С	_	1.2			
D	8.0	1.05			
F	1.00	Тур			
F1	0.45	0.75			
G	0.65	Тур			
K	0.19	0.30			
L 6.40 Typ					
All Din	nensions	s in mm			

SO-8



	SO-8				
Dim	Min	Max			
Α	1	1.75			
A1	0.10	0.20			
A2	1.30	1.50			
A3	0.15	0.25			
b	0.3	0.5			
D	4.85	4.95			
Е	5.90	6.10			
E1	3.85	3.95			
е	1.27	Тур			
h	1	0.35			
L	0.62	0.82			
θ	0°	8°			
All Di	mensions	in mm			

#### MSOP-8



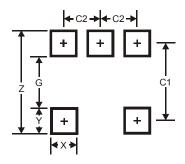
MSOP-8				
Dim	Min	Max	Тур	
Α	-	1.10	-	
<b>A1</b>	0.05	0.15	0.10	
A2	0.75	0.95	0.86	
<b>A3</b>	0.29	0.49	0.39	
q	0.22	0.38	0.30	
O	0.08	0.23	0.15	
D	2.90	3.10	3.00	
П	4.70	5.10	4.90	
E1	2.90	3.10	3.00	
E3	2.85	3.05	2.95	
е	ı	ı	0.65	
L	0.40	0.80	0.60	
а	0°	8°	4°	
Х	-	-	0.750	
у	-	-	0.750	
All Dimensions in mm				



### **Suggested Pad Layout**

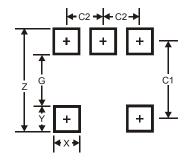
Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.

#### SOT25



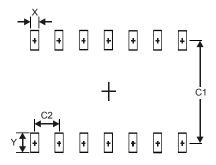
Dimensions	Value (in mm)	
Z	3.20	
G	1.60	
Х	0.55	
Υ	0.80	
C1	2.40	
C2	0.95	

### SOT353



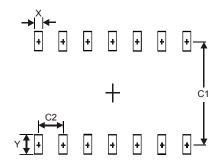
Dimensions	Value (in mm)
Z	2.5
G	1.3
X	0.42
Υ	0.6
C1	1.9
C2	0.65

### SO-14



Dimensions	Value (in mm)	
Х	0.60	
Y	1.50	
C1	5.4	
C2	1.27	

### TSSOP-14



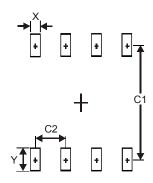
Dimensions	Value (in mm)
Х	0.45
Υ	1.45
C1	5.9
C2	0.65



## Suggested Pad Layout (cont.)

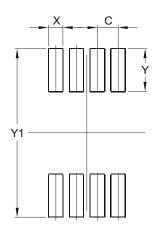
Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.

#### SO-8



Dimensions	Value (in mm)	
Х	0.60	
Y	1.55	
C1	5.4	
C2	1.27	

### MSOP-8



Dimensions	Value (in mm)
С	0.650
Х	0.450
Y	1.350
Y1	5.300



#### IMPORTANT NOTICE

DIODES INCORPORATED MAKES NO WARRANTY OF ANY KIND, EXPRESS OR IMPLIED, WITH REGARDS TO THIS DOCUMENT, INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A PARTICULAR PURPOSE (AND THEIR EQUIVALENTS UNDER THE LAWS OF ANY JURISDICTION).

Diodes Incorporated and its subsidiaries reserve the right to make modifications, enhancements, improvements, corrections or other changes without further notice to this document and any product described herein. Diodes Incorporated does not assume any liability arising out of the application or use of this document or any product described herein; neither does Diodes Incorporated convey any license under its patent or trademark rights, nor the rights of others. Any Customer or user of this document or products described herein in such applications shall assume all risks of such use and will agree to hold Diodes Incorporated and all the companies whose products are represented on Diodes Incorporated website, harmless against all damages.

Diodes Incorporated does not warrant or accept any liability whatsoever in respect of any products purchased through unauthorized sales channel. Should Customers purchase or use Diodes Incorporated products for any unintended or unauthorized application, Customers shall indemnify and hold Diodes Incorporated and its representatives harmless against all claims, damages, expenses, and attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized application.

Products described herein may be covered by one or more United States, international or foreign patents pending. Product names and markings noted herein may also be covered by one or more United States, international or foreign trademarks.

This document is written in English but may be translated into multiple languages for reference. Only the English version of this document is the final and determinative format released by Diodes Incorporated.

#### LIFE SUPPORT

Diodes Incorporated products are specifically not authorized for use as critical components in life support devices or systems without the express written approval of the Chief Executive Officer of Diodes Incorporated. As used herein:

- A. Life support devices or systems are devices or systems which:
  - 1. are intended to implant into the body, or
  - 2. support or sustain life and whose failure to perform when properly used in accordance with instructions for use provided in the labeling can be reasonably expected to result in significant injury to the user.
- B. A critical component is any component in a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or to affect its safety or effectiveness.

Customers represent that they have all necessary expertise in the safety and regulatory ramifications of their life support devices or systems, and acknowledge and agree that they are solely responsible for all legal, regulatory and safety-related requirements concerning their products and any use of Diodes Incorporated products in such safety-critical, life support devices or systems, notwithstanding any devices- or systems-related information or support that may be provided by Diodes Incorporated. Further, Customers must fully indemnify Diodes Incorporated and its representatives against any damages arising out of the use of Diodes Incorporated products in such safety-critical, life support devices or systems.

Copyright © 2013, Diodes Incorporated

www.diodes.com