

RATING AND CHARACTERISTIC CURVES L50ESDL5V0H4-2



FIG.1- 8/20us pulse waveform according to IEC 61000-4-5

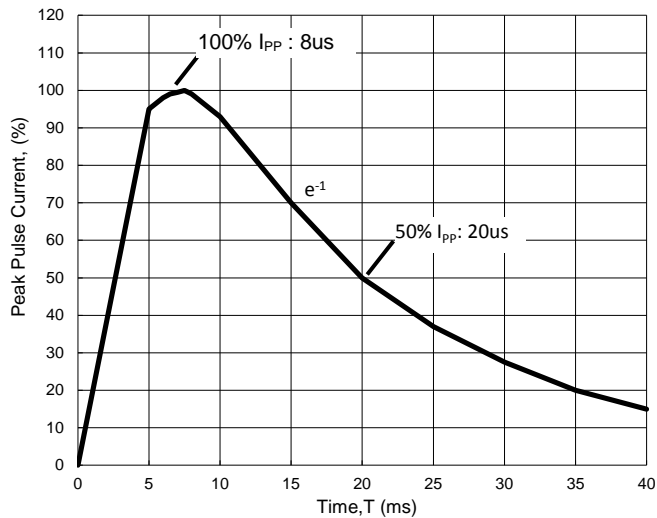


FIG.2- ESD pulse waveform according to IEC 61000-4-2

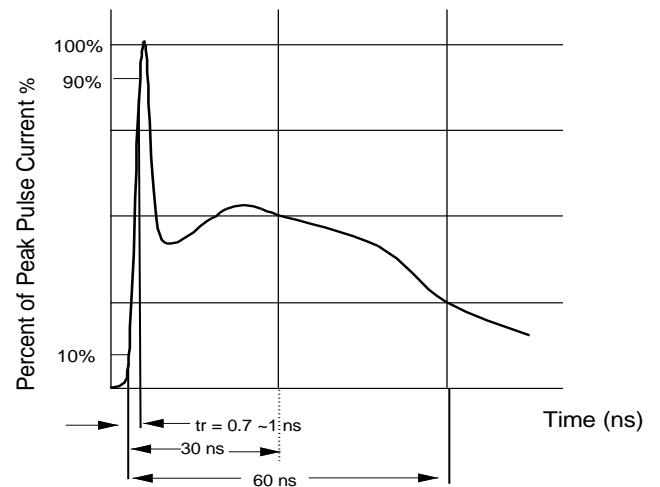


FIG3- power dissipation versus pulse time

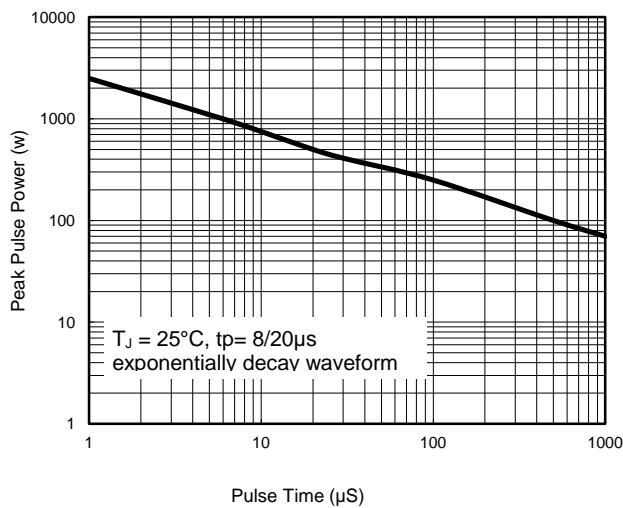


FIG4- peak pulse power versus T_J

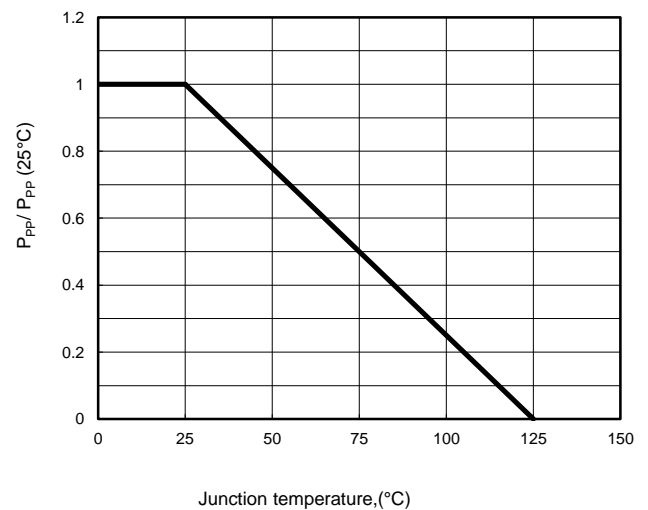


FIG.5- capacitance versus reverse voltage

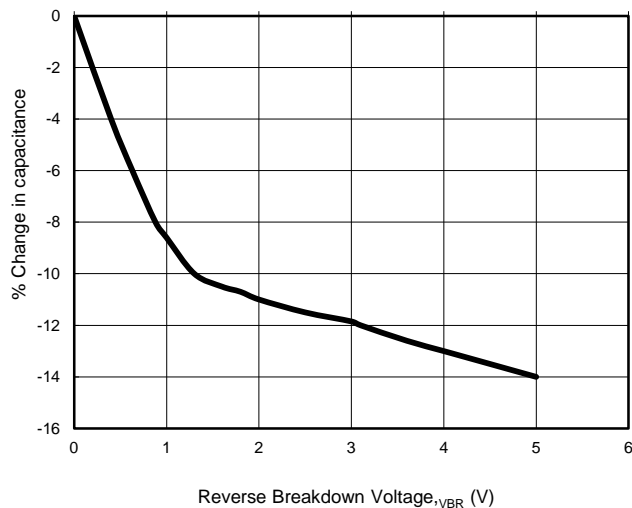
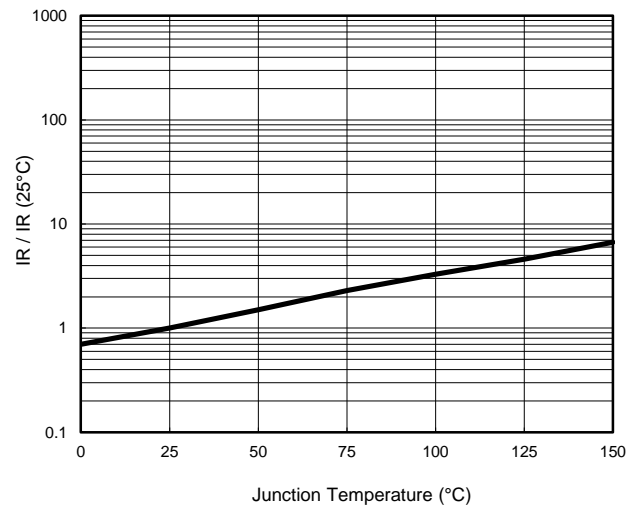


FIG.6- reverse leakage current versus T_J



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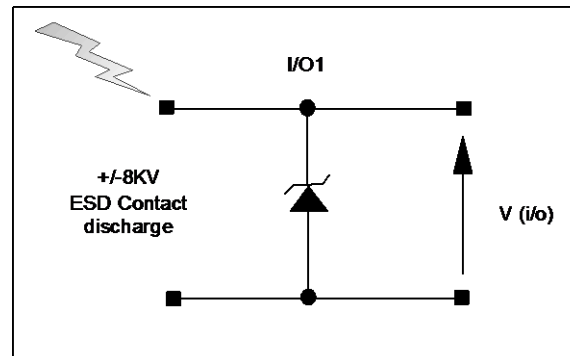


FIG.7- ESD Test Configuration

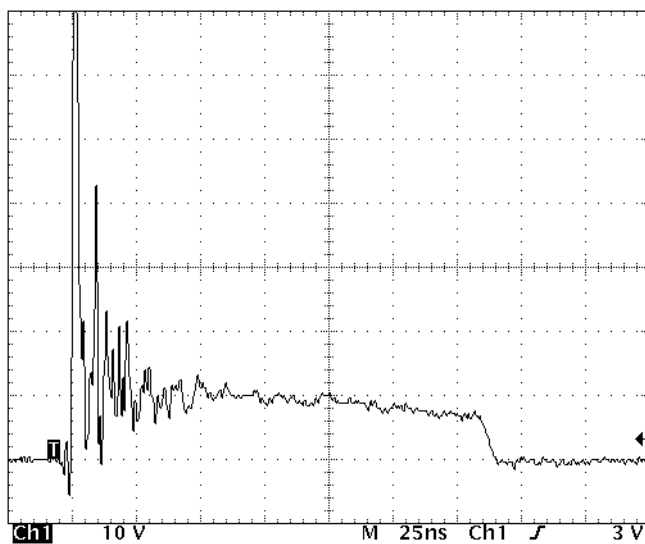


FIG.8- Clamped +8 kV ESD voltage waveform

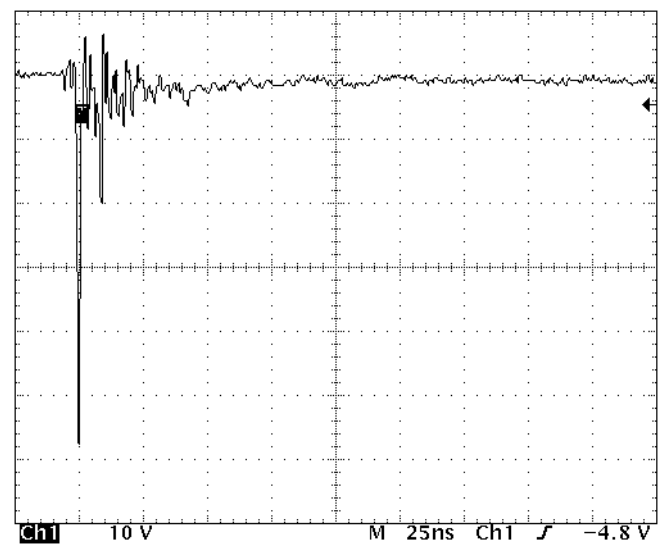
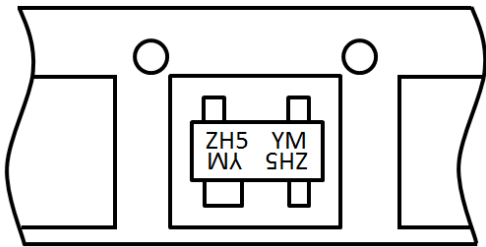


FIG.9- Clamped -8 kV ESD voltage waveform

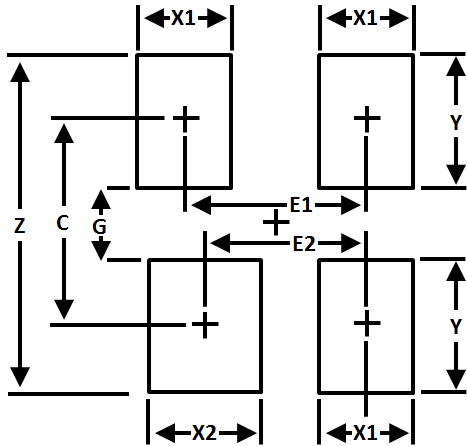
Marking & Orientation



Packaging Information

DEVICE	Q'TY/REEL (PCS)	REEL DIA. (INCH)	Q'TY/BOX (PCS)	Q'TY/CARTON (PCS)
L50ESDL5V0H4-2	3000	7	45000	180K

SOT-143 Soldering Pad Layout



DIM.	MILLIMETERS	INCHES
C	(2.20)	(0.086)
E1	1.92	0.075
E2	1.72	0.067
G	0.80	0.031
X1	1.00	0.039
X2	1.20	0.047
Y	1.40	0.055
Z	3.60	0.141

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