

Figure 1. 8/20 us pulse waveform according to IEC 61000-4-5

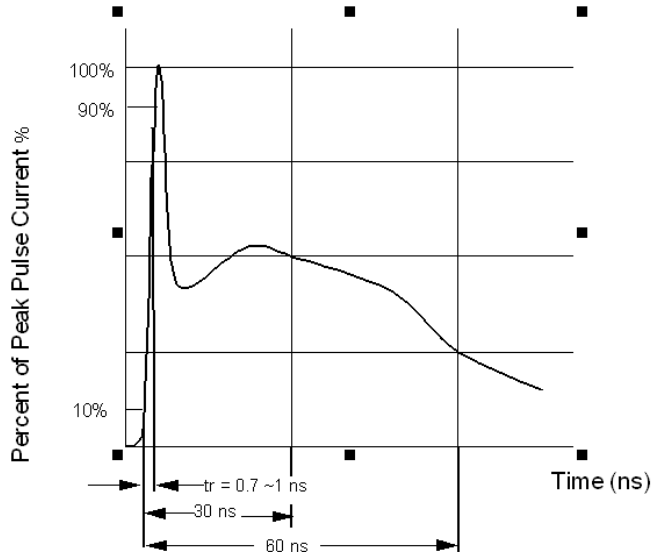


Figure 2. ESD pulse waveform according to IEC 61000-4-2

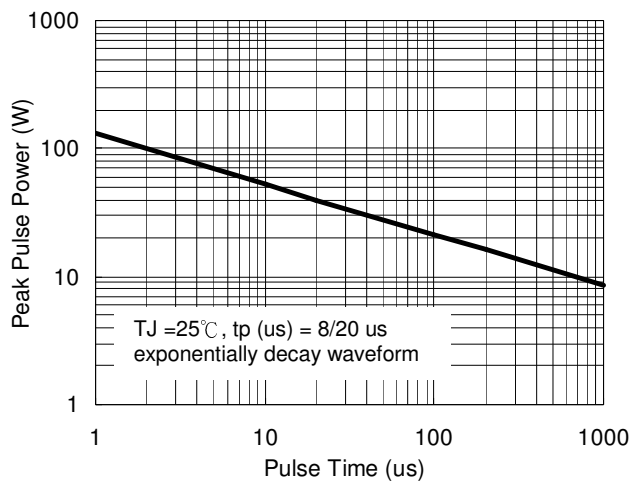


Figure 3. Power Dissipation versus Pulse Time

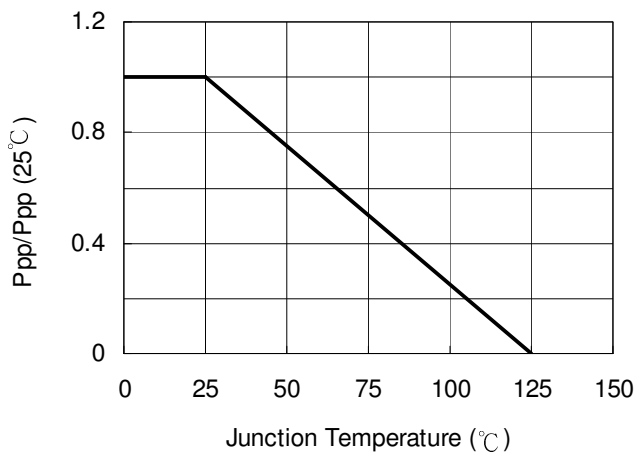


Figure 4. Peak pulse power versus T_J

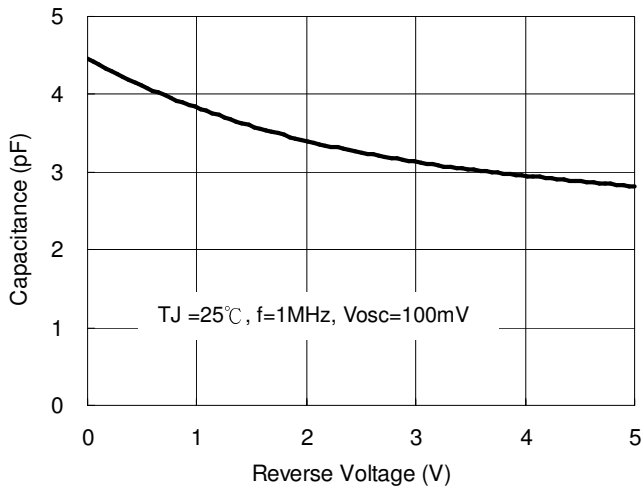


Figure 5. Typical Junction Capacitance

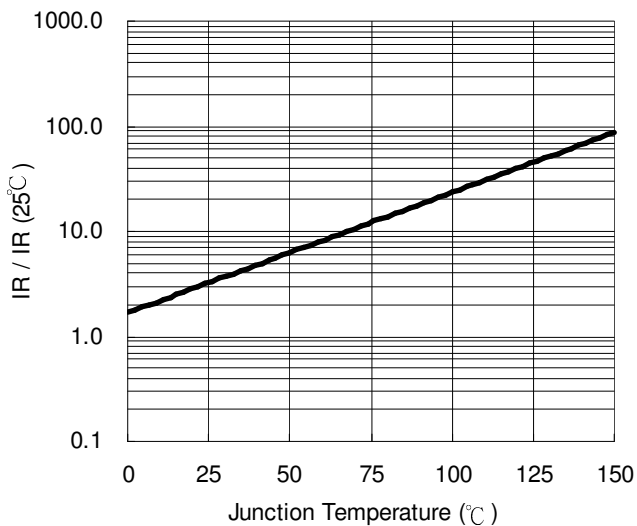


Figure 6. Reverse Leakage Current versus T_J

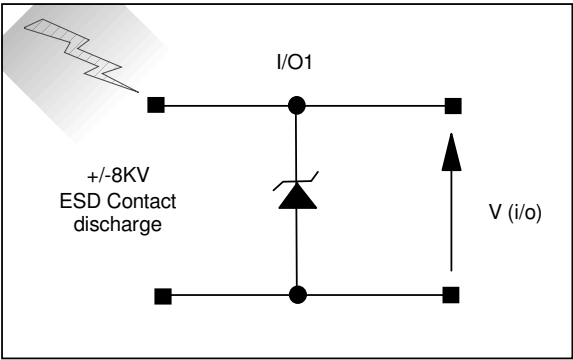


Figure 7. ESD Test Configuration

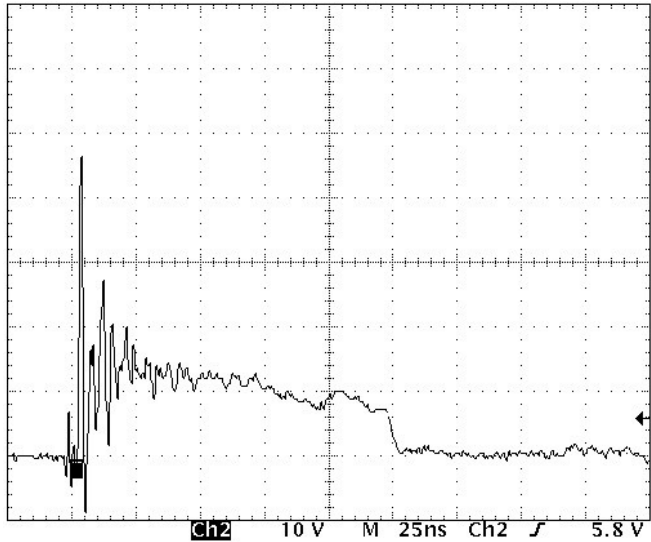


Figure 8. Clamped +8 kV ESD voltage waveform

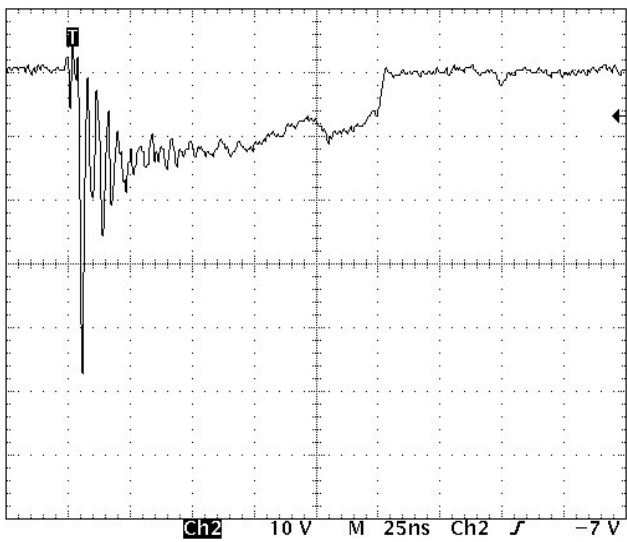
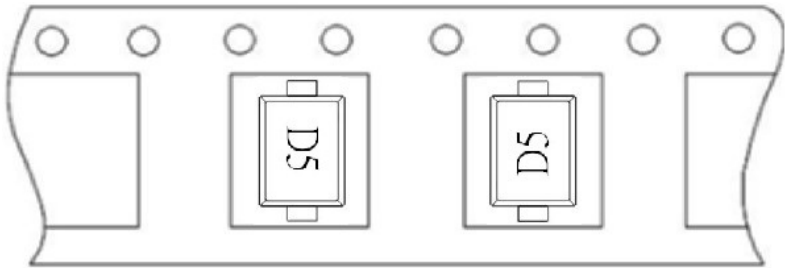


Figure 9. Clamped -8 kV ESD voltage waveform

Marking &Orientation

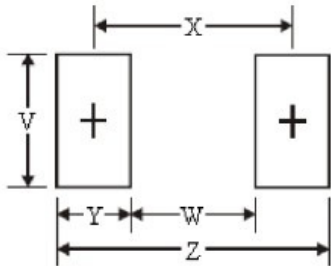


Note: Marking is none direction

Packaging Information

DEVICE	Q'TY/REEL (PCS)	REEL DIA. (INCH)	Q'TY/BOX (PCS)	Q'TY/CARTON (PCS)
L04ESD5V0CP2	8000	7	120K	480K

SOD-923 Soldering Pad Layout



Dim.	Millimeters	Inches
Z	1.20	0.047
X	0.90	0.035
W	0.60	0.023
Y	0.30	0.011
V	0.40	0.015

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