

with AGC, 2.3 - 2.5 GHz

GaAs MMIC LOW NOISE AMPLIFIER

v02.0605

# ROHS V



Gain vs. Temperature, VctI = 0V



Input Return Loss vs. Temperature, VctI = 0V



Gain Over Control Voltage Range



Noise Figure vs. Temperature, Vctl = 0V



Output Return Loss vs. Temperature, Vctl = 0V



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#### Gain vs. Control Voltage @ 2.4 GHz 26 20 14 GAIN (dB) 8 2 -4 -10 0.5 2.0 2.5 3.0 0.0 1.0 1.5 Control Voltage (Vdc)

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### Input Return Loss Over Control Voltage Range



### Noise Figure and Output IP3 vs. Control Voltage

	Frequency = 2.4 GHz	
VCTL	Noise Figure	OIP3 (dBm)*
0V	2.5	7.1
1.7V	4.0	-4.4
3.0V	10.0	-12.9
* Two-tone input power = -30 dBm per tone.		

### Power Compression @ 2.4 GHz, Vctl = 0V

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### Output Return Loss Over Control Voltage Range



### Reverse Isolation vs. Temperature, Vctl = 0V



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# Absolute Maximum Ratings

Drain Bias Voltage (Vdd)	+7 Vdc	
Control Voltage Range (Vctl)	-0.2V to Vdd	
RF Input Power (RFIN)(Vdd = +3 Vdc)	-7 dBm	
Channel Temperature	150 °C	
Continuous Pdiss (T = 85 °C) (derate 5.62 mW/°C above 85 °C)	0.365 W	
Thermal Resistance (channel to lead)	178 °C/W	
Storage Temperature	-65 to +150 °C	
Operating Temperature	-40 to +85 °C	

### Gain Control

Vctl (Vdc)	Gain State	Typical Ictl (uA)
0.0	Maximum	25
1.5	Middle	25
Vdd	Minimum	25



ELECTROSTATIC SENSITIVE DEVICE OBSERVE HANDLING PRECAUTIONS

### **Outline Drawing**



.031

0.80

#### NOTES:

1. LEADFRAME MATERIAL: COPPER ALLOY

.009 .003 0.22 0.08

2. DIMENSIONS ARE IN INCHES [MILLIMETERS]

DIMENSION DOES NOT INCLUDE MOLDFLASH OF 0.15mm PER SIDE.

A DIMENSION DOES NOT INCLUDE MOLDFLASH OF 0.25mm PER SIDE.

5. ALL GROUND LEADS MUST BE SOLDERED TO PCB RF GROUND.

### Package Information

Part Number	Package Body Material	Lead Finish	MSL Rating	Package Marking <sup>[3]</sup>
HMC287MS8	Low Stress Injection Molded Plastic	Sn/Pb Solder	MSL1 <sup>[1]</sup>	H287 XXXX
HMC287MS8E	RoHS-compliant Low Stress Injection Molded Plastic	100% matte Sn	MSL1 <sup>[2]</sup>	<u>H287</u> XXXX

[1] Max peak reflow temperature of 235 °C

[2] Max peak reflow temperature of 260 °C

[3] 4-Digit lot number XXXX

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U1

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J1

#### **Evaluation PCB** J3 J4 0 0 0 Ο 0 0 0 GŇD DD RFIN rfou 000 0 00 0 0 0 J2

0 0 0

### List of Materials for Evaluation PCB 103739<sup>[1]</sup>

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Item	Description
J1, J2	PCB Mount SMA Connector
J3, J4	DC Pin
U1	HMC287MS8 / HMC287MS8E Amplifier
PCB <sup>[2]</sup>	Evaluation Board 1.6" x 1.5"

[1] Reference this number when ordering complete evaluation PCB [2] Circuit Board Material: Rogers 4350

The circuit board used in the final application should use RF circuit design techniques. Signal lines should have 50 ohm impedance while the package ground leads and exposed paddle should be connected directly to the ground plane similar to that shown. A sufficient number of via holes should be used to connect the top and bottom ground planes. The evaluation circuit board shown is available from Hittite upon request.

4 LOW NOISE AMPLIFIERS - SMT

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Notes:

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