

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Drain-Source Voltage		V _{DSS}	60	V
Gate-Source Voltage		V_{GSS}	±20	V
Continuous Drain Current (Note 5)	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	I _D	14.8 11.9	А
Continuous Drain Current (Note 6)	$T_C = +25^{\circ}C$ $T_C = +100^{\circ}C$	I _D	70 50	А
Maximum Continuous Body Diode Forward Current (Note 6)		I _S	60	Α
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)		I _{DM}	130	Α
Avalanche Current, L=0.1mH		I _{AS}	20	Α
Avalanche Energy, L=0.1mH		E _{AS}	20	mJ

Thermal Characteristics ($@T_A = +25^{\circ}C$, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 5)	P _D	3.1	W
Thermal Resistance, Junction to Ambient (Note 5)	$R_{\theta JA}$	47	°C/W
Total Power Dissipation (Note 6)	P _D	60	W
Thermal Resistance, Junction to Case (Note 6)	R _{θJC}	2.5	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +175	°C

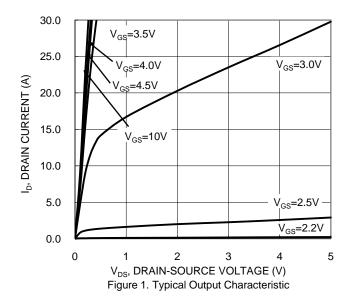
Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

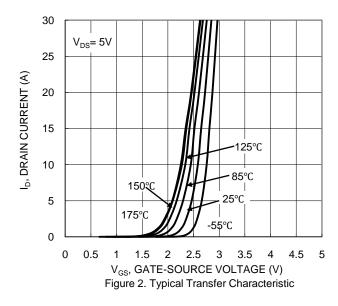
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BV _{DSS}	60	_	_	V	$V_{GS} = 0V$, $I_D = 1mA$	
Zero Gate Voltage Drain Current	I _{DSS}	_	_	1	μA	V _{DS} = 48V, V _{GS} = 0V	
Gate-Source Leakage	I _{GSS}	_	_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)			•	•	•		
Gate Threshold Voltage	V _{GS(TH)}	1	_	3	V	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	
Static Drain-Source On-Resistance		_	5.3	8	mΩ	$V_{GS} = 10V, I_D = 20A$	
Static Dialii-Source Off-Resistance	R _{DS(ON)}	_	8.3	12		V _{GS} = 4.5V, I _D = 20A	
Diode Forward Voltage	V_{SD}	_	0.9	1.2	V	V _{GS} = 0V, I _S = 20A	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	Ciss	_	2090	_		$V_{DS} = 30V, V_{GS} = 0V,$ f = 1MHz	
Output Capacitance	Coss	_	746	_	pF		
Reverse Transfer Capacitance	C _{rss}	_	38.5	_			
Gate Resistance	Rg	_	0.59	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$	
Total Gate Charge (V _{GS} = 4.5V)	Qg	_	19.3	_			
Total Gate Charge (V _{GS} = 10V)	Qg	-	41.3	_	nC	V _{DS} = 30V, I _D = 20A	
Gate-Source Charge	Q _{gs}	_	6	_	nc nc		
Gate-Drain Charge	Q _{gd}	_	8.8	_			
Turn-On Delay Time	t _{D(ON)}	_	5.7	_		$V_{DD} = 30V, V_{GS} = 10V,$ $I_{D} = 20A, R_{G} = 3\Omega$	
Turn-On Rise Time	t _R	_	4.3	_			
Turn-Off Delay Time	t _{D(OFF)}	_	23.4	_	ns		
Turn-Off Fall Time	t _F	_	9.7	_			
Body Diode Reverse Recovery Time	t _{RR}	_	35.4	_	ns	L = 20A di/dt = 100A/ug	
Body Diode Reverse Recovery Charge	Q_{RR}	_	38.2	_	nC	$_{\rm nC}$ $I_{\rm F}$ = 20A, di/dt = 100A/ μ s	

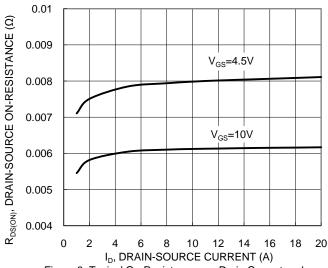
Notes:

- 5. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
- Device mounted on infinite heat sink and measured by thermal couple attached on bottom heat sink of package.
 Short duration pulse test used to minimize self-heating effect.
 Guaranteed by design. Not subject to product testing.









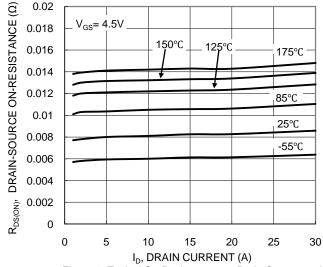
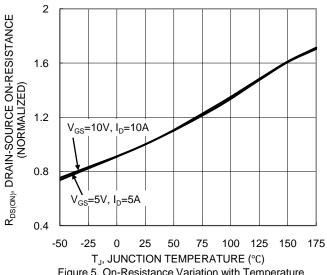
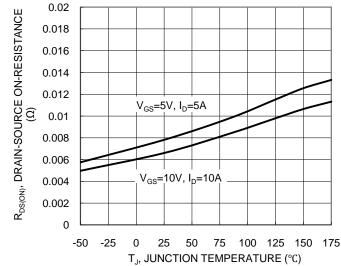


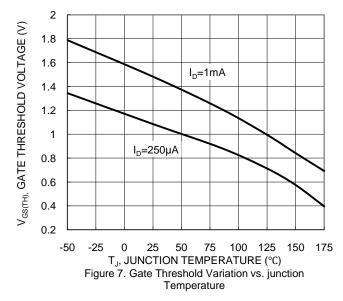
Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

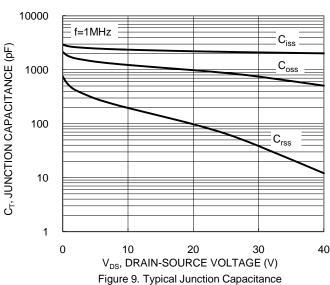
Figure 4. Typical On-Resistance vs. Drain Current and Temperature

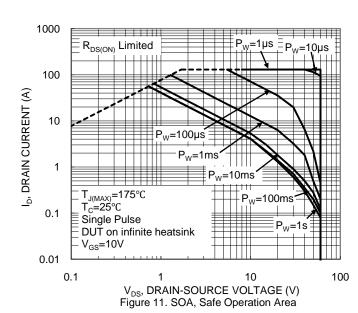


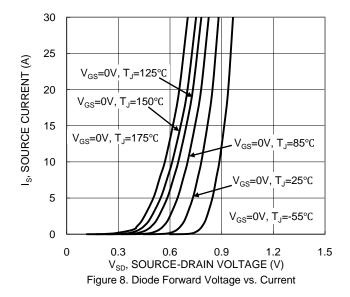


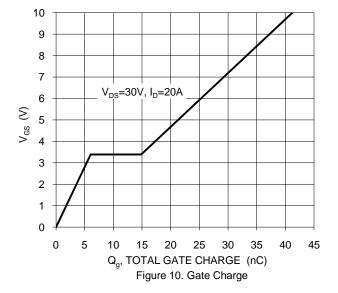




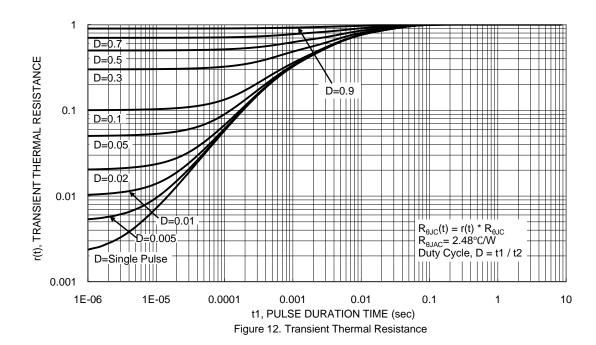






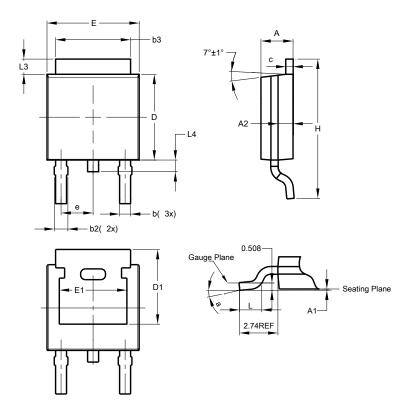






Package Outline Dimensions

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.

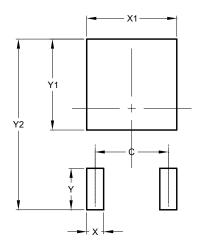


TO252 (DPAK)					
Dim	Min	Max	Тур		
Α	2.19	2.39	2.29		
A 1	0.00	0.13	0.08		
A2	0.97	1.17	1.07		
p	0.64	0.88	0.783		
b2	0.76	1.14	0.95		
b3	5.21	5.46	5.33		
С	0.45	0.58	0.531		
D	6.00	6.20	6.10		
D1	5.21	-	-		
е	-	-	2.286		
Е	6.45	6.70	6.58		
E1	4.32	-	-		
Н	9.40	10.41	9.91		
L	1.40	1.78	1.59		
L3	0.88	1.27	1.08		
L4	0.64	1.02	0.83		
а	0°	10°	-		
All	All Dimensions in mm				



Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)		
С	4.572		
Х	1.060		
X1	5.632		
Y	2.600		
Y1	5.700		
Y2	10.700		

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