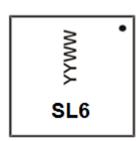


Marking Information

POWERDI®3333-8



SL6 = Product Type Marking Code YYWW = Date Code Marking YY = Last Digit of Year (ex: 14 = 2014) WW = Week Code (01 ~ 53)

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

| Characteristic | | Symbol | Value | Units |
|---|--|------------------|----------|-------|
| Drain-Source Voltage | | V _{DSS} | 60 | V |
| Gate-Source Voltage | | V _{GSS} | ±20 | V |
| Continuous Durin Courset (Note 5) V | $T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$ | I _D | 15 12 | Α |
| Continuous Drain Current (Note 5) V _{GS} = 10V | $T_C = +25$ °C $T_C = +70$ °C | I _D | 80 65 | A |
| Maximum Continuous Body Diode Forward Current (Note | I _S | 80 | Α | |
| Pulsed Drain Current (10µs pulse, duty cycle = 1%) | | I _{DM} | 80 | Α |
| Avalanche Current, L=0.1mH | | I _{AS} | 20 | Α |
| Avalanche Energy, L=0.1mH | | E _{AS} | 20 | mJ |

Thermal Characteristics

| Characteristic | | Symbol | Value | Units |
|--|----------------|-------------------|-------------|-------|
| Total Power Dissipation (Note 5) | $T_A = +25$ °C | P_{D} | 2.2 | W |
| Thermal Resistance, Junction to Ambient (Note 5) | | $R_{\theta JA}$ | 55 | °C/W |
| Total Power Dissipation (Note 6) | $T_C = +25$ °C | P_{D} | 62.5 | W |
| Thermal Resistance, Junction to Case (Note 6) | | R ₀ JC | 2 | °C/W |
| Operating and Storage Temperature Range | | $T_{J_i}T_{STG}$ | -55 to +150 | °C |

Notes:

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^{5.} R_{BJA} is determined with the device mounted on FR-4 substrate PC board, 2oz copper, with 1-inch square copper plate. R_{BJC} is guaranteed by design while $R_{\theta JA}$ is determined by the user's board design.

^{6.} Short duration pulse test used to minimize self-heating effect.

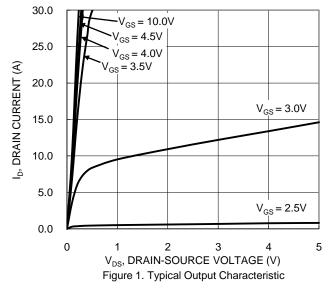


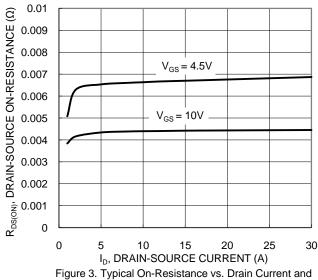
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

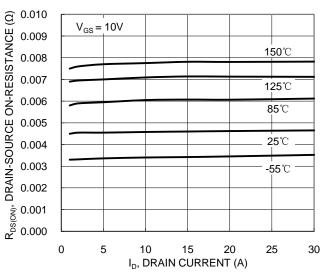
| Characteristic | Symbol | Min | Тур | Max | Unit | Test Condition | |
|--|---------------------|-----|------|------|------|--|--|
| OFF CHARACTERISTICS (Note 6) | | | | • | • | | |
| Drain-Source Breakdown Voltage | BV _{DSS} | 60 | _ | _ | V | $V_{GS} = 0V, I_D = 250\mu A$ | |
| Zero Gate Voltage Drain Current | I _{DSS} | _ | _ | 1 | μΑ | $V_{DS} = 48V, V_{GS} = 0V$ | |
| Gate-Source Leakage | I _{GSS} | _ | _ | ±100 | nA | $V_{GS} = \pm 20V, V_{DS} = 0V$ | |
| ON CHARACTERISTICS (Note 6) | | | | • | • | | |
| Gate Threshold Voltage | V _{GS(TH)} | 0.8 | _ | 2 | V | $V_{DS} = V_{GS}, I_{D} = 250 \mu A$ | |
| Static Drain-Source On-Resistance | | _ | 4.5 | 6 | mΩ | $V_{GS} = 10V, I_D = 20A$ | |
| Static Drain-Source On-Resistance | R _{DS(ON)} | _ | 6.5 | 8.5 | | $V_{GS} = 4.5V, I_D = 15A$ | |
| Forward Transconductance | G _{FS} | _ | 100 | _ | S | $V_{DS} = 5V, I_{D} = 20A$ | |
| Diode Forward Voltage | V _{SD} | _ | 0.9 | 1.2 | V | V _{GS} = 0V, I _S = 20A | |
| DYNAMIC CHARACTERISTICS (Note 7) | | | | | | | |
| Input Capacitance | C _{iss} | _ | 2090 | _ | | V _{DS} = 30V, V _{GS} = 0V, f = 1MHz | |
| Output Capacitance | Coss | | 746 | _ | pF | | |
| Reverse Transfer Capacitance | C _{rss} | _ | 38.5 | _ | | | |
| Gate Resistance | Rg | _ | 0.59 | _ | Ω | $V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$ | |
| Total Gate Charge (V _{GS} = 4.5V) | Qg | _ | 19.3 | _ | | | |
| Total Gate Charge (V _{GS} = 10V) | Qg | _ | 41.3 | _ | nC | V _{DS} = 30V, I _D = 20A | |
| Gate-Source Charge | Q _{gs} | _ | 6.0 | _ | IIC | | |
| Gate-Drain Charge | Q _{gd} | _ | 8.8 | _ | | | |
| Turn-On Delay Time | t _{D(ON)} | _ | 5.7 | _ | | $V_{DD} = 30V, V_{GS} = 10V,$ $I_{D} = 20A, R_{G} = 3\Omega$ | |
| Turn-On Rise Time | t _R | _ | 4.3 | _ |] | | |
| Turn-Off Delay Time | t _{D(OFF)} | _ | 23.4 | _ | ns | | |
| Turn-Off Fall Time | t _F | _ | 9.7 | _ | | | |
| Body Diode Reverse Recovery Time | t _{RR} | _ | 35.4 | _ | ns | 1 200 1:/14 1000/ | |
| Body Diode Reverse Recovery Charge | Q_{RR} | | 38.2 | _ | nC | $I_F = 20A$, di/dt = 100A/ μ s | |

Note: 7. Guaranteed by design. Not subject to product testing.



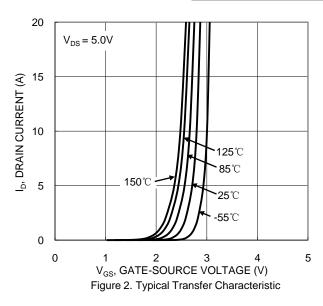


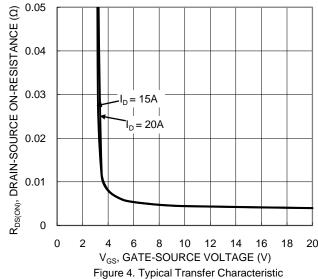




Gate Voltage

Figure 5. Typical On-Resistance vs. Drain Current and Temperature





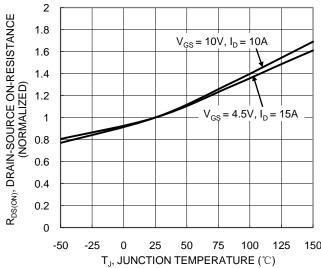


Figure 6. On-Resistance Variation with Temperature



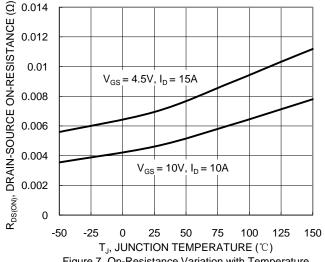


Figure 7. On-Resistance Variation with Temperature

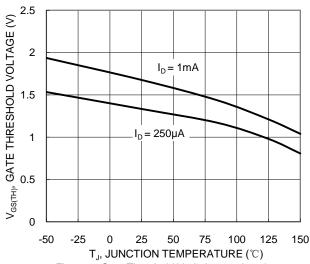
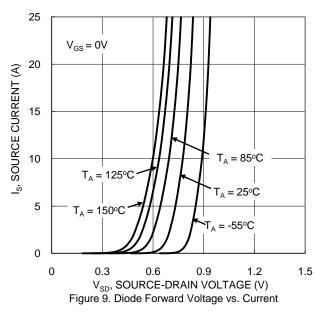
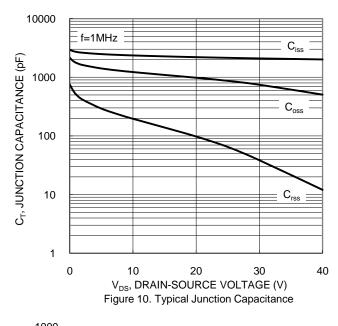
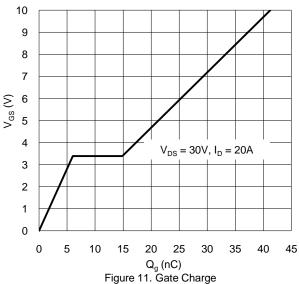


Figure 8. Gate Threshold Variation vs. Junction Temperature







1000 ____ R_{DS(ON)} Limited =100µs 100 DRAIN CURRENT (A) 10 1 =100ms <u>ث</u> P_W =1s $T_{J(Max)} = 150^{\circ}C$ $T_A = 25^{\circ}C$ 0.1 Single Pulse DUT on 1*MRP Board V_{GS}= 10V 0.01 0.1 10 100 V_{DS}, DRAIN-SOURCE VOLTAGE (V) Figure 12. SOA, Safe Operation Area



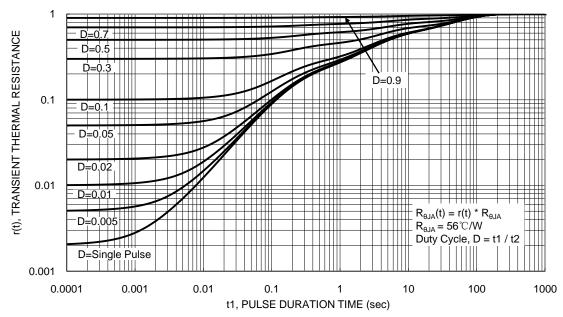


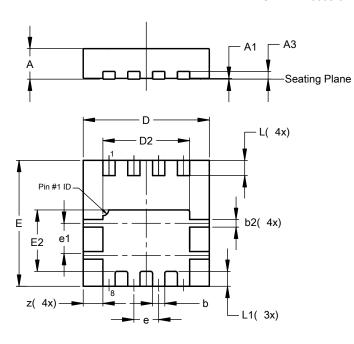
Figure 13. Transient Thermal Resistance



Package Outline Dimensions

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.

POWERDI®3333-8



| POWERDI®3333-8 | | | | | |
|----------------------|------|------|-------|--|--|
| Dim | Min | Max | Тур | | |
| Α | 0.75 | 0.85 | 0.80 | | |
| A1 | 0.00 | 0.05 | 0.02 | | |
| A3 | | _ | 0.203 | | |
| b | 0.27 | 0.37 | 0.32 | | |
| b2 | _ | _ | 0.20 | | |
| D | 3.25 | 3.35 | 3.30 | | |
| D2 | 2.22 | 2.32 | 2.27 | | |
| Е | 3.25 | 3.35 | 3.30 | | |
| E2 | 1.56 | 1.66 | 1.61 | | |
| е | - | - | 0.65 | | |
| e1 | 0.79 | 0.89 | 0.84 | | |
| L | 0.35 | 0.45 | 0.40 | | |
| L1 | _ | - | 0.39 | | |
| Z | _ | _ | 0.515 | | |
| All Dimensions in mm | | | | | |

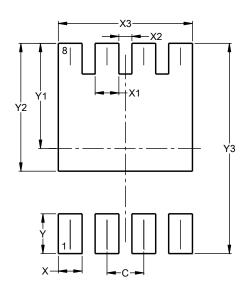
Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.

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| Dimensions | Value (in mm) |
|------------|---------------|
| C | 0.650 |
| X | 0.420 |
| X1 | 0.420 |
| X2 | 0.230 |
| Х3 | 2.370 |
| Y | 0.700 |
| Y1 | 1.850 |
| Y2 | 2.250 |
| Y3 | 3 700 |



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