

# **Marking Information**

Site 1



PC = Product Type Marking Code YM = Date Code Marking Y = Year (ex: H = 2020) M = Month (ex: 9 = September) Dot Denotes Pin 1

Date Code Key												
Year	201	9	2020		2021	20	22	2023		2024	2	2025
Code	G		Н				J	K		L		М
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D

Site 2



PC = Product Type Marking Code YWX = Date Code Marking Y = Year (ex: 0 = 2020) W = Week (ex: a = Week 27; z Represents Week 52 and 53) X = Internal Code (ex: U = Monday)

Date Code Key									
Year	2019	2020	2021	2022	2023	202	4 2025	2026	
Code	9	0	1	2	3	4	5	6	
Week	1-26			27-52			53		
Code	A-Z			a-z			Z		
Internal Code	Sun	Mon		Tue	Wed	Thu	Fri	Sat	
Code	Т	U		V	W	Х	Y	Z	



# Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units		
Drain-Source Voltage		V <sub>DSS</sub>	-20	V	
Gate-Source Voltage			V <sub>GSS</sub>	±12	V
Continuous Drain Current (Note 5) // - 45/	Steady State	T <sub>A</sub> = +25°C T <sub>A</sub> = +70°C	ID	-6.2 -4.9	А
Continuous Drain Current (Note 5) $V_{GS}$ = -4.5V	t<5s	T <sub>A</sub> = +25°C T <sub>A</sub> = +70°C	ID	-7.5 -5.9	А
Continuous Drain Current (Note 5) // - 1 9)/	Steady State	T <sub>A</sub> = +25°C T <sub>A</sub> = +70°C	ID	-4.2 -3.4	А
Continuous Drain Current (Note 5) $V_{GS}$ = -1.8V	t<5s	T <sub>A</sub> = +25°C T <sub>A</sub> = +70°C	ID	-5.2 -4.1	А
Pulsed Drain Current (10µs pulse, duty cycle = 1%)	•	I <sub>DM</sub>	-25	А	
Maximum Continuous Body Diode Forward Current	ls	2.5	А		

# **Thermal Characteristics**

Characteristic		Symbol	Value	Units
Total Power Dissipation (Note 6)		PD	0.66	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady state	P	189	°C/W
Thermal Resistance, Junction to Ambient (Note 6)	t<5s	$R_{ ext{ heta}}JA$	123	°C/W
Total Power Dissipation (Note 5)		PD	2.03	W
Thermal Desistance Junction to Ambient (Note 5)	Steady state	D	61	°C/W
Thermal Resistance, Junction to Ambient (Note 5)	t<5s	$R_{ ext{ heta}JA}$	40	°C/W
Thermal Resistance, Junction to Case (Note 5)		$R_{ extsf{ heta}Jc}$	9.3	°C/W
Operating and Storage Temperature Range		TJ. TSTG	-55 to +150	°C

# Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	-20		—	V	$V_{GS} = 0V, I_D = -250\mu A$
Zero Gate Voltage Drain Current	IDSS	_		-1	μA	$V_{DS} = -20V, V_{GS} = 0V$
Gate-Source Leakage	I <sub>GSS</sub>	_	_	±100	nA	$V_{GS}$ = ±12.0V, $V_{DS}$ = 0V
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V <sub>GS(th)</sub>	-0.4	_	-1.1	V	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$
			25	36		V <sub>GS</sub> = -4.5V, I <sub>D</sub> = -4.6A
Static Drain-Source On-Resistance	R <sub>DS (ON)</sub>		33	56	mΩ	V <sub>GS</sub> = -2.5V, I <sub>D</sub> = -3.8A
		_	50	75		V <sub>GS</sub> = -1.8V, I <sub>D</sub> = -2.0A
Forward Transfer Admittance	Y <sub>fs</sub>	_	9	_	S	V <sub>DS</sub> = -10V, I <sub>D</sub> = -4.5A
Diode Forward Voltage	V <sub>SD</sub>	_	-0.7	-1.2	V	$V_{GS} = 0V, I_{S} = -2.1A$
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	Ciss	_	1537	—	pF	
Output Capacitance	Coss	_	146	_	pF	V <sub>DS</sub> = -10V, V <sub>GS</sub> = 0V f = 1.0MHz
Reverse Transfer Capacitance	Crss	_	127	—	pF	
Gate Resistance	Rg	_	10.4	—	Ω	V <sub>DS</sub> = 0V, V <sub>GS</sub> = 0V, f = 1.0MHz
Total Gate Charge	Qg	_	14.4	—		
Gate-Source Charge	Q <sub>gs</sub>	_	2.6	_	nC	V <sub>DS</sub> = -10V, V <sub>GS</sub> = -4.5V I <sub>D</sub> = -4.5A
Gate-Drain Charge	Q <sub>gd</sub>	_	2.7	—		ID4.5A
Turn-On Delay Time	t <sub>D(on)</sub>	_	13.7	_		
Turn-On Rise Time	tr	_	14.0			$V_{DD}$ = -10V, $V_{GS}$ = -4.5V, $R_{G}$ = 6 $\Omega$ ,
Turn-Off Delay Time	t <sub>D(off)</sub>	_	79.1	_	ns	R <sub>L</sub> = 10Ω, I <sub>D</sub> = -1A
Turn-Off Fall Time	t <sub>f</sub>	_	35.5	_		

 Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper pad layout.
Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided. Notes:

7. Short duration pulse test used to minimize self-heating effect.
8. Guaranteed by design. Not subject to production testing.



### DMP2066UFDE

2

2.5

T<sub>A</sub>=150°C

T<sub>A</sub>=−55°C

20

15

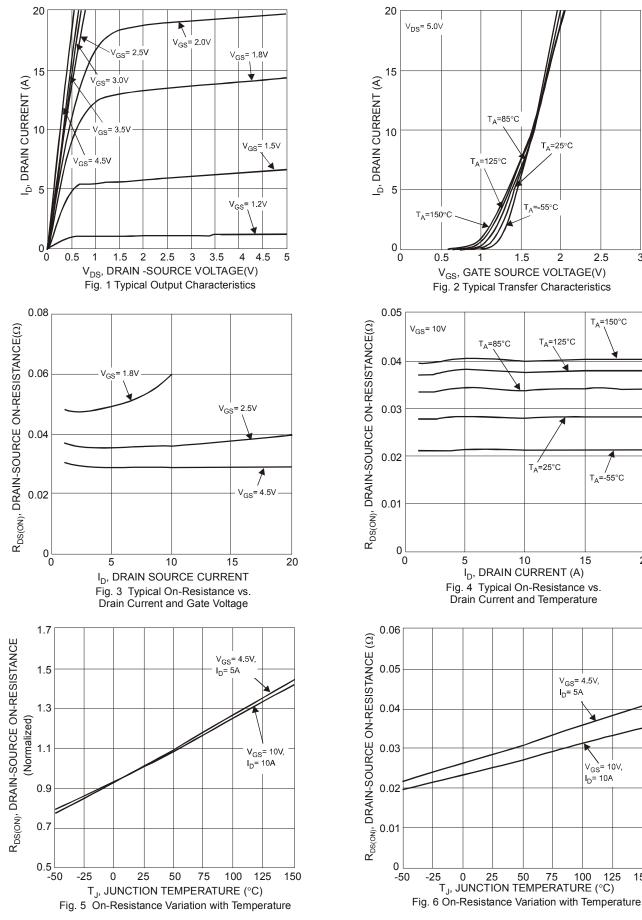
V<sub>GS</sub>= 4.5V, I<sub>D</sub>= 5A

V<sub>GS</sub>= 10V, I<sub>D</sub>= 10A

100

125

3

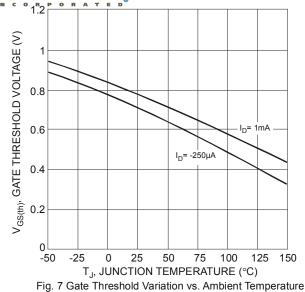


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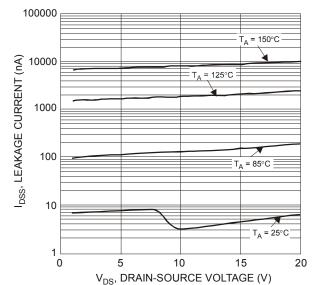
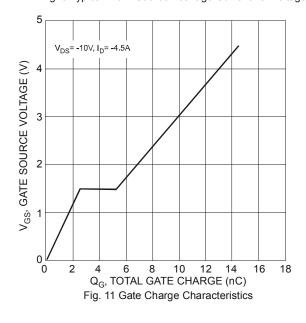
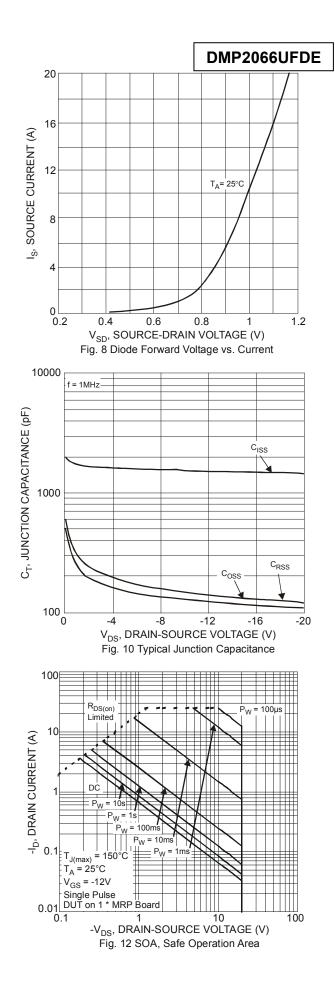


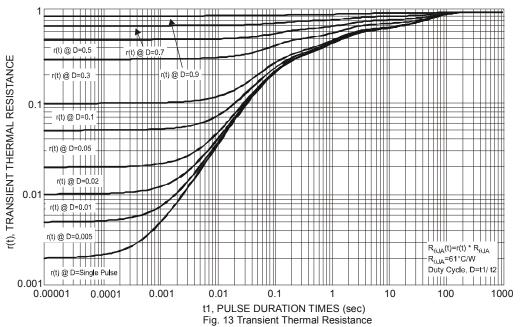
Fig. 9 Typical Drain-Source Leakage Current vs. Voltage





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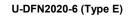


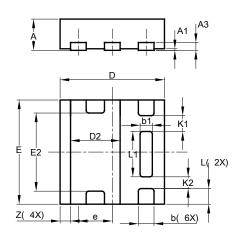




### **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

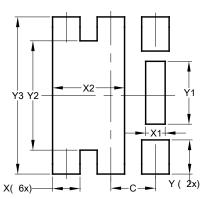




U-DFN2020-6						
Туре Е						
Dim	Min	Max	Тур			
Α	0.57	0.63	0.60			
A1	0	0.05	0.03			
A3	-	-	0.15			
b	0.25	0.35	0.30			
b1	0.185	0.285	0.235			
D	1.95	2.05	2.00			
D2	0.85	1.05	0.95			
E	1.95	2.05	2.00			
E2	1.40	1.60	1.50			
е	-	-	0.65			
L	0.25	0.35	0.30			
L1	0.82	0.92	0.87			
K1	-	_	0.305			
K2	-	_	0.225			
Z	_	_	0.20			
All	Dimen	isions i	in mm			

### Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



#### U-DFN2020-6 (Type E)

Dimensions	Value (in mm)
С	0.650
X	0.400
X1	0.285
X2	1.050
Y	0.500
Y1	0.920
Y2	1.600
Y3	2.300



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