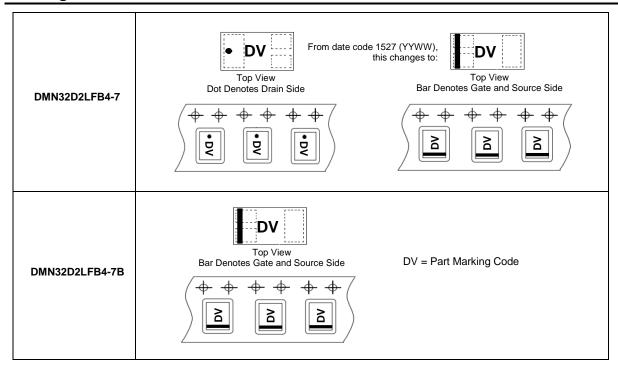


Marking Information



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Drain Source Voltage	V_{DSS}	30	V
Gate-Source Voltage	V_{GSS}	±10	V
Drain Current (Note 5)	I_{D}	300	mA

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Total Power Dissipation (Note 5) @T _A = +25°C	P _D	350	mW
Thermal Resistance, Junction to Ambient (Note 5)	R _{0JA}	357	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

Note:

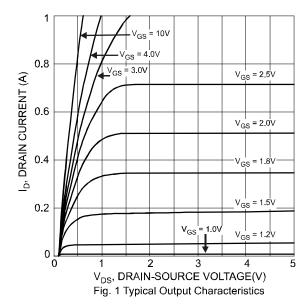
^{5.} Device mounted on FR-4 PCB, pad layout as shown on Diodes Inc. suggested pad layout document AP02001, which can be found on our website at http://www.diodes.com.

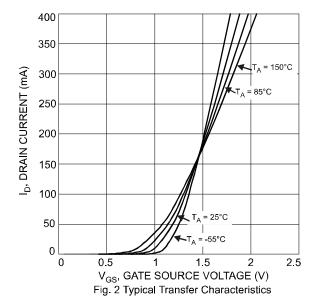


Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 6)							
Drain-Source Breakdown Voltage		BV _{DSS}	30			V	$V_{GS} = 0V, I_D = 10\mu A$
Zero Gate Voltage Drain Current	@ T _C = +25°C	I _{DSS}	_	_	1	μA	$V_{DS} = 30V, V_{GS} = 0V$
Gate-Body Leakage		I _{GSS}	_	_	±10 ±500	μA nA	$V_{GS} = \pm 10V, V_{DS} = 0V$ $V_{GS} = \pm 5V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 6)							
Gate Threshold Voltage		V _{GS(th)}	0.6		1.2	V	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$
			_	_	2.2		$V_{GS} = 1.8V, I_D = 20mA$
Static Drain-Source On-Resistance		R _{DS (ON)}	_	_	1.5	Ω	$V_{GS} = 2.5V, I_D = 20mA$
				_	1.2		$V_{GS} = 4.0V, I_D = 100mA$
Forward Transconductance		Y _{fs}	100	_	_	mS	$V_{DS} = 10V, I_{D} = 0.1A$
Source-Drain Diode Forward Voltage		V_{SD}	0.5	_	1.4	V	$V_{GS} = 0V, I_{S} = 115mA$
DYNAMIC CHARACTERISTICS							
Input Capacitance		C _{iss}	_	39	78	pF	., ., ., .,
Output Capacitance		Coss	_	10	20	pF	$V_{DS} = 3V, V_{GS} = 0V$ -f = 1.0MHz
Reverse Transfer Capacitance		Crss	_	3.6	7.2	pF	1 = 1.0IVII IZ
Switching Time	Turn-on Time	t _{on}	_	11	22	nS	$V_{DD} = 5V, I_D = 10mA,$
	Turn-off Time	t _{off}	_	51	102	nS	$V_{GS} = 0.5V$

Note: 6. Short duration pulse test used to minimize self-heating effect.





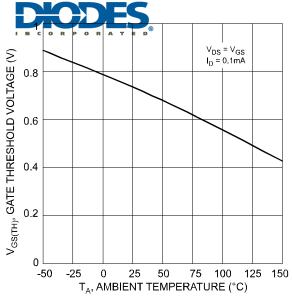
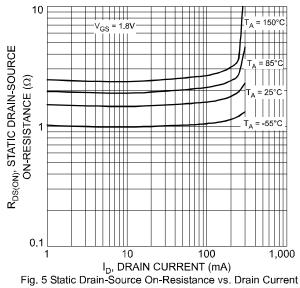


Fig. 3 Gate Threshold Voltage vs. Ambient Temperature



1.6 R_{DS(ON)}, STATIC DRAIN-SOURCE ON-RESISTANCE (NORMALIZED) V_{GS} = 4V = 100 mA1.4 $V_{GS} = 2.5V$ $I_D = 20 \text{mA}$. = 1 8V √_{GS} 1.2 $I_D = 20mA$ 0.6 0 25 50 75 100 125 150 T_A, AMBIENT TEMPERATURE (C°)

Fig. 7 Normalized Static Drain-Source On-Resistance vs. Ambient Temperature

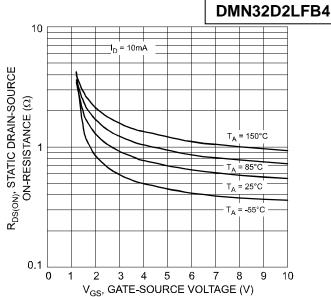


Fig. 4 Static Drain-Source On-Resistance vs. Gate-Source Voltage

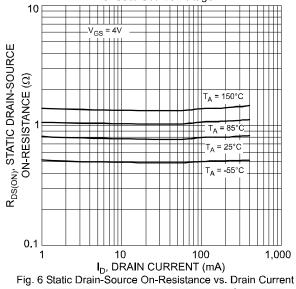
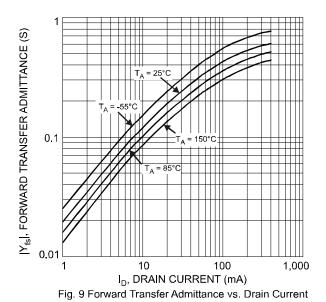
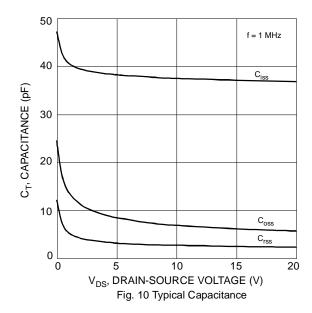


Fig. 8 Reverse Drain Current vs. Source-Drain Voltage

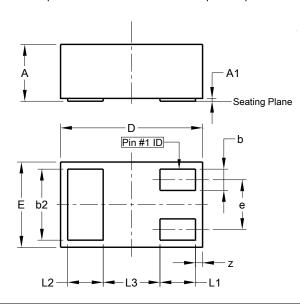






Package Outline Dimensions

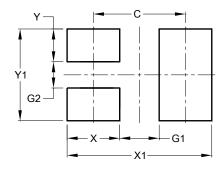
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.



X2-DFN1006-3				
Dim	Min	Max	Тур	
Α	ı	0.40	ı	
A1	0.00	0.05	0.03	
b	0.10	0.20	0.15	
b2	0.45	0.55	0.50	
D	0.95	1.05	1.00	
Е	0.55	0.65	0.60	
е	-	-	0.35	
L1	0.20	0.30	0.25	
L2	0.20	0.30	0.25	
L3	ı	1	0.40	
Z	0.02	0.08	0.05	
All Dimensions in mm				

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
С	0.70
G1	0.30
G2	0.20
X	0.40
X1	1.10
Y	0.25
Y1	0.70



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