

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

| Characteristic | Symbol | Value | Unit | Conditions |
|------------------------------------|--------------------------|-------|------|------------------------|
| Peak Pulse Power Dissipation | P _{PP} | 90 | W | 8/20µs, Per Figure 2 |
| Peak Pulse Current | IPP | 3 | А | 8/20µs, Per Figure 2 |
| ESD Protection – Contact Discharge | V _{ESD_Contact} | ±30 | kV | Standard IEC 61000-4-2 |
| ESD Protection – Air Discharge | V _{ESD_Air} | ±30 | kV | Standard IEC 61000-4-2 |

Thermal Characteristics

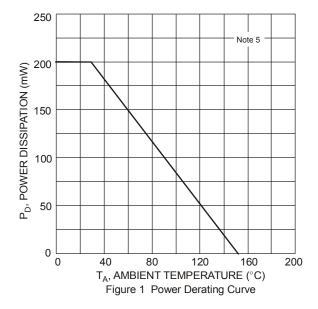
| Characteristic | Symbol | Value | Unit |
|--|----------------------|-------------|------|
| Package Power Dissipation (Note 5) | PD | 200 | mW |
| Thermal Resistance, Junction to Ambient (Note 5) | R _{0JA} | 625 | °C/W |
| Operating and Storage Temperature Range | TJ, T _{STG} | -65 to +150 | °C |

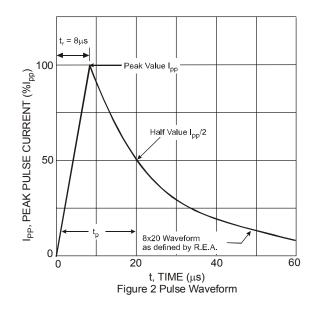
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

| Characteristic | Symbol | Min | Тур | Max | Unit | Test Conditions |
|---------------------------------------|------------------|-----|-----|-----|------|---|
| Reverse Standoff Voltage | V _{RWM} | — | — | 20 | V | — |
| Channel Leakage Current (Note 6) | I _{RM} | _ | | 100 | nA | V _{RWM} = 20V |
| Clamping Voltage Desitive Transients | N/ | — | — | 27 | V | I _{PP} = 1A, t _p = 8/20µS |
| Clamping Voltage, Positive Transients | V _{CL} | — | — | 30 | V | $I_{PP} = 3A, t_p = 8/20\mu S$ |
| Breakdown Voltage | V _{BR} | 21 | — | 25 | V | I _R = 1mA |
| Differential Resistance | R _{DIF} | — | 1.8 | _ | Ω | I _R = 1A, t _p = 8/20μS |
| Channel Input Capacitance | CT | _ | 10 | 15 | pF | V _R = 0V, f = 1MHz |

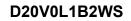
Notes: 5. Device mounted on FR-4 PCB pad layout (2oz copper) as shown on Diodes, Inc. suggested pad layout AP02001, which can be found on our website at http://www.diodes.com.

Short duration pulse test used to minimize self-heating effect.









11 10.5

10

9.5

7.5 7 6.5 6

5.5 5 4.5

4

0 2

6 8 10 12 14 16 18 20

 V_{R} , REVERSE VOLTAGE (V)

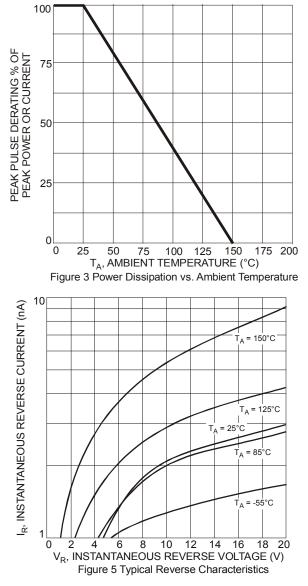
Figure 4 Typical Total Capacitance vs. Reverse Voltage

4

9 8.5 8

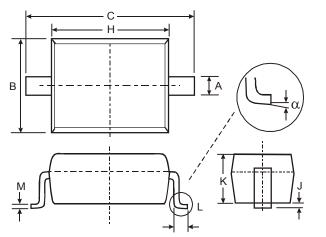
C_T, TOTAL CAPACITANCE (pF)

1MHz



Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.

Package Outline Dimensions

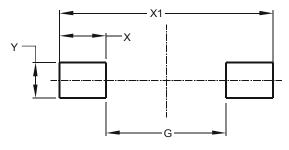


| SOD323 | | |
|----------------------|------|------|
| Dim | Min | Max |
| Α | 0.25 | 0.35 |
| В | 1.20 | 1.40 |
| С | 2.30 | 2.70 |
| Н | 1.60 | 1.80 |
| J | 0.00 | 0.10 |
| κ | 1.0 | 1.1 |
| L | 0.20 | 0.40 |
| М | 0.10 | 0.15 |
| α | 0° | 8° |
| All Dimensions in mm | | |



Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



| Dimensions | Value (in mm) |
|------------|---------------|
| G | 1.520 |
| Х | 0.590 |
| X1 | 2.700 |
| Y | 0.450 |

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