BS107, BS107A

ELECTRICAL CHARACTERISTICS ($T_A = 25^{\circ}C$ unless otherwise noted)

Characteristic	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS			-		•
Zero-Gate-Voltage Drain Current (V _{DS} = 130 Vdc, V _{GS} = 0)	I _{DSS}	-	-	30	nAdc
Drain-Source Breakdown Voltage (V _{GS} = 0, I _D = 100 μAdc)	V _{(BR)DSX}	200	-	-	Vdc
Gate Reverse Current (V _{GS} = 15 Vdc, V _{DS} = 0)	I _{GSS}	-	0.01	10	nAdc
ON CHARACTERISTICS (Note 3)					
Gate Threshold Voltage (I _D = 1.0 mAdc, V _{DS} = V _{GS})	V _{GS(Th)}	1.0	-	3.0	Vdc
Static Drain-Source On Resistance BS107 ($V_{GS} = 2.6$ Vdc, $I_D = 20$ mAdc) ($V_{GS} = 10$ Vdc, $I_D = 200$ mAdc) BS107A ($V_{GS} = 10$ Vdc) ($I_D = 100$ mAdc) ($I_D = 250$ mAdc)	r _{DS(on)}	- - -	- - 4.5 4.8	28 14 6.0 6.4	Ω
SMALL-SIGNAL CHARACTERISTICS	,		ı		1
Input Capacitance (V _{DS} = 25 Vdc, V _{GS} = 0, f = 1.0 MHz)	C _{iss}	-	60	-	pF
Reverse Transfer Capacitance (V _{DS} = 25 Vdc, V _{GS} = 0, f = 1.0 MHz)	C _{rss}	-	6.0	-	pF
Output Capacitance (V _{DS} = 25 Vdc, V _{GS} = 0, f = 1.0 MHz)	C _{oss}	-	30	-	pF
Forward Transconductance (V _{DS} = 25 Vdc, I _D = 250 mAdc)	9 _{fs}	200	400	-	mmhos
SWITCHING CHARACTERISTICS					
Turn-On Time	t _{on}	-	6.0	15	ns
Turn-Off Time	t _{off}	-	12	15	ns

^{3.} Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2.0%.

RESISTIVE SWITCHING

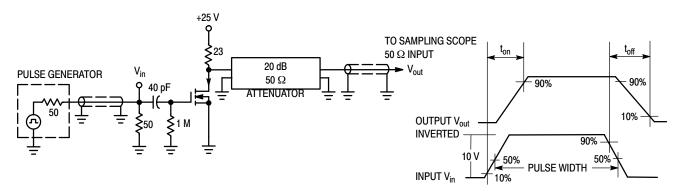


Figure 1. Switching Test Circuit

Figure 2. Switching Waveforms

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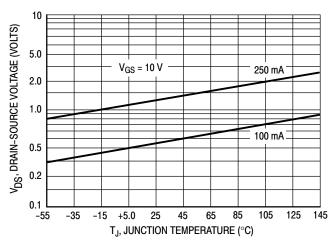


Figure 3. On Voltage versus Temperature

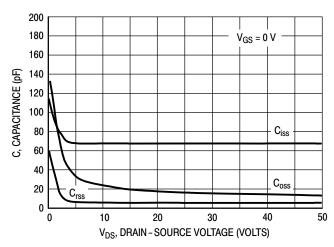


Figure 4. Capacitance Variation

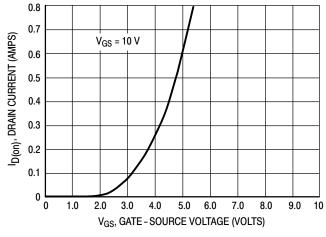


Figure 5. Transfer Characteristic

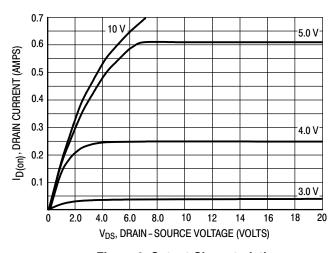


Figure 6. Output Characteristic

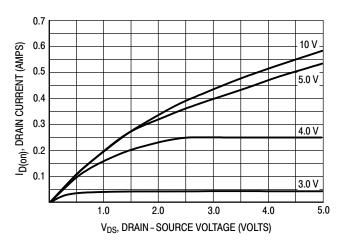
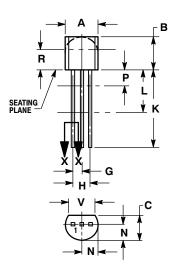


Figure 7. Saturation Characteristic

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PACKAGE DIMENSIONS

TO-92 (TO-226) CASE 29-11 ISSUE AM



STRAIGHT LEAD **BULK PACK**



NOTES

- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982. CONTROLLING DIMENSION: INCH.
- CONTOUR OF PACKAGE BEYOND DIMENSION R IS UNCONTROLLED.
- LEAD DIMENSION IS UNCONTROLLED IN P AND BEYOND DIMENSION K MINIMUM.

	INC	HES	MILLIMETERS		
DIM	MIN	MAX	MIN	MAX	
Α	0.175	0.205	4.45	5.20	
В	0.170	0.210	4.32	5.33	
С	0.125	0.165	3.18	4.19	
D	0.016	0.021	0.407	0.533	
G	0.045	0.055	1.15	1.39	
Н	0.095	0.105	2.42	2.66	
۲	0.015	0.020	0.39	0.50	
K	0.500		12.70		
L	0.250		6.35		
N	0.080	0.105	2.04	2.66	
Р		0.100		2.54	
R	0.115		2.93	-	
٧	0.135		3.43		

STYLE 30:

PIN 1. DRAIN

- GATE
- 3. SOURCE

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