

DYNAMIC CHARACTERISTICS

APT60GT60BR_SRQ

Symbol	Characteristic	Test Conditions	MIN	TYP	MAX	UNIT
C_{ies}	Input Capacitance	Capacitance $V_{GE} = 0V$ $V_{CE} = 25V$ $f = 1\text{ MHz}$		3125	3590	pF
C_{oes}	Output Capacitance			310	450	
C_{res}	Reverse Transfer Capacitance			180	310	
Q_g	Total Gate Charge ³	Gate Charge $V_{GE} = 15V$ $V_{CC} = 0.5V_{CES}$ $I_C = I_{C2}$		275	410	nC
Q_{ge}	Gate-Emitter Charge			19	30	
Q_{gc}	Gate-Collector ("Miller") Charge			120	180	
$t_{d(on)}$	Turn-on Delay Time	Resistive Switching (25°C) $V_{GE} = 15V$ $V_{CC} = 0.5V_{CES}$ $I_C = I_{C2}$ $R_G = 10\Omega$		20	40	ns
t_r	Rise Time			95	190	
$t_{d(off)}$	Turn-off Delay Time			315	470	
t_f	Fall Time			245	490	
$t_{d(on)}$	Turn-on Delay Time	Inductive Switching (150°C) $V_{CLAMP(Peak)} = 0.66V_{CES}$ $V_{GE} = 15V$ $I_C = I_{C2}$ $R_G = 10\Omega$ $T_J = +150^\circ C$		25	50	ns
t_r	Rise Time			59	120	
$t_{d(off)}$	Turn-off Delay Time			430	650	
t_f	Fall Time			65	130	
E_{on}	Turn-on Switching Energy			1.6	3.2	mJ
E_{off}	Turn-off Switching Energy			2.4	4.8	
E_{ts}	Total Switching Losses			4.0	8.0	
$t_{d(on)}$	Turn-on Delay Time	Inductive Switching (25°C) $V_{CLAMP(Peak)} = 0.66V_{CES}$ $V_{GE} = 15V$ $I_C = I_{C2}$ $R_G = 5\Omega$ $T_J = +25^\circ C$		26	50	ns
t_r	Rise Time			63	125	
$t_{d(off)}$	Turn-off Delay Time			395	590	
t_f	Fall Time			68	140	
E_{ts}	Total Switching Losses			3.4	7.0	mJ
gfe	Forward Transconductance	$V_{CE} = 20V, I_C = I_{C2}$	4			S

THERMAL AND MECHANICAL CHARACTERISTICS

Symbol	Characteristic	MIN	TYP	MAX	UNIT
$R_{\theta JC}$	Junction to Case			0.25	°C/W
$R_{\theta JA}$	Junction to Ambient			40	
W_T	Package Weight		0.22		oz
			6.1		gm
Torque	Mounting Torque (using a 6-32 or 3mm Binding Head Machine Screw)			10	lb•in
				1.1	N•m

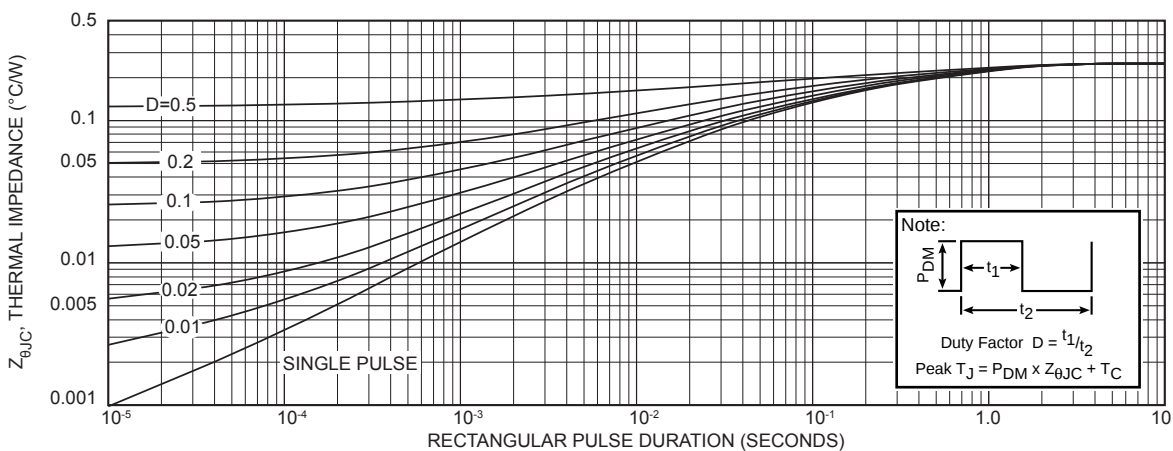
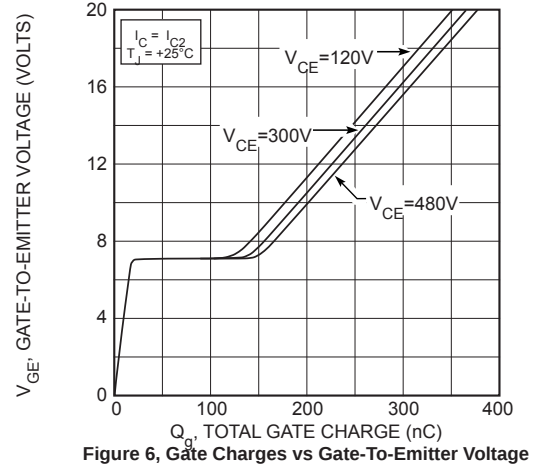
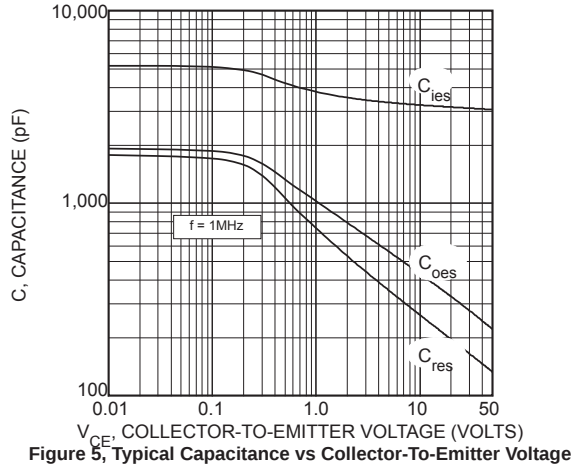
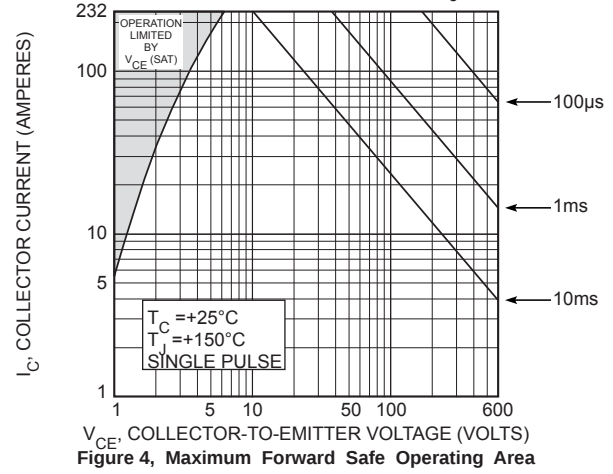
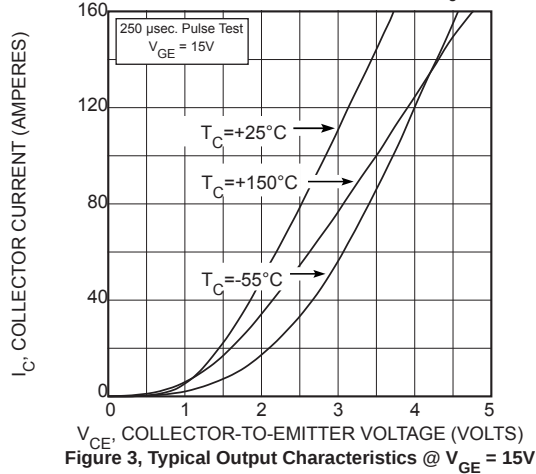
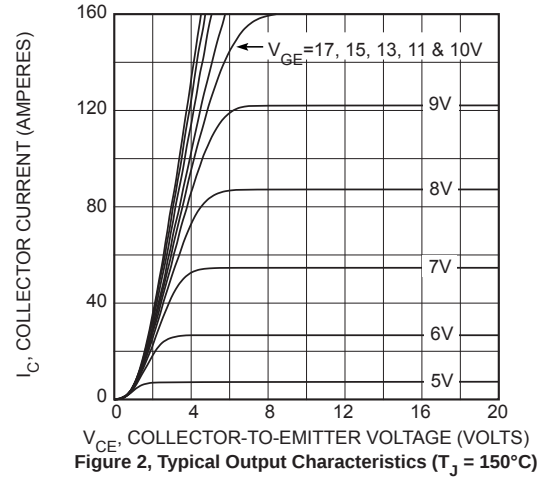
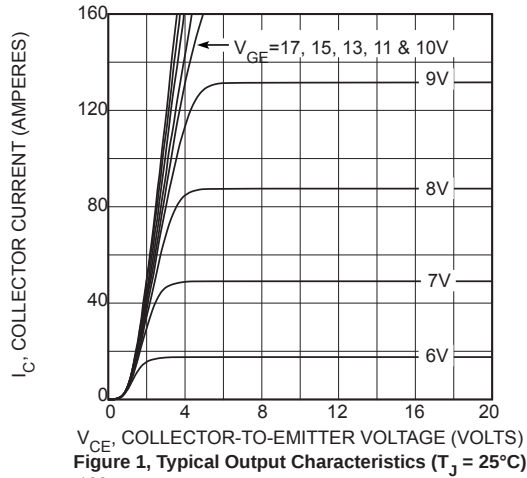
① Repetitive Rating: Pulse width limited by maximum junction temperature.

② $I_C = I_{C2}$, $R_{GE} = 25\Omega$, $L = 36\mu H$, $T_J = 25^\circ C$

③ See MIL-STD-750 Method 3471

④ The maximum current is limited by lead temperature.

Microsemi Reserves the right to change, without notice, the specifications and information contained herein.



Note:

P_{DM}

t_1

t_2

Duty Factor $D = t_1/t_2$

Peak $T_J = P_{DM} \times Z_{\theta JC} + T_C$

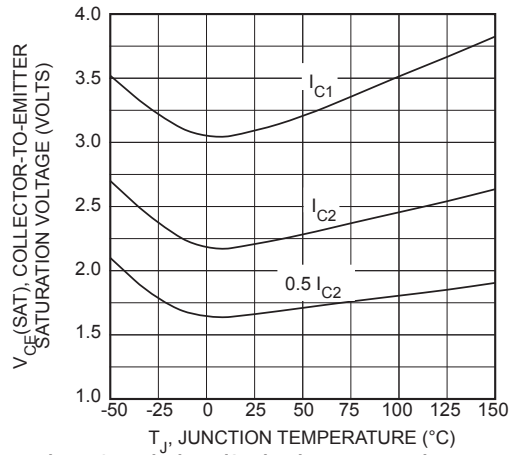
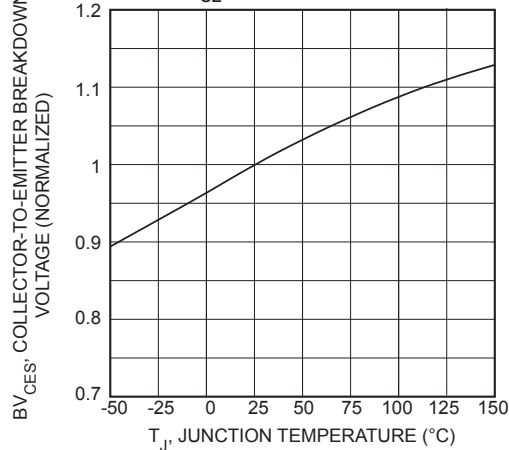
Figure 8, Typical $V_{CE(SAT)}$ Voltage vs Junction Temperature

Figure 10, Breakdown Voltage vs Junction Temperature

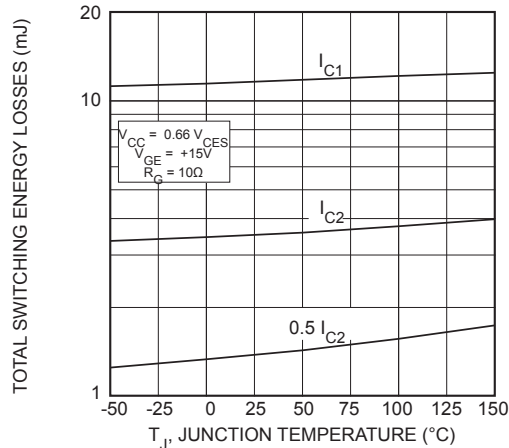


Figure 12, Typical Switching Energy Losses vs. Junction Temperature

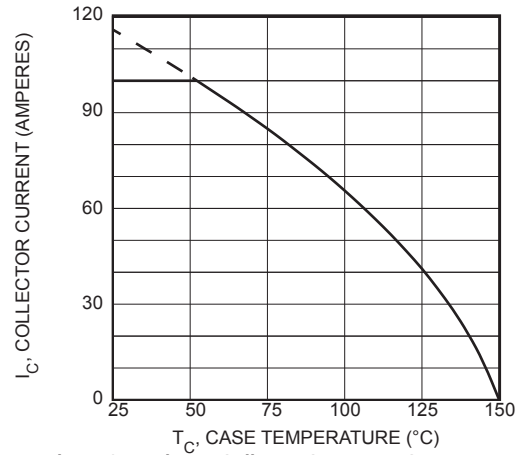


Figure 9, Maximum Collector Current vs Case Temperature

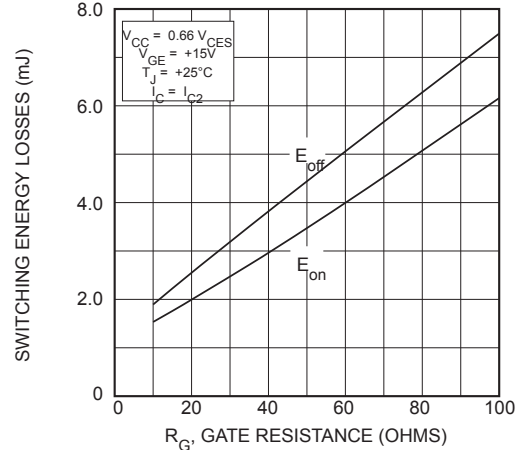


Figure 11, Typical Switching Energy Losses vs Gate Resistance

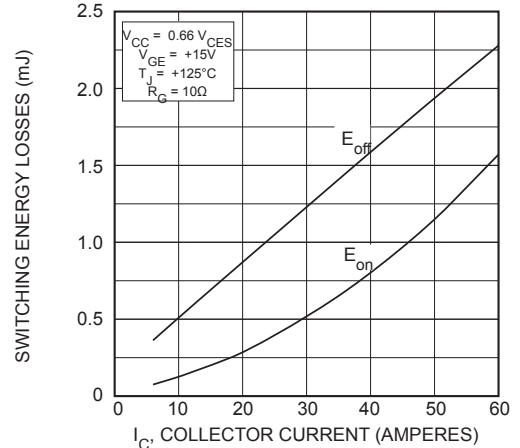


Figure 13, Typical Switching Energy Losses vs Collector Current

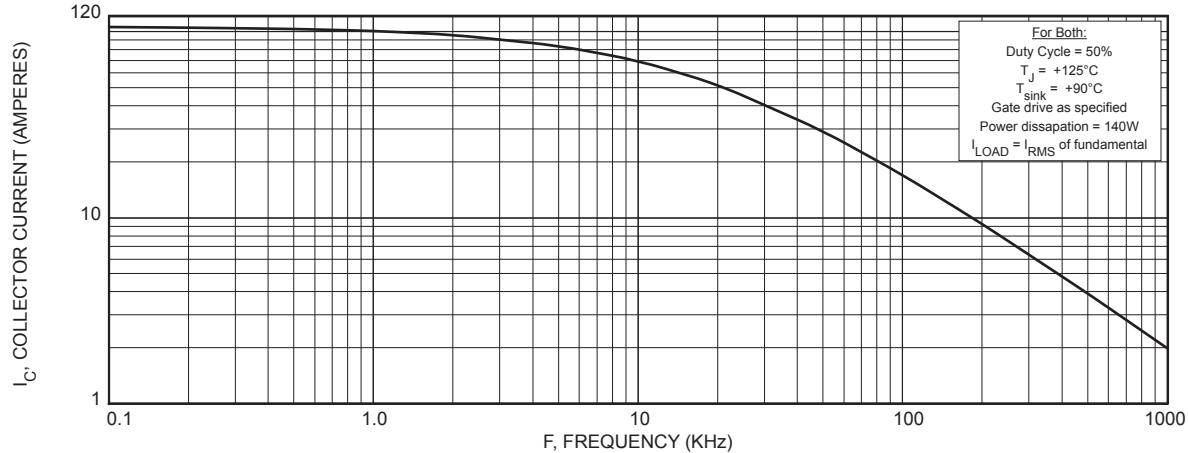


Figure 14, Typical Load Current vs Frequency

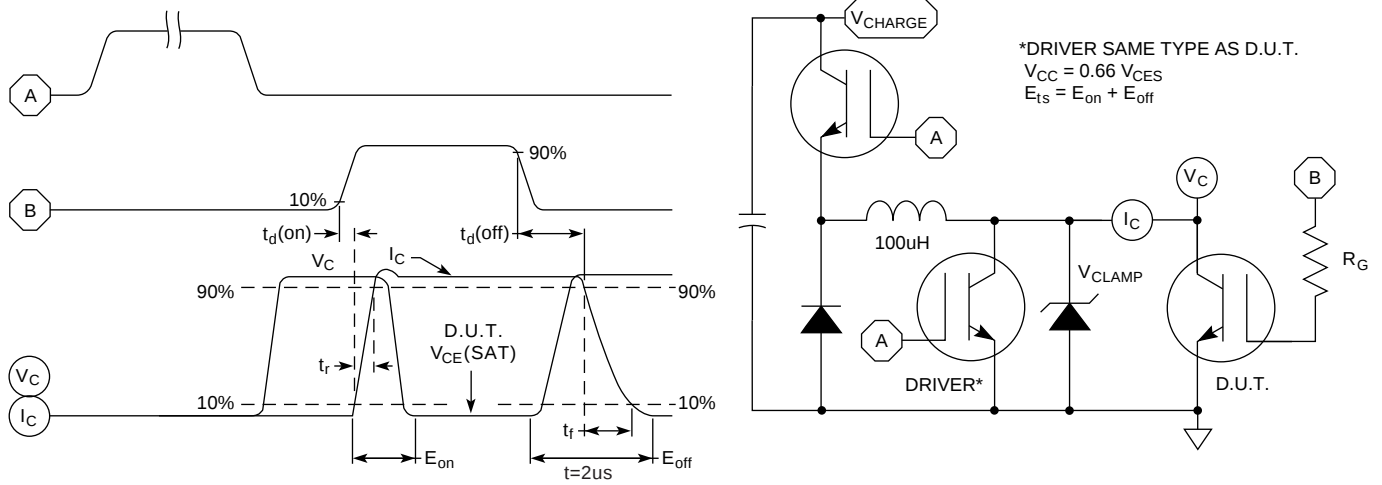


Figure 15, Switching Loss Test Circuit and Waveforms

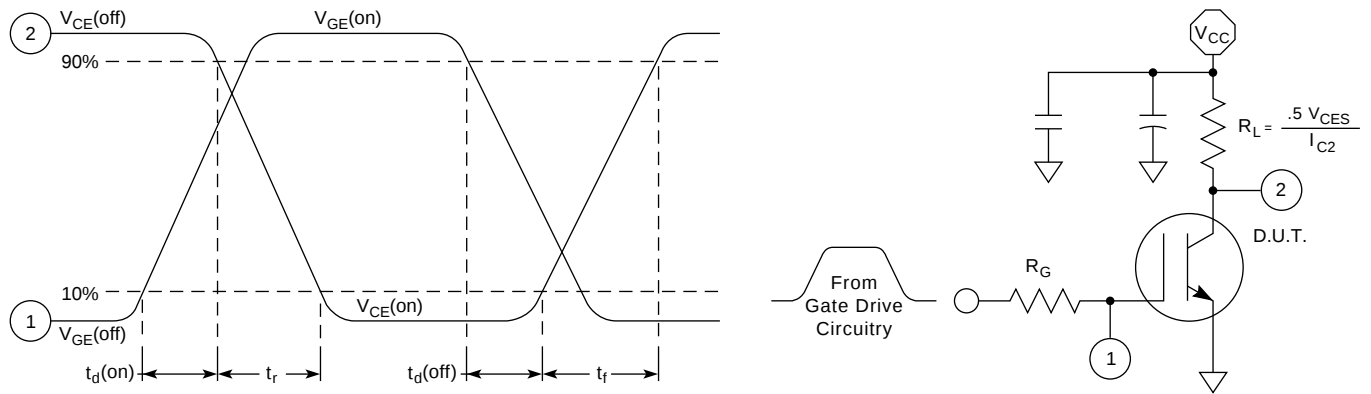
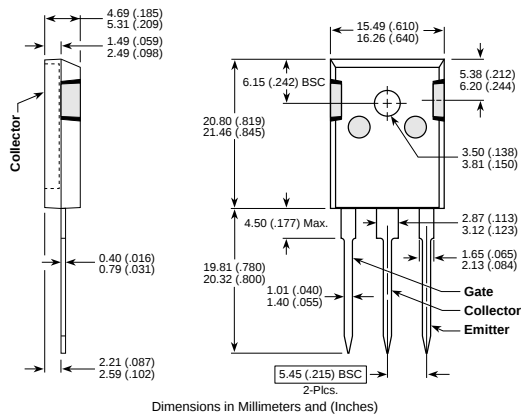


Figure 16, Resistive Switching Time Test Circuit and Waveforms

TO-247 Package Outline

e1 SAC: Tin, Silver, Copper



D³PAK Package Outline

e3 SAC: Tin, Silver, Copper

