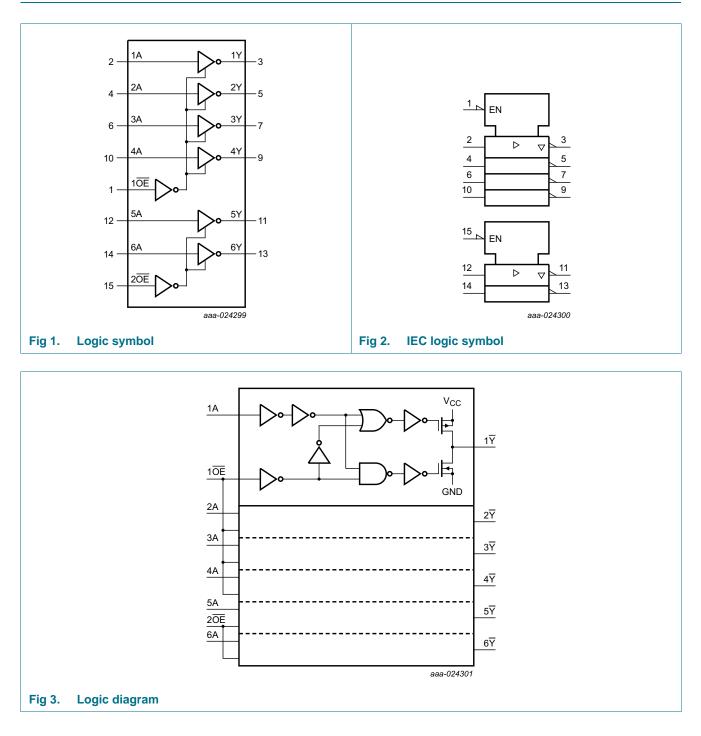
Hex buffer/line driver; 3-state; inverting

4. Functional diagram

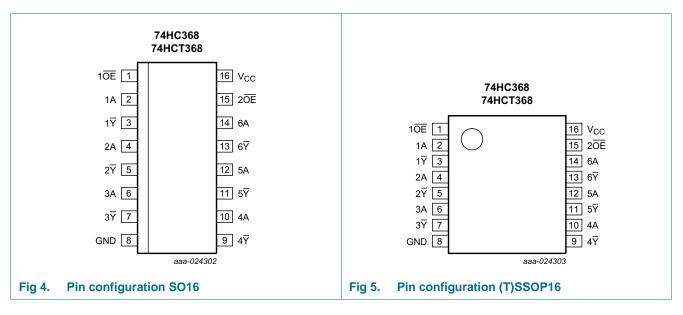


74HC_HCT368

Hex buffer/line driver; 3-state; inverting

Pinning information 5.

5.1 Pinning



5.2 Pin description

Table 2. **Pin description** mhal ___

Symbol	Pin	Description
10E, 20E	1, 15	output enable input (active LOW)
1A, 2A, 3A, 4A, 5A, 6A	2, 4, 6, 10, 12, 14	data input
$1\overline{Y}, 2\overline{Y}, 3\overline{Y}, 4\overline{Y}, 5\overline{Y}, 6\overline{Y}$	3, 5, 7, 9, 11, 13	bus output
GND	8	ground (0 V)
V _{CC}	16	supply voltage

Functional description 6.

Table 3. Function table^[1]

Input nOE	Output	
nOE	nA	nY
L	L	Н
L	Н	L
Н	Х	Z

[1] H = HIGH voltage level; L = LOW voltage level; X = don't care; Z = high-impedance OFF-state.

Hex buffer/line driver; 3-state; inverting

7. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions		Min	Max	Unit
V _{CC}	supply voltage			-0.5	+7	V
I _{IK}	input clamping current	$V_{\rm I} < -0.5$ V or $V_{\rm I} > V_{\rm CC}$ + 0.5 V		-	±20	mA
I _{ОК}	output clamping current	V_{O} < -0.5 V or V_{O} > V_{CC} + 0.5 V		-	±20	mA
I _O	output current	$-0.5 \text{ V} < \text{V}_{\text{O}} < \text{V}_{\text{CC}} + 0.5 \text{ V}$		-	±35	mA
I _{CC}	supply current			-	70	mA
I _{GND}	ground current			-70	-	mA
T _{stg}	storage temperature			-65	+150	°C
P _{tot}	total power dissipation	SO16, SSOP16 and TSSOP16 packages	[1]	-	500	mW

For SO16 packages: P_{tot} derates linearly with 8 mW/K above 70 °C.
 For SSOP16 and TSSOP16 packages: P_{tot} derates linearly with 5.5 mW/K above 60 °C.

8. Recommended operating conditions

Table 5. Recommended operating conditions

Voltages are referenced to GND (ground = 0 V)

Symbol	Parameter	Conditions		74HC368			74HCT368		
			Min	Тур	Max	Min	Тур	Max	
V _{CC}	supply voltage		2.0	5.0	6.0	4.5	5.0	5.5	V
Vi	input voltage		0	-	V _{CC}	0	-	V _{CC}	V
Vo	output voltage		0	-	V _{CC}	0	-	V _{CC}	V
T _{amb}	ambient temperature		-40	+25	+125	-40	+25	+125	°C
Δt/ΔV	input transition rise and fall rate	$V_{CC} = 2.0 V$	-	-	625	-	-	-	ns/V
		$V_{CC} = 4.5 V$	-	1.67	139	-	1.67	139	ns/V
		$V_{CC} = 6.0 V$	-	-	83	-	-	-	ns/V

Hex buffer/line driver; 3-state; inverting

9. Static characteristics

Table 6. Static characteristics

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions		25 °C		–40 °C t	o +85 °C	–40 °C to +125 °C		Unit
			Min	Тур	Max	Min	Max	Min	Max	-
74HC368	8	1				1	1	1	1	
VIH	HIGH-level	V _{CC} = 2.0 V	1.5	1.2	-	1.5	-	1.5	-	V
	input voltage	V _{CC} = 4.5 V	3.15	2.4	-	3.15	-	3.15	-	V
		V _{CC} = 6.0 V	4.2	3.2	-	4.2	-	4.2	-	V
VIL	LOW-level	V _{CC} = 2.0 V	-	0.8	0.5	-	0.5	-	0.5	V
	input voltage	V _{CC} = 4.5 V	-	2.1	1.35	-	1.35	-	1.35	V
		V _{CC} = 6.0 V	-	2.8	1.8	-	1.8	-	1.8	V
V _{OH}	HIGH-level	$V_{I} = V_{IH} \text{ or } V_{IL}$								
	output voltage	$I_0 = -20 \ \mu A; V_{CC} = 2.0 \ V$	1.9	2.0	-	1.9	-	1.9	-	V
		$I_0 = -20 \ \mu A; V_{CC} = 4.5 \ V$	4.4	4.5	-	4.4	-	4.4	-	V
		$I_0 = -20 \ \mu A; V_{CC} = 6.0 \ V$	5.9	6.0	-	5.9	-	5.9	-	V
		$I_{O} = -6.0 \text{ mA}; V_{CC} = 4.5 \text{ V}$	3.98	4.32	-	3.84	-	3.7	-	V
		$I_{O} = -7.8 \text{ mA}; V_{CC} = 6.0 \text{ V}$	5.48	5.81	-	5.34	-	5.2	-	V
V _{OL}	LOW-level	$V_{I} = V_{IH} \text{ or } V_{IL}$								
	output voltage	$I_0 = 20 \ \mu A; \ V_{CC} = 2.0 \ V$	-	0	0.1	-	0.1	-	0.1	V
		$I_0 = 20 \ \mu A; \ V_{CC} = 4.5 \ V$	-	0	0.1	-	0.1	-	0.1	V
		$I_0 = 20 \ \mu A; \ V_{CC} = 6.0 \ V$	-	0	0.1	-	0.1	-	0.1	V
		$I_0 = 6.0 \text{ mA}; V_{CC} = 4.5 \text{ V}$	-	0.15	0.26	-	0.33	-	0.4	V
		$I_0 = 7.8 \text{ mA}; V_{CC} = 6.0 \text{ V}$	-	0.16	0.26	-	0.33	-	0.4	V
lı	input leakage current	$V_I = V_{CC}$ or GND; $V_{CC} = 6.0 V$	-	-	±0.1	-	±1.0	-	±1.0	μA
I _{OZ}	OFF-state output current	$V_{I} = V_{IH} \text{ or } V_{IL}; V_{CC} = 6.0 \text{ V};$ $V_{O} = V_{CC} \text{ or GND}$	-	-	±0.5	-	±5.0	-	±10	μΑ
I _{CC}	supply current		-	-	8.0	-	80	-	160	μΑ
CI	input capacitance		-	3.5	-	-	-	-	-	pF

5 of 16

Hex buffer/line driver; 3-state; inverting

Symbol	Parameter	Conditions		25 °C	;	–40 °C t	o +85 °C	–40 °C to +125 °C		Unit
			Min	Тур	Max	Min	Max	Min	Max	
74HCT3	68					1		1	1	
V _{IH}	HIGH-level input voltage	V_{CC} = 4.5 V to 5.5 V	2.0	1.6	-	2.0	-	2.0	-	V
V _{IL}	LOW-level input voltage	V_{CC} = 4.5 V to 5.5 V	-	1.2	0.8	-	0.8	-	0.8	V
V _{OH}	HIGH-level	$V_{I} = V_{IH} \text{ or } V_{IL}; V_{CC} = 4.5 \text{ V}$								
	output voltage	I _O = -20 μA	4.4	4.5	-	4.4	-	4.4	-	V
		$I_0 = -6 \text{ mA}$	3.98	4.32	-	3.84	-	3.7	-	V
V _{OL} LOW-level output voltage	$V_{I} = V_{IH} \text{ or } V_{IL}; V_{CC} = 4.5 \text{ V}$									
	I _O = 20 μA	-	0	0.1	-	0.1	-	0.1	V	
		I _O = 6.0 mA	-	0.16	0.26	-	0.33	-	0.4	V
lı	input leakage current	$V_I = V_{CC} \text{ or GND};$ $V_{CC} = 5.5 \text{ V}$	-	-	±0.1	-	±1.0	-	±1.0	μA
I _{OZ}	OFF-state output current		-	-	±0.5	-	±5.0	-	±10	μA
I _{CC}	supply current	$V_I = V_{CC}$ or GND; $V_{CC} = 5.5$ V; $I_O = 0$ A	-	-	8.0	-	80	-	160	μA
ΔI _{CC}	additional supply current	per input pin; $V_I = V_{CC} - 2.1 V$; other inputs at V_{CC} or GND; $V_{CC} = 4.5 V$ to 5.5 V; $I_O = 0 A$								
		10E, nA inputs	-	100	360	-	450	-	490	μA
		20E inputs	-	90	324	-	405	-	441	μA
CI	input capacitance		-	3.5	-	-	-	-	-	pF

Table 6. Static characteristics ...continued

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

10. Dynamic characteristics

Table 7. Dynamic characteristics

GND = 0 V; for load circuit, see <u>Figure 8</u>.

Symbol	Parameter	Conditions	25 °C			–40 °C to	Unit	
			Min	Тур	Мах	Max (85 °C)	Max (125 °C)	-
74HC368	3							
t _{pd}	propagation delay	nA to $n\overline{Y}$; [1]						
		see <u>Figure 6</u>						
		V _{CC} = 2.0 V	-	30	95	120	145	ns
		$V_{CC} = 4.5 V$	-	11	19	24	29	ns
		$V_{CC} = 5.0 \text{ V}; \text{ C}_{L} = 15 \text{ pF}$	-	9	-	-	-	ns
		V _{CC} = 6.0 V	-	9	16	20	25	ns

74HC_HCT368

All information provided in this document is subject to legal disclaimers.

Hex buffer/line driver; 3-state; inverting

Symbol	Parameter	Conditions			25 °C		–40 °C to	Unit	
				Min	Тур	Max	Max (85 °C)	Max (125 °C)	_
t _{en}	enable time	$n\overline{OE}$ to $n\overline{Y}$; see Figure 7	[2]						
		V _{CC} = 2.0 V		-	41	150	190	225	ns
		V _{CC} = 4.5 V		-	15	30	38	45	ns
		V _{CC} = 6.0 V		-	12	26	33	38	ns
t _{dis}	disable time	$n\overline{OE}$ to $n\overline{Y}$; see Figure 7	[3]						
		V _{CC} = 2.0 V		-	55	150	190	225	ns
		V _{CC} = 4.5 V		-	20	30	38	45	ns
		V _{CC} = 6.0 V		-	16	26	33	38	ns
t _t transition time		see Figure 6	<u>[4]</u>						
	V _{CC} = 2.0 V		-	14	60	75	90	ns	
		V _{CC} = 4.5 V		-	5	12	15	18	ns
		V _{CC} = 6.0 V		-	4	10	13	15	ns
C _{PD}	power dissipation capacitance	per buffer; $V_I = GND$ to V_{CC}	<u>[5]</u>	-	30	-	-	-	pF
74HCT36	68								-
t _{pd}	propagation delay	nA to $n\overline{Y}$;	[1]						
		see Figure 6							
		V _{CC} = 4.5 V		-	13	24	30	36	ns
		V _{CC} = 5.0 V; C _L = 15 pF		-	11	-	-	-	ns
t _{en}	enable time	$\overline{\text{OE}}$ to $\overline{\text{NY}}$; V_{CC} = 4.5 V; see Figure 7	[2]	-	17	35	44	53	ns
t _{dis}	disable time	$\overline{\text{NOE}}$ to $\overline{\text{NY}}$; V _{CC} = 4.5 V; see [3] Figure 7		-	20	35	44	53	ns
t	transition time	$V_{CC} = 4.5 \text{ V}; \text{ see } Figure 6$	<u>[4]</u>	-	5	12	15	18	ns
C _{PD}	power dissipation capacitance	per buffer; V _I = GND to V _{CC} – 1.5 V	-	30	-	-	-	pF	

Table 7. Dynamic characteristics ...continued

GND = 0 V; for load circuit, see <u>Figure 8</u>.

 $\label{eq:tpd} [1] \quad t_{pd} \text{ is the same as } t_{PHL} \text{ and } t_{PLH}.$

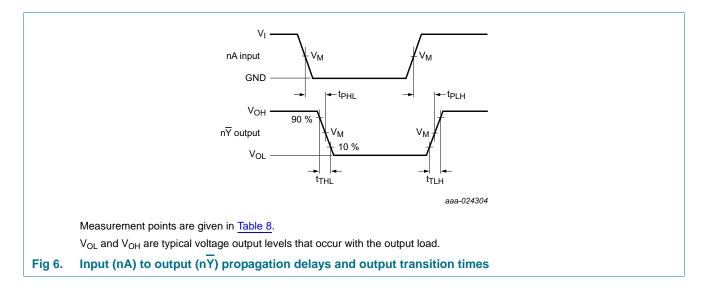
- $[3] \quad t_{dis} \text{ is the same as } t_{PHZ} \text{ and } t_{PLZ}.$
- $\label{eq:ttilde} [4] \quad t_t \text{ is the same as } t_{THL} \text{ and } t_{TLH}.$
- - f_i = input frequency in MHz;
 - f_o = output frequency in MHz;
 - C_L = output load capacitance in pF;
 - V_{CC} = supply voltage in V;

N = number of inputs switching;

 Σ (C_L \times V_{CC}^2 \times f_o) = sum of outputs.

Hex buffer/line driver; 3-state; inverting

11. Waveforms



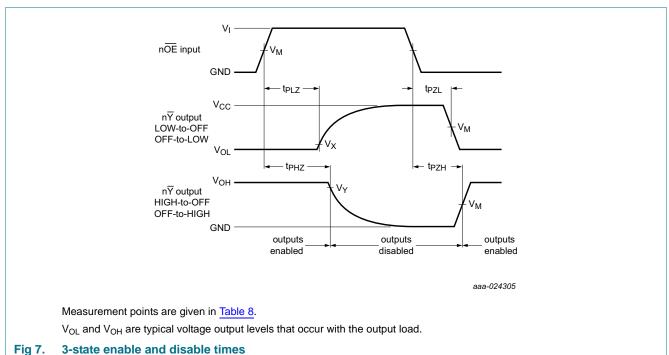


Table 8.Measurement points

Туре	Input	Output				
	V _M	V _M	V _X	V _Y		
74HC368	$0.5 imes V_{CC}$	$0.5 imes V_{CC}$	$0.1 \times V_{CC}$	$0.9 imes V_{CC}$		
74HCT368	1.3 V	1.3 V	$0.1 \times V_{CC}$	$0.9 \times V_{CC}$		

74HC_HCT368

Nexperia

74HC368; 74HCT368

Hex buffer/line driver; 3-state; inverting

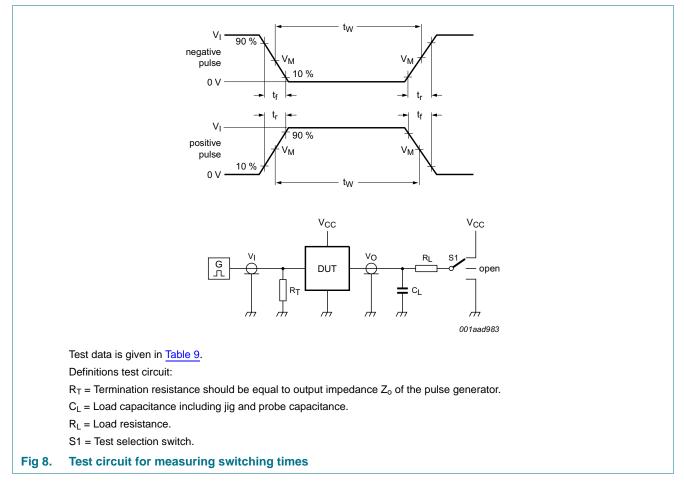


Table 9. Test data

Туре	Input		Load		S1 position		
	VI	t _r , t _f	CL	RL	t _{PHL} , t _{PLH}	t _{PZH} , t _{PHZ}	t _{PZL} , t _{PLZ}
74HC368	V _{CC}	6 ns	15 pF, 50 pF	1 kΩ	open	GND	V _{CC}
74HCT368	3 V	6 ns	15 pF, 50 pF	1 kΩ	open	GND	V _{CC}

Hex buffer/line driver; 3-state; inverting

12. Package outline

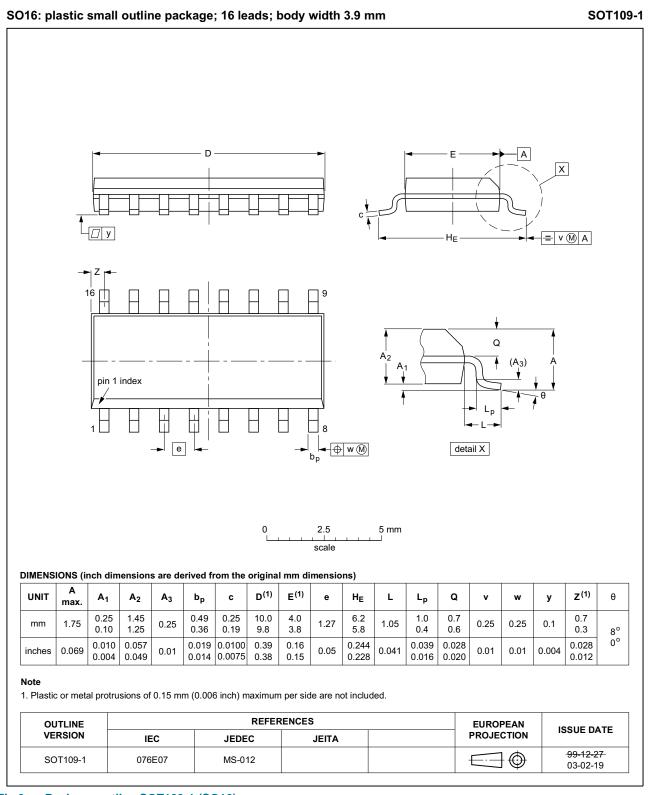


Fig 9. Package outline SOT109-1 (SO16)

74HC_HCT368 Product data sheet

Hex buffer/line driver; 3-state; inverting

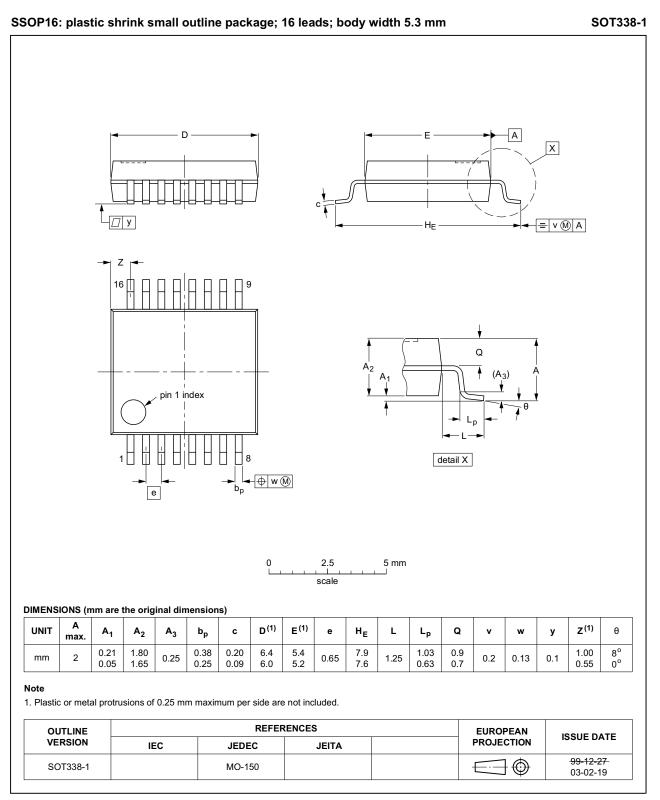


Fig 10. Package outline SOT338-1 (SSOP16)

74HC_HCT368 Product data sheet

© Nexperia B.V. 2017. All rights reserved 11 of 16

Hex buffer/line driver; 3-state; inverting

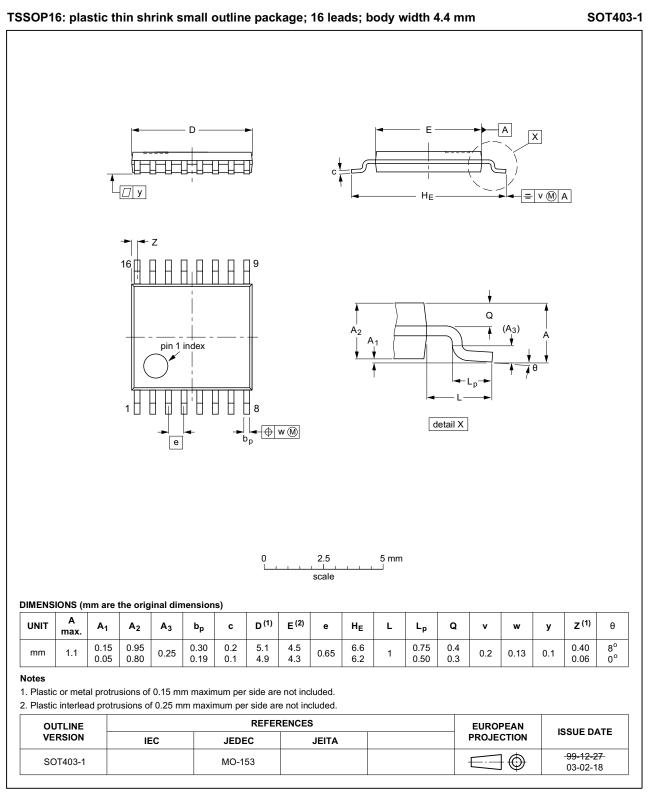


Fig 11. Package outline SOT403-1 (TSSOP16)

All information provided in this document is subject to legal disclaimers.

74HC_HCT368

Hex buffer/line driver; 3-state; inverting

13. Abbreviations

Table 10. Abbreviations						
Acronym	Description					
CMOS	Complementary Metal Oxide Semiconductor					
DUT	Device Under Test					
ESD	ElectroStatic Discharge					
НВМ	Human Body Model					
MM	Machine Model					
TTL	Transistor-Transistor Logic					

14. Revision history

Table 11. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes	
74HC_HCT368 v.3	20160809	Product data sheet	-	74HC_HCT368_CNV v.2	
Modifications:	 The format of this data sheet has been redesigned to comply with the new identity guidelines of NXP Semiconductors. 			y with the new identity	
	 Legal texts have been adapted to the new company name where appropriate. 				
	 Type numbers 74HC368N and 74HCT368N removed. 				
74HC_HCT368_CNV v.2	19901201	Product specification	-	-	

74HC_HCT368

Hex buffer/line driver; 3-state; inverting

15. Legal information

15.1 Data sheet status

Document status[1][2]	Product status ^[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

[3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL http://www.nexperia.com.

15.2 Definitions

Draft — The document is a draft version only. The content is still under internal review and subject to formal approval, which may result in modifications or additions. Nexperia does not give any

representations or warranties as to the accuracy or completeness of information included herein and shall have no liability for the consequences of use of such information.

Short data sheet — A short data sheet is an extract from a full data sheet with the same product type number(s) and title. A short data sheet is intended for quick reference only and should not be relied upon to contain detailed and full information. For detailed and full information see the relevant full data sheet, which is available on request via the local Nexperia sales office. In case of any inconsistency or conflict with the short data sheet, the full data sheet shall prevail.

Product specification — The information and data provided in a Product data sheet shall define the specification of the product as agreed between Nexperia and its customer, unless Nexperia and

customer have explicitly agreed otherwise in writing. In no event however, shall an agreement be valid in which the Nexperia product is deemed to offer functions and qualities beyond those described in the Product data sheet.

15.3 Disclaimers

Limited warranty and liability — Information in this document is believed to be accurate and reliable. However, Nexperia does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information. Nexperia takes no responsibility for the content in this document if provided by an information source outside of Nexperia.

In no event shall Nexperia be liable for any indirect, incidental, punitive, special or consequential damages (including - without limitation - lost profits, lost savings, business interruption, costs related to the removal or replacement of any products or rework charges) whether or not such damages are based on tort (including negligence), warranty, breach of contract or any other legal theory.

Notwithstanding any damages that customer might incur for any reason whatsoever, Nexperia's aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the *Terms and conditions of commercial sale* of Nexperia.

Right to make changes — Nexperia reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof. Suitability for use — Nexperia products are not designed, authorized or warranted to be suitable for use in life support, life-critical or safety-critical systems or equipment, nor in applications where failure or malfunction of a Nexperia product can reasonably be expected to result in personal injury, death or severe property or environmental damage. Nexperia and its suppliers accept no liability for inclusion and/or use of Nexperia products in such equipment or applications and therefore such inclusion and/or use is at the customer's own risk.

Applications — Applications that are described herein for any of these products are for illustrative purposes only. Nexperia makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

Customers are responsible for the design and operation of their applications and products using Nexperia products, and Nexperia accepts no liability for any assistance with applications or customer product design. It is customer's sole responsibility to determine whether the Nexperia product is suitable and fit for the customer's applications and products planned, as well as for the planned application and use of customer's third party customer(s). Customers should provide appropriate design and operating safeguards to minimize the risks associated with their applications and products.

Nexperia does not accept any liability related to any default, damage, costs or problem which is based on any weakness or default in the customer's applications or products, or the application or use by customer's third party customer(s). Customer is responsible for doing all necessary testing for the customer's applications and products using Nexperia products in order to avoid a default of the applications and the products or of the application or use by customer's third party customer(s). Nexperia does not accept any liability in this respect.

Limiting values — Stress above one or more limiting values (as defined in the Absolute Maximum Ratings System of IEC 60134) will cause permanent damage to the device. Limiting values are stress ratings only and (proper) operation of the device at these or any other conditions above those given in the Recommended operating conditions section (if present) or the Characteristics sections of this document is not warranted. Constant or repeated exposure to limiting values will permanently and irreversibly affect the quality and reliability of the device.

Terms and conditions of commercial sale - Nexperia

products are sold subject to the general terms and conditions of commercial sale, as published at http://www.nexperia.com/profile/terms, unless otherwise agreed in a valid written individual agreement. In case an individual agreement is concluded only the terms and conditions of the respective agreement shall apply. Nexperia hereby expressly objects to applying the customer's general terms and conditions with regard to the purchase of Nexperia products by customer.

No offer to sell or license — Nothing in this document may be interpreted or construed as an offer to sell products that is open for acceptance or the grant, conveyance or implication of any license under any copyrights, patents or other industrial or intellectual property rights.

74HC_HCT368

Product data sheet

Nexperia

74HC368; 74HCT368

Hex buffer/line driver; 3-state; inverting

Export control — This document as well as the item(s) described herein may be subject to export control regulations. Export might require a prior authorization from competent authorities.

Non-automotive qualified products — Unless this data sheet expressly states that this specific Nexperia product is automotive qualified, the product is not suitable for automotive use. It is neither qualified nor tested in accordance with automotive testing or application requirements. Nexperia accepts no liability for inclusion and/or use of

non-automotive qualified products in automotive equipment or applications.

In the event that customer uses the product for design-in and use in automotive applications to automotive specifications and standards, customer (a) shall use the product without Nexperia's warranty of the

product for such automotive applications, use and specifications, and (b) whenever customer uses the product for automotive applications beyond

Nexperia's specifications such use shall be solely at customer's own risk, and (c) customer fully indemnifies Nexperia for any liability, damages or failed product claims resulting from customer design and use of the product for automotive applications beyond Nexperia's standard warranty and Nexperia's product specifications.

Translations — A non-English (translated) version of a document is for reference only. The English version shall prevail in case of any discrepancy between the translated and English versions.

15.4 Trademarks

Notice: All referenced brands, product names, service names and trademarks are the property of their respective owners.

16. Contact information

For more information, please visit: http://www.nexperia.com

For sales office addresses, please send an email to: salesaddresses@nexperia.com

Hex buffer/line driver; 3-state; inverting

17. Contents

1	General description 1
2	Features and benefits 1
3	Ordering information 1
4	Functional diagram 2
5	Pinning information
5.1	Pinning
5.2	Pin description 3
6	Functional description 3
7	Limiting values 4
8	Recommended operating conditions 4
9	Static characteristics 5
10	Dynamic characteristics 6
11	Waveforms 8
12	Package outline 10
13	Abbreviations 13
14	Revision history 13
15	Legal information 14
15.1	Data sheet status 14
15.2	Definitions 14
15.3	Disclaimers 14
15.4	Trademarks 15
16	Contact information 15
17	Contents 16

© Nexperia B.V. 2017. All rights reserved

For more information, please visit: http://www.nexperia.com For sales office addresses, please send an email to: salesaddresses@nexperia.com Date of release: 09 August 2016