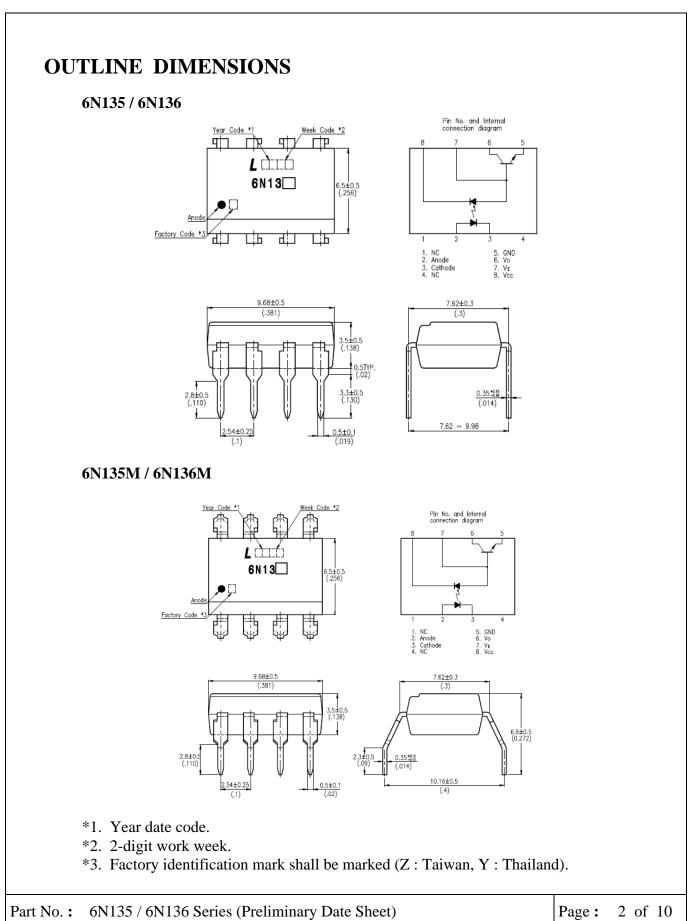
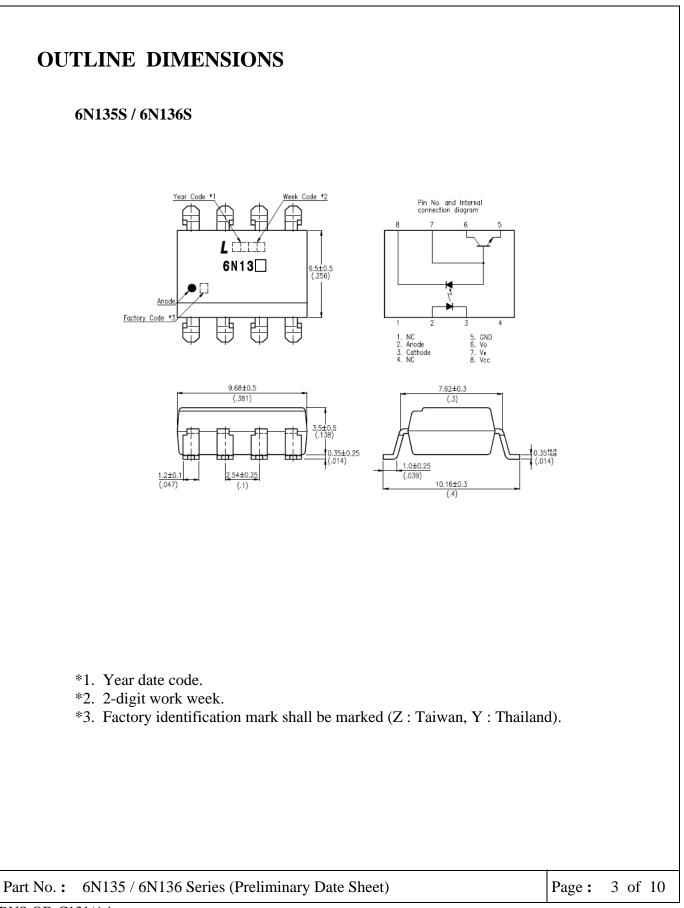


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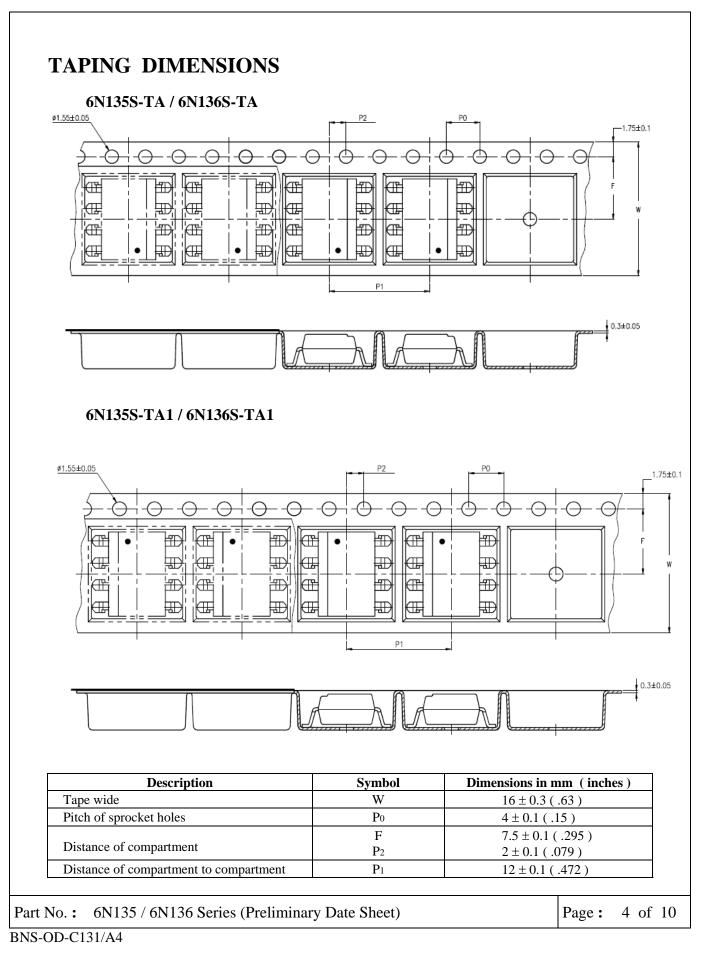


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			('	$Ta = 25^{\circ}C$	
	PARAMETER	SYMBOL	RATING	UNIT	
	Forward Current	IF	25	mA	
INPUT	Reverse Voltage	VR	5	v	
	Power Dissipation	Р	35	mW	
	Supply Voltage	V _{CC}	-0.5 ~ +30	v	
	Output Voltage	Vo	-0.5 ~ +20	V	
OUTPUT	Emitter-base Reverse Voltage	V _{EBR}	0.5	V	
	Average Output Current	Io	8	mA	
	Power Dissipation	Po	100	mW	
1 Isolati	ion Voltage	V _{iso}	5000	Vrms	
Operating Temperature Storage Temperature		T _{opr}	-40 ~ +100	°C	
		T _{stg}	-55 ~ +125	°C	
2 Solde	ring Temperature	T_{sol}	260	°C	

ABSOLUTE MAXIMUM RATING

Notes:

1. AC For 1 Minute, $R.H. = 40 \sim 60\%$

Isolation voltage shall be measured using the following method.

- (1) Short between anode and cathode on the primary side and between collector and emitter on the secondary side.
- (2) The isolation voltage tester with zero-cross circuit shall be used.
- (3) The waveform of applied voltage shall be a sine wave.
- 2. For 10 Seconds

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ELECTRICAL - OPTICAL CHARACTERISTICS

 $(T_{A} = 25^{\circ}C, unless otherwise specified)$

	PARAMETER		SYMBOL	MIN.	TYP.	MAX.	UNIT	CONDITIONS	
Input Forward Voltage			V _F		1.45	1.7	V	Ta=25℃, IF=1.6mA	
Input Forward Voltage Temperature Coefficient			$\Delta V_{\rm F}/\Delta Ta$		-1.6	_	m V /°C	IF=16mA	
	Input Reverse Voltage		BV _R	5.0	_	_	V	Ta=25°C, IR=10 μ	
,	Current Transfer Ratio	6N135	СТР	7		50	- %	I _F =16mA, Vo=0.4V, V _{CC} =4.5V	
3 C		6N136	CTR	19	_	50			
Logic L		6N135	V _{OL}		_	0.4	v	I _F =16mA, I _O =1.1mA V _{CC} =4.5V	
	Logic Low (0) Output Voltage					0.5		I _F =16mA, I _O =0.8mA V _{CC} =4.5V	
		6N136				0.4		$I_{\rm F}{=}16mA,I_{\rm O}{=}3mA$, $V_{\rm CC}{=}4.5V$	
					_	0.5		$I_{F}=0, V_{CC}=V_{O}=5.5V$ $T_{A}=25^{\circ}C$	
Logic High (1) Output Current			I _{OH}	_		1	μΑ	$I_{F}=0, V_{CC}=V_{0}=15V$ $T_{A}=25^{\circ}C$	
						50		$I_F=0, V_{CC}=Vo=15V$	
ļ	Logic Low (0) Supply Current	I _{CCL}			200	uA	IF=16mA, V _{CC} =15V Vo=open		
4 Logic High (1) Supply Current				_	_	1		$I_F=0, V_{CC}=15V,$ Vo= open, $T_A = 25^{\circ}$	
			I _{ссн} —		_	2	μA	$I_{F}=0, V_{CC}=15V,$ Vo= open	
ł	All typical at $T_A = 25^{\circ}C$		1		<u> </u>	<u> </u>	<u> </u>	1	
	No. : 6N135 / 6N136 Series	(Preliminary	y Data Sha	(at)				Page: 6 of	

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PARAMETER		SYM.	MIN.	ТҮР.	^o C, $V_{CC} = 5V$, unle MAX.		UNIT	CONDITIONS
					$T_A=25^{\circ}C$			
	N125		—	_	1.5			IF = 16mA, R _L = 4.1k Ω (7)
Propagation Delay time to Logic Low Output (1)→(0)	5N135					2		
	5N136	t _{PHL}	_		0.8		- μs -	IF = 16mA, $R_L = 1.9k\Omega(8)$
	01130		_			1.0		
	N125	- t _{PLH}	_	_	1.5			IF = 16mA, $R_L = 4.1k\Omega (7)$
	5N135					2	- us	
Propagation Delay time to Logic High Output (0)→(1)			_		0.8			IF = 16mA, $R_L = 1.9k \Omega(8)$
6	6N136					1		
Instantaneous common mode rejection at high logic output (1)	$\mid \mathbf{C}\mathbf{M}_{^{_{\mathrm{H}}}}\mid$		1000	_		V / μs	$I_{F}=0,$ $\mid V_{CM} \mid =10V_{P},$ $RL=4.1k\Omega$ $RL=1.9k\Omega$
Instantaneous common mode rejection at low logic output (0))	CM _L		1000			V / μs	$I_{F}=16mA$ $\mid V_{CM} \mid =10_{P-P},$ $RL=4.1k\Omega$ $RL=1.9k\Omega$
** All typical at $T_A = 25^\circ$	ĈĊ							

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SWITCHING TEST CIRCUITS (AC) **Switching Time Test Circuit** 7 5 V v_o 6 1.5 V **Common Mode Immunity Test Circuit** Noise Shield +5 \ V_{CM} 10 V 6 0.1 μF GND 5 Vo 5 V Switch at A : IF = 0 mA Pulse Vo \sim Vot Switch at A : I_F = 16 mA Part No.: 6N135 / 6N136 Series (Preliminary Date Sheet) Page: 8 of 10

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ISOLATION CHARACTERISTICS

PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT	CONDITIONS
6 Isolation Resistance (Input-output)	R _{I-O}		10 ¹²			Ta=25°C, RH<45%, V _{I-0} =500V DC
6 Capacitance (Input-output)	C _{I-O}		0.6		pF	f=1MHz

** All typical at $T_A = 25^{\circ}C$

Notes,

1. AC For 1 Minute, $R.H. = 40 \sim 60\%$

Isolation voltage shall be measured using the following method.

- (1) Short between anode and cathode on the primary side and between collector and emitter on the secondary side.
- (2) The isolation voltage tester with zero-cross circuit shall be used.
- (3) The waveform of applied voltage shall be a sine wave.
- 2. For 10 Seconds

3. Current Transfer Ratio (CTR) is defined as the ration of output collector current, Io, to the forward LED input current, IF, times 100%.

- 4. Add a 0.1uF bypass capacitor connected between pin5 and pin8 is recommended.
- 5. Common transient immunity in logic high level is the maximum tolerance (positive) dV_{CM}/dt on the leading edge of the common mode pulse signal, V_{CM} , to assure that the output will remain in a logic high state (i.e., Vo>2.0V). Common mode transient immunity in a logic low level is the maximum tolerance (negative) dV_{CM}/dt on the teailing edge of the common mode pulse signal, VCM, to assure that the output will remain in a logic low state (i.e., Vo<0.8V).
- 6. Device considered a two terminal device. Pins 1, 2, 3 and 4 shorted together and Pins 5, 6, 7 and 8 shorted together.
- 7. The 4.1 k Ω load represents 1 LSTTL unit load of 0.36mAand 6.1 k Ω pull up resistor.
- 8. The $1.9 \text{ k}\Omega$ load represents 1 TTL unit load of 1.6mAand 5.6 k Ω pull up resistor.

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Notes:

- Lite-On is continually improving the quality, reliability, function or design and

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- The products shown in this publication are designed for the general use in electronic applications such as office automation equipment, communications devices, audio / visual equipment, electrical application and instrumentation.
- For equipment/devices where high reliability or safety is required, such as space applications, nuclear power control equipment, medical equipment, etc, please contact our sales representatives.
- When requiring a device for any "specific" application, please contact our sales in advice.
- If there are any questions about the contents of this publication, please contact us at your convenience.
- The contents described herein are subject to change without prior notice.
- Do not immerse unit's body in solder paste.

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