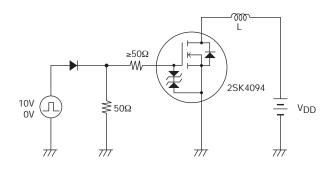
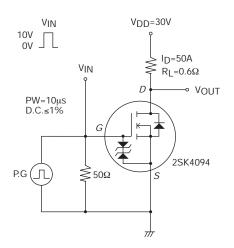
Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Unit
raiametel 5		or Conditions		typ	max	
Drain-to-Source Breakdown Voltage	V(BR)DSS	ID=1mA, VGS=0V	60			V
Zero-Gate Voltage Drain Current	IDSS	V _{DS} =60V, V _{GS} =0V			1	μΑ
Gate-to-Source Leakage Current	IGSS	V _{GS} =16V, V _{DS} =0V			±10	μΑ
Cutoff Voltage	VGS(off)	V _{DS} =10V, I _D =1mA	1.2		2.6	V
Forward Transfer Admittance	yfs	V _{DS} =10V, I _D =50A	45	75		S
Static Drain-to-Source On-State Resistance	R _{DS} (on)1	I _D =50A, V _G S=10V		3.8	5.0	$m\Omega$
	R _{DS} (on)2	ID=50A, VGS=4V		4.9	7.0	$m\Omega$
Input Capacitance	Ciss			12500		pF
Output Capacitance	Coss	V _{DS} =20V, f=1MHz		1200		pF
Reverse Transfer Capacitance	Crss			950		pF
Turn-ON Delay Time	t _d (on)			80		ns
Rise Time	t _r	Con Fig 2		630		ns
Turn-OFF Delay Time	t _d (off)	See Fig.2		860		ns
Fall Time	tf			750		ns
Total Gate Charge	Qg			220		nC
Gate-to-Source Charge	Qgs	V _{DS} =30V, V _{GS} =10V, I _D =100A		30		nC
Gate-to-Drain "Miller" Charge	Qgd			55		nC
Diode Forward Voltage	V _{SD}	IS=100A, VGS=0V		1.0	1.2	V

Fig.1 Avalanche Resistance Test Circuit

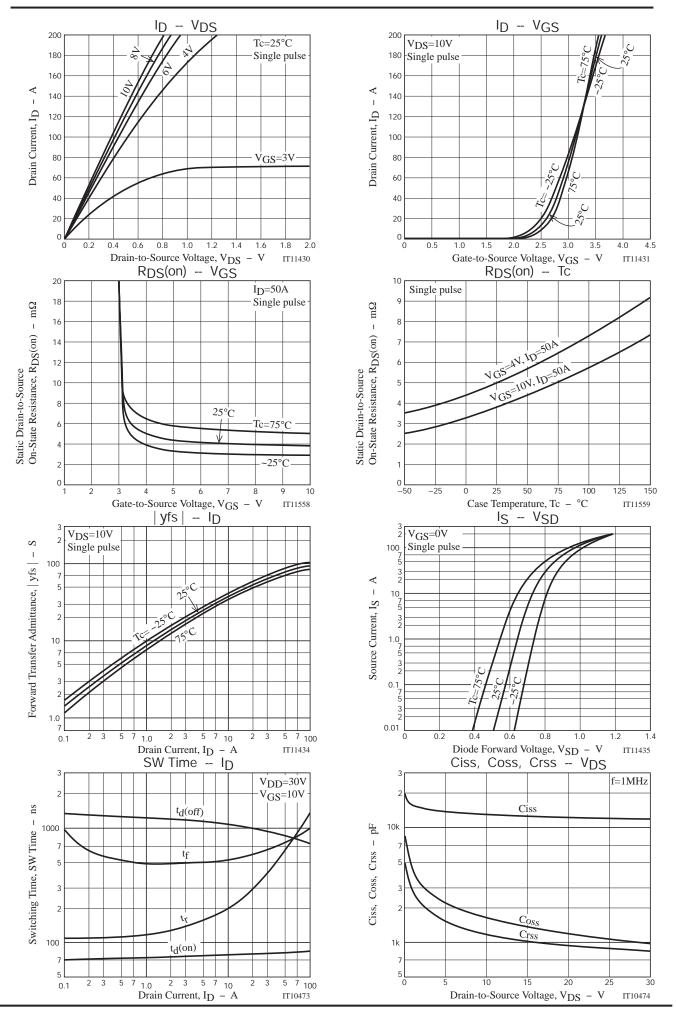
Fig.2 Switching Time Test Circuit

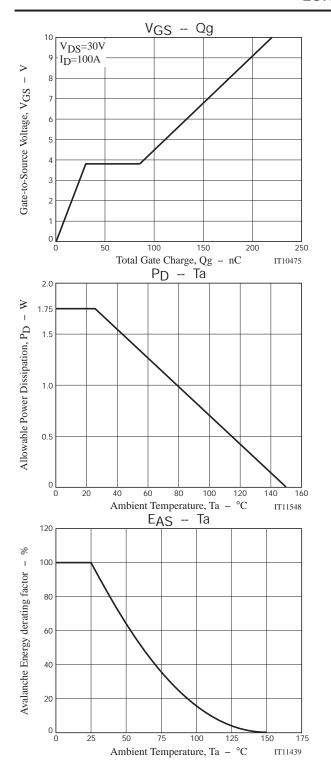


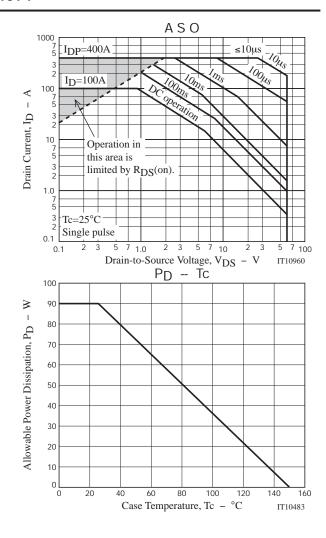


Ordering Information

Device	Package	Shipping	memo
2SK4094-1E	TO-220-3L	50pcs./magazine	Pb Free







Magazine Specification

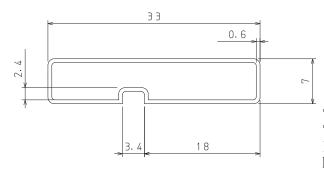
2SK4094-1E

1. Packing Format

Package Name	Maximum Number of devices contained (pcs)			Packing format		
1 40 44 80 144 440	Magazine	Inner box	Outer box	Inner BOX	Outer BOX	
TO-220-3L	50	1,000	4000	SPD-0V0001 20 magazines contained Dimensions:mm(external) 568×150×55	SPD-LV0010 4 inner boxes contained Dimensions:mm (external) 590x225x178	

2. Magazine dimensions

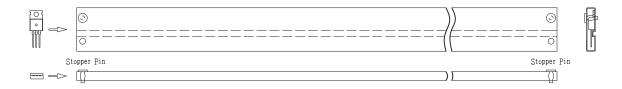
(unit:mm)



To lerance= ± 0 . 2mm Thickness=0. 6+0. 2/-0mm Length = 512. 6 ± 1 mm

Material = PVC (Antistatic treatment)

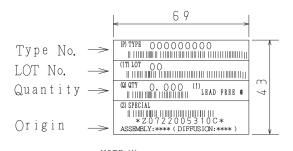
3. Storage method to magazine

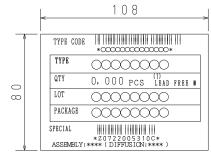


4. Inner box label (unit:mm)

5. Outer box label (unit:mm)

It is a label at the time of factory shipments. The form of a label may change in physical distribution process.



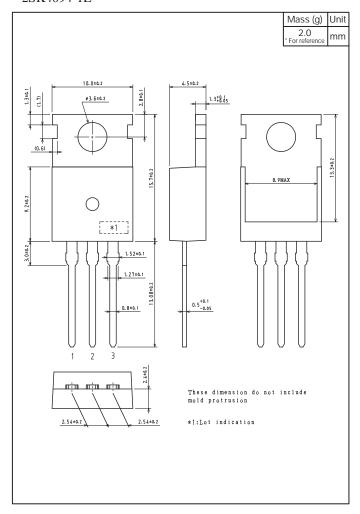


The LEAD FREE * description shows that the surface treatment of the terminal is lead free.

Label		JEITA Phase
LEAD FREE	3	JEITA Phase 3A

Outline Drawing

2SK4094-1E



Note on usage: Since the 2SK4094 is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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