10MQ060NPbF

Vishay High Power Products Schottky Rectifier, 2.1 A



ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS	
Maximum forward voltage drop See fig. 1	V _{FM} ⁽¹⁾	1 A	T 05 %C	0.63	V	
		1.5 A	T _J = 25 °C	0.71		
		1 A	T 105 %C	0.57		
		1.5 A	T _J = 125 °C	0.63		
Maximum reverse leakage current See fig. 2	I _{RM} ⁽¹⁾	T _J = 25 °C		0.5	mA	
		T _J = 125 °C	V _R = Rated V _R	7.5		
Threshold voltage	V _{F(TO)}	T _J = T _J maximum		0.45	V	
Forward slope resistance	r _t			86.8	mΩ	
Typical junction capacitance	CT	$V_R = 10 V_{DC}$, $T_J = 25 °C$, test signal = 1 MHz		31	pF	
Typical series inductance	L _S	Measured lead to lead 5 mm from package body		2.0	nH	
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs	

Note

 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS		
Maximum junction and storage temperature range	T _J ⁽¹⁾ , T _{Stg}		- 55 to 150	°C		
Maximum thermal resistance, junction to ambient	R _{thJA}	DC operation	80	°C/W		
Approximate weight			0.07	g		
			0.002	oz.		
Marking device		Case style SMA (similar D-64)	V1H			

Note

(1) $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$ thermal runaway condition for a diode on its own heatsink



DC

2.0

1.6

2.4

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D = 0.20 D = 0.25

D = 0.33 ~ D = 0.50 <

D = 0.75

See note (1)

0.4

Square wave (D = 0.50) 80 % rated V_{B} applied

0.8

1.2

150

140

130

120

110

100

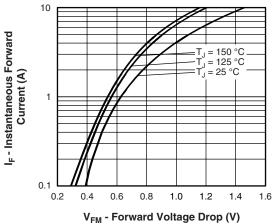
90

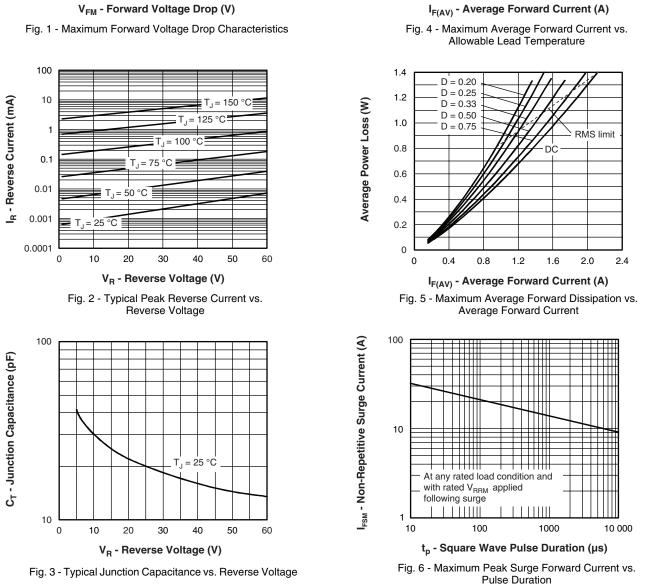
80

70

0

Allowable Case Temperature (°C)





Note

⁽¹⁾ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$;

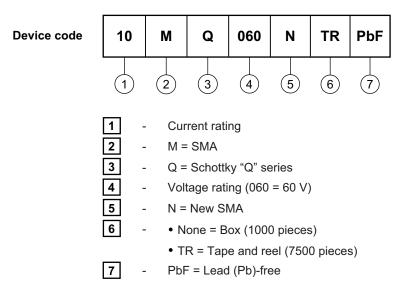
Pd = Forward power loss = $I_{F(AV)} \times V_{FM}$ at $(I_{F(AV)}/D)$ (see fig. 6); Pd_{REV} = Inverse power loss = $V_{R1} \times I_R$ (1 - D); I_R at V_{R1} = 80 % rated V_R

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ORDERING INFORMATION TABLE



LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95018			
Part marking information	www.vishay.com/doc?95029			
Packaging information	www.vishay.com/doc?95034			



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