Vishay Semiconductors

Input Rectifier Diode, 10 A



ELECTRICAL SPECIFICATIONS									
PARAMETER	SYMBOL	TEST CO	VALUES	UNITS					
Maximum forward voltage drop	V _{FM}	10 A, T _J = 25 °C	1.1	V					
Forward slope resistance	r _t	T.I = 150 °C	20	mΩ					
Threshold voltage	V _{F(TO)}	1j = 150 C	0.82	V					
Maximum reverse lookage ourrent	I _{RM}	T _J = 25 °C	V - Reted V	0.05	~^				
Maximum reverse leakage current		T _J = 150 °C	$V_R = Rated V_{RRM}$	0.50	mA				

THERMAL - MECHANICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS				
Maximum junction and storage temperature range	T _J , T _{Stg}		- 40 to 150	°C				
Maximum thermal resistance, junction to case	R _{thJC}	DC operation	2.5	°C/W				
Maximum thermal resistance, junction to ambient (PCB mount)	R _{thJA} ⁽¹⁾		62					
Soldering temperature	Ts		240	°C				
Approximate weight			2	g				
			0.07	oz.				
Marking device		Constant TO 22040		10ETS08				
		Case style TO-220AC	10ETS12					

Note

 $^{(1)}$ When mounted on 1" square (650 mm²) PCB of FR-4 or G-10 material 4 oz. (140 μm) copper 40 °C/W

For recommended footprint and soldering techniques refer to application note #AN-994



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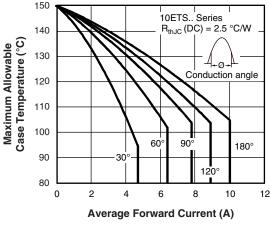


Fig. 1 - Current Rating Characteristics

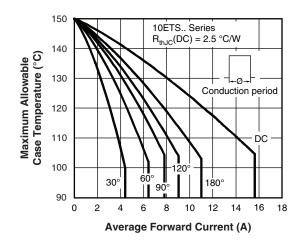


Fig. 2 - Current Rating Characteristics

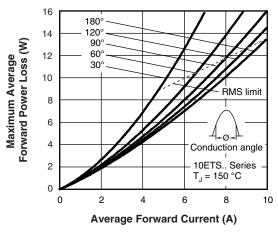
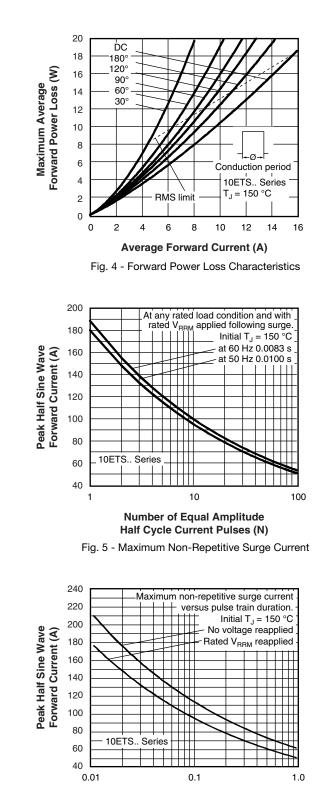


Fig. 3 - Forward Power Loss Characteristics



Pulse Train Duration (s)

Fig. 6 - Maximum Non-Repetitve Surge Current

Document Number: 94337 Revision: 22-Jul-10 For technical questions within your region, please contact one of the following: <u>DiodesAmericas@vishay.com</u>, <u>DiodesAsia@vishay.com</u>, <u>DiodesEurope@vishay.com</u>

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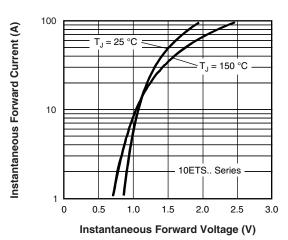


Fig. 7 - Forward Voltage Drop Characteristics

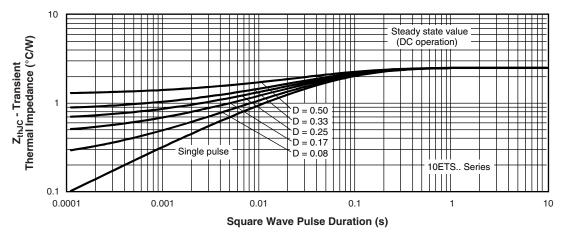


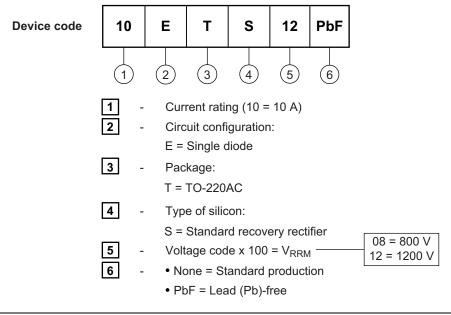
Fig. 8 - Thermal Impedance Z_{thJC} Characteristics



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ORDERING INFORMATION TABLE



LINKS TO RELATED DOCUMENTS						
Dimensions	www.vishay.com/doc?95221					
Part marking information	www.vishay.com/doc?95224					



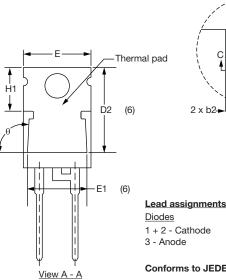
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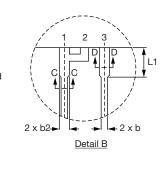
TO-220AC

plane

DIMENSIONS in millimeters and inches









Diodes 1 + 2 - Cathode 3 - Anode

Conforms to JEDEC outline TO-220AC

SYMBOL	MILLIMETERS		INCHES		NOTES		SYMBOL	MILLIMETERS		INCHES		NOTES
	MIN.	MAX.	MIN.	MAX.	NOTES	STMBO	STIVIDOL	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.25	4.65	0.167	0.183			E1	6.86	8.89	0.270	0.350	6
A1	1.14	1.40	0.045	0.055			E2	-	0.76	-	0.030	7
A2	2.56	2.92	0.101	0.115			е	2.41	2.67	0.095	0.105	
b	0.69	1.01	0.027	0.040			e1	4.88	5.28	0.192	0.208	
b1	0.38	0.97	0.015	0.038	4		H1	6.09	6.48	0.240	0.255	6, 7
b2	1.20	1.73	0.047	0.068			L	13.52	14.02	0.532	0.552	
b3	1.14	1.73	0.045	0.068	4		L1	3.32	3.82	0.131	0.150	2
с	0.36	0.61	0.014	0.024			L3	1.78	2.13	0.070	0.084	
c1	0.36	0.56	0.014	0.022	4		L4	0.76	1.27	0.030	0.050	2
D	14.85	15.25	0.585	0.600	3		ØР	3.54	3.73	0.139	0.147	
D1	8.38	9.02	0.330	0.355			Q	2.60	3.00	0.102	0.118	
D2	11.68	12.88	0.460	0.507	6		θ	90° t	o 93°	90° t	o 93°	
E	10.11	10.51	0.398	0.414	3, 6							

Notes

⁽¹⁾ Dimensioning and tolerancing as per ASME Y14.5M-1994

- ⁽²⁾ Lead dimension and finish uncontrolled in L1
- (3) Dimension D, D1 and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- ⁽⁴⁾ Dimension b1, b3 and c1 apply to base metal only
- ⁽⁵⁾ Controlling dimension: inches
- ⁽⁶⁾ Thermal pad contour optional within dimensions E, H1, D2 and E1
- ⁽⁷⁾ Dimension E2 x H1 define a zone where stamping and singulation irregularities are allowed
- ⁽⁸⁾ Outline conforms to JEDEC TO-220, D2 (minimum) where dimensions are derived from the actual package outline

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