Electrical Characteristics ($_{TA}$ =25 $^{\circ}$ C Unless Otherwise Specified)

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Unit	
Static							
BV _{DSS}	Drain-Source Breakdown Voltage	V_{GS} =0V, I_D =250 μ A	60			٧	
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{DS}=V_{GS}$, $I_D=250$ uA	1	1.6	2	V	
I _{GSS}	Gate-Body Leakage	$V_{DS} = 0V, V_{GS} = \pm 20V$	1		±10	uA	
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =60V,V _{GS} =0V	1		1	μA	
R _{DS(ON)}	Drain-Source On-Resistance	V _{GS} =10V, I _D =0.5A	1	2.5	7.5	Ω	
		V _{GS} =5V, I _D =0.05A	1	3	7.5	Ω	
V _{SD}	Diode Forward voltage	I _S =115mA, V _{GS} =0V	-		1.5	V	
g FS	Forward Transconductance	I _D =200mA, V _{DS} =2V	80			mS	
I _S	Maximum Diode Forward Current				115	mA	
Dynamic							
C _{iss}	Input Capacitance			17			
C _{oss}	Output Capacitance	V _{DS} =25V, V _{GS} =0V, f=1MHz		15		pF	
C _{rss}	Reverse Transfer Capacitance			5			
t _{d(on)}	Turn-On Delay Time	V _{ps} =25V, I _p =0.5A,		7		ns	
$t_{\sf d(off)}$	Turn-Off Delay Time	V_{GS} =10V, R_L =50 Ω , R_{GS} =25 Ω		30			
Qg	Total Gate Charge	V _{DS} =48V, I _D =0.5A, V _{GS} =10V	1	1.22		nC	

Note: pulse width<= 300us, duty cycle<=2%

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TYPICAL ELECTRICAL CHARACTERISTICS

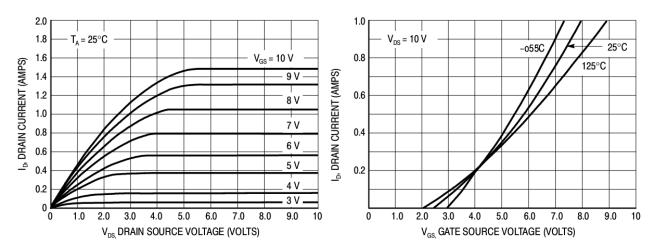


Figure 1. Ohmic Region

Figure 2. Transfer Characteristics

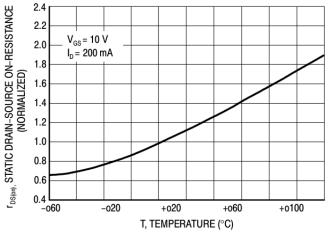


Figure 3. Temperature versus Static Drain–Source On–Resistance

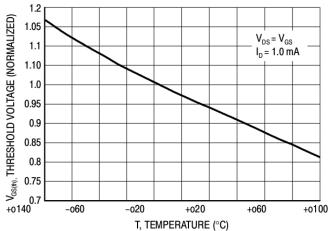
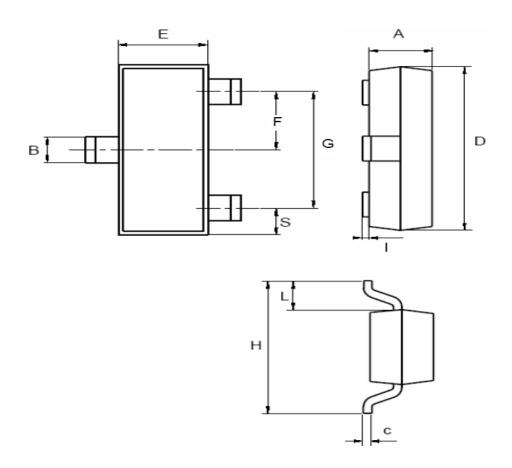


Figure 4. Temperature versus Gate Threshold Voltage



SOT-23					
DIM.	MIN.	MAX.			
Α	0.89	1.40			
В	0.30	0.51			
С	0.085	0.18			
D	2.75	3.04			
E	1.20	1.60			
F	0.85	1.05			
G	1.70	2.10			
Н	2.10	2.75			
I	0.0	0.1			
L	0.60 typ.				
S	0.35	0.65			
All Dimensions in millimeter					

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