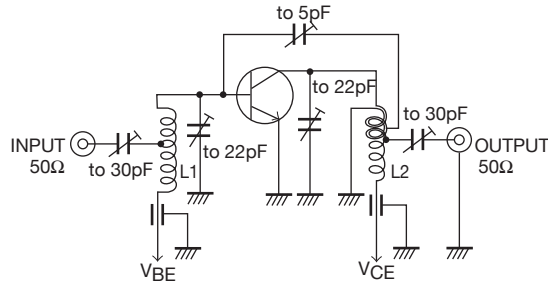


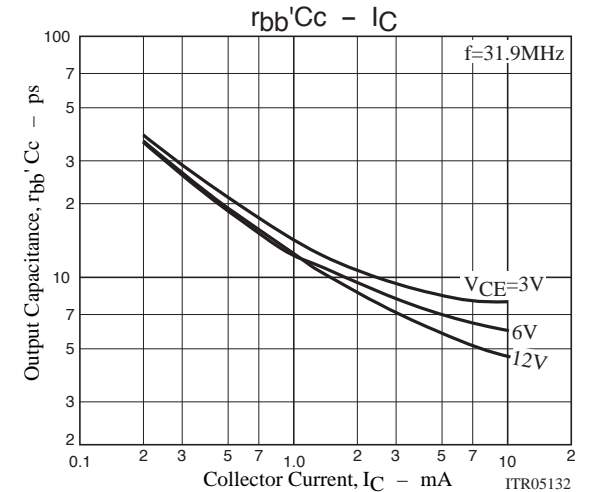
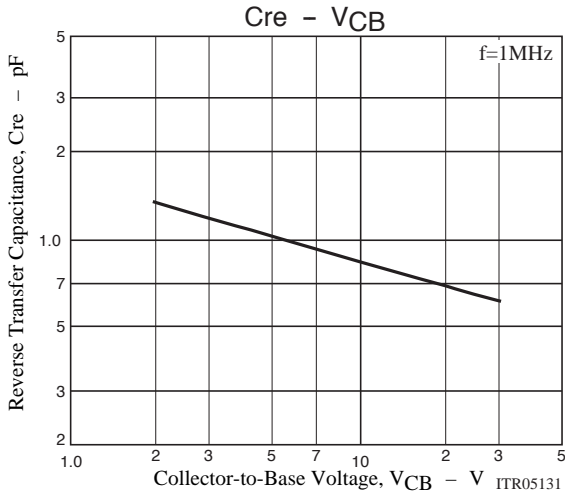
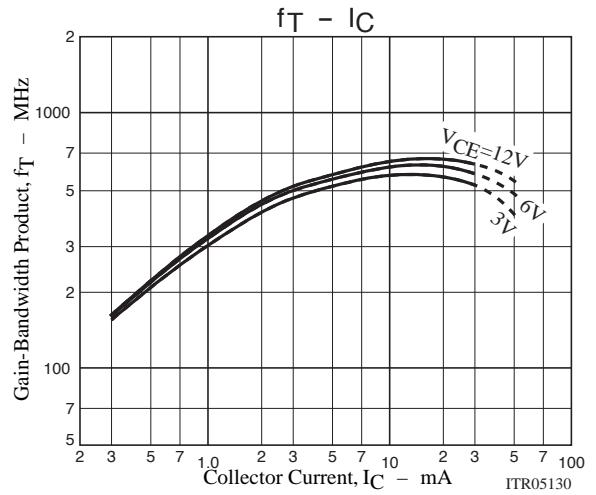
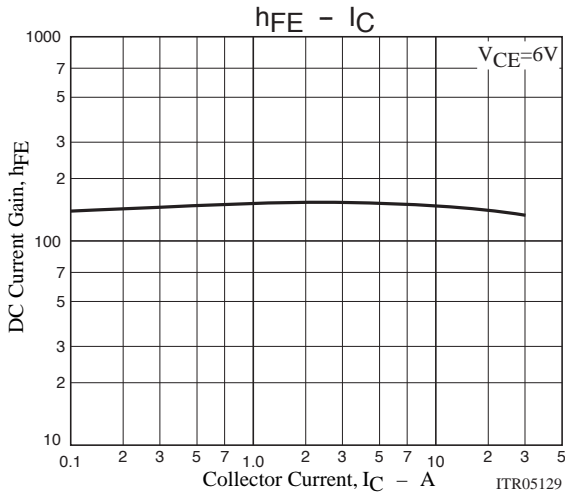
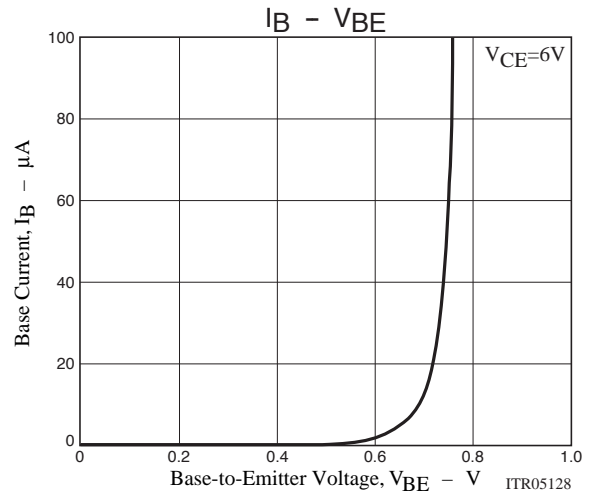
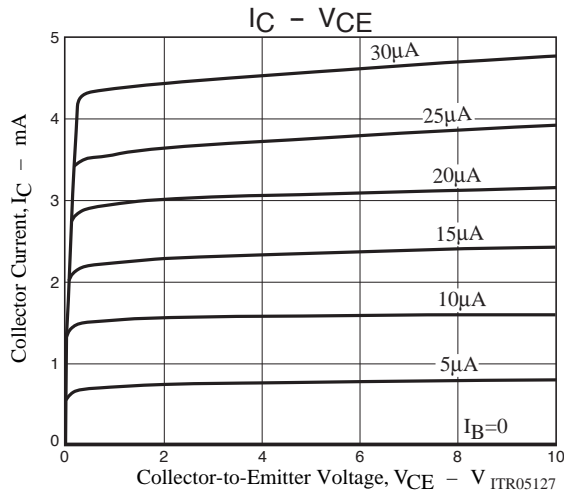
NF, PG Test Circuit

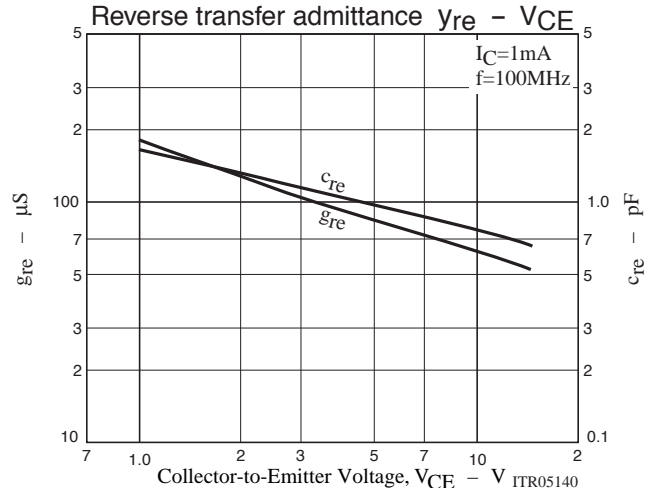
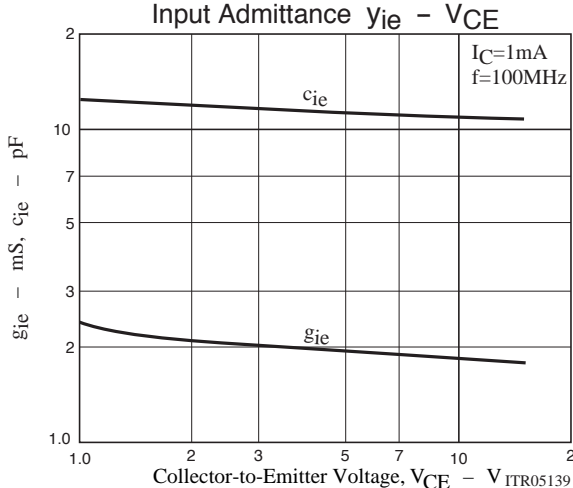
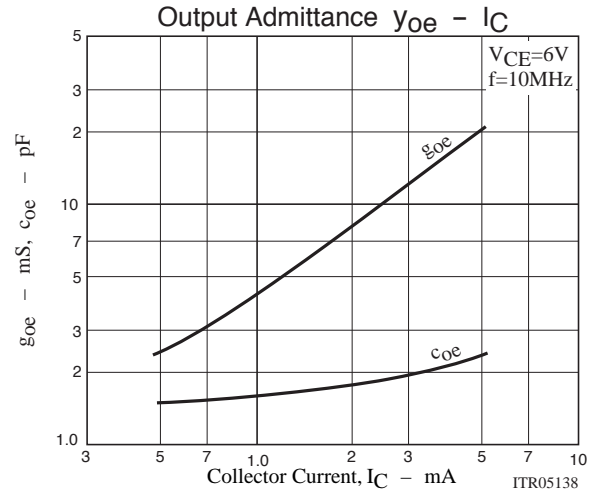
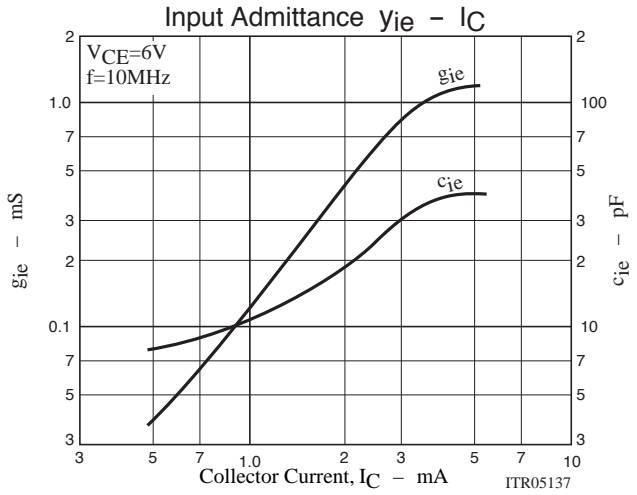
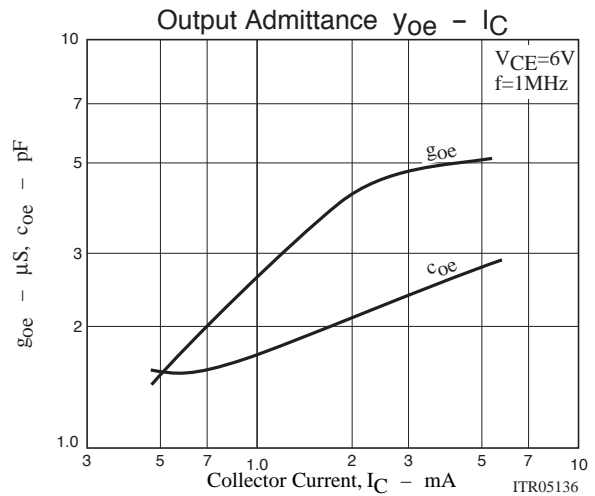
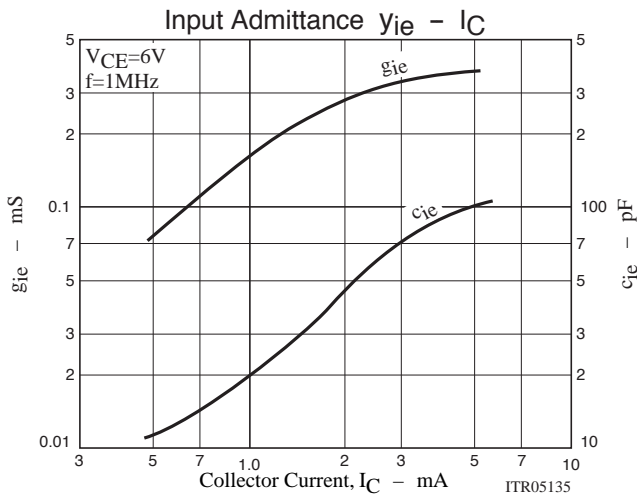
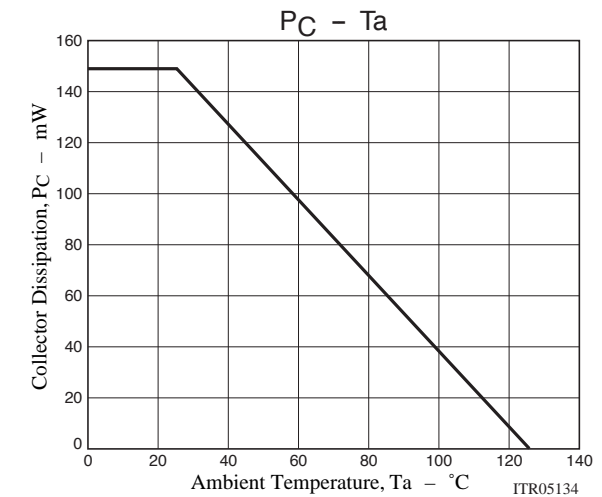
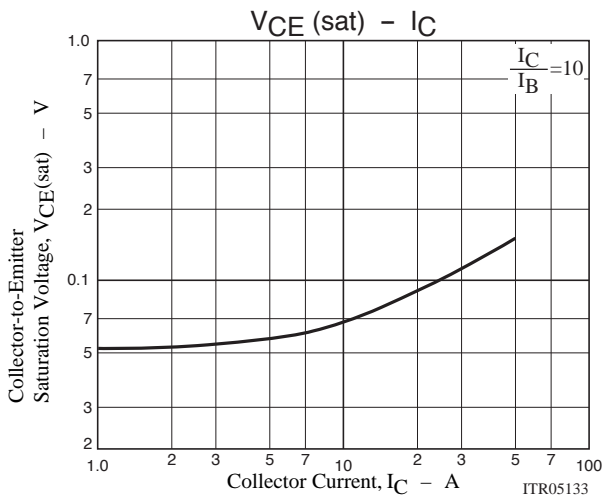


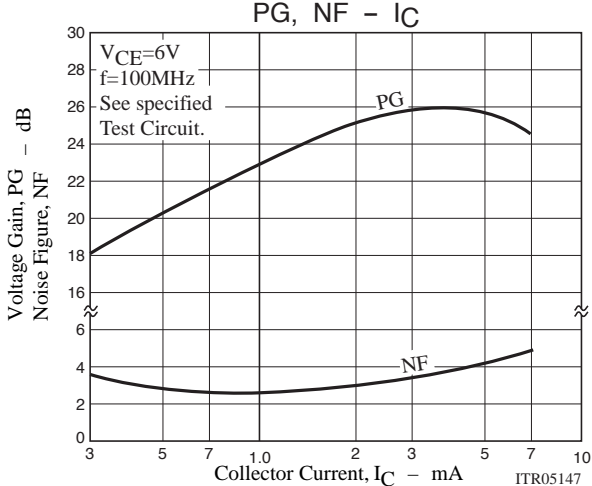
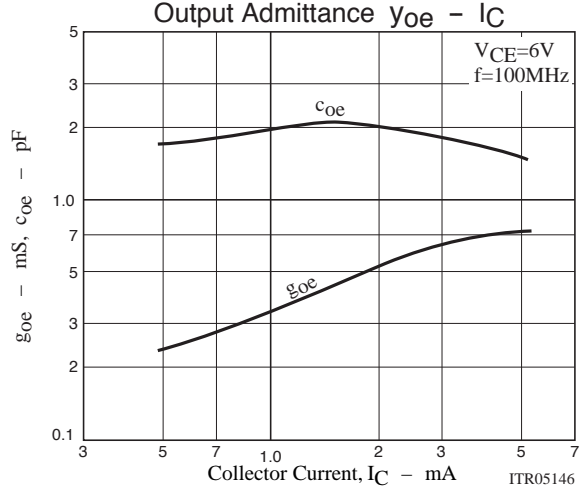
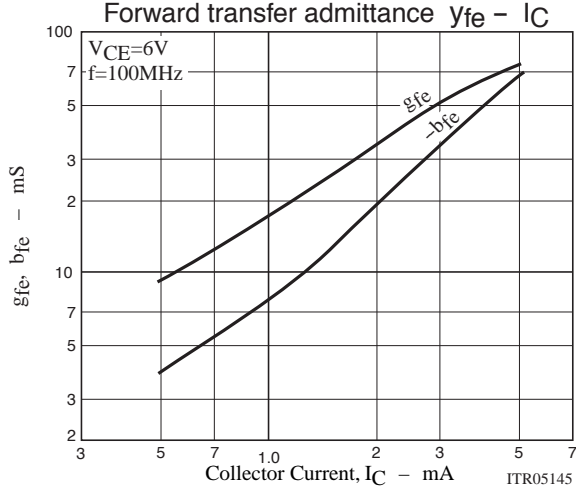
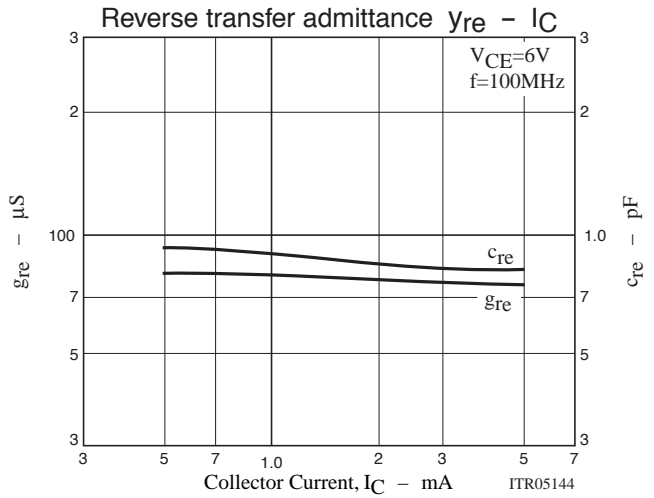
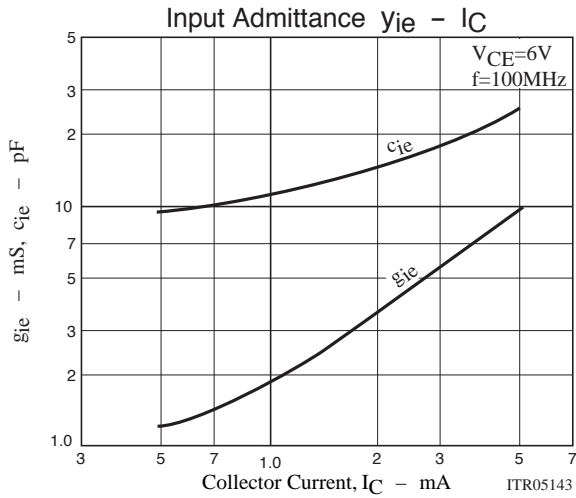
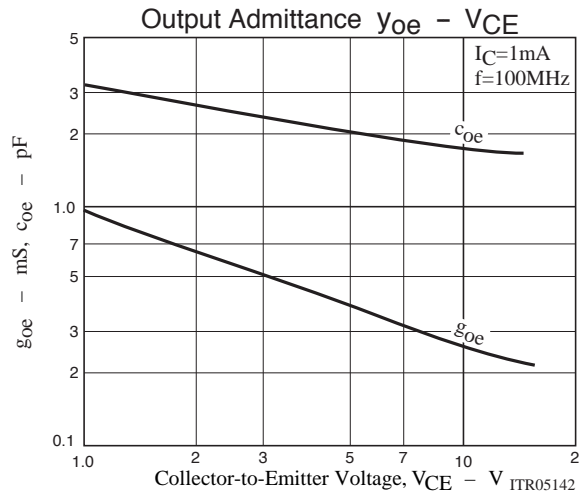
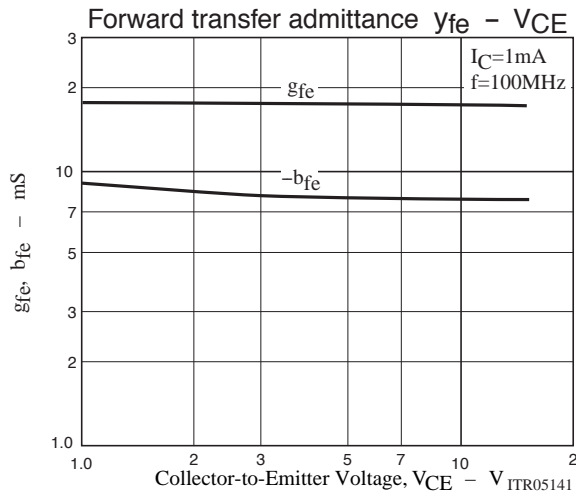
L1 : 1mmø plated wire 10mmø 4T, tap : 2T from  $V_{BE}$  side.

L2 : 1mmø plated wire 10mmø 7T, tap : 1T from  $V_{CE}$  side.

L3 : 1mmø enameled wire 10mmø 3T.







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