

2SB1216, 2SD1816

ELECTRICAL CHARACTERISTICS at Ta = 25°C (Note 2)

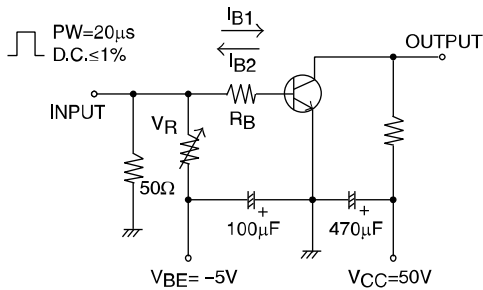
Parameter	Symbol	Conditions	Value			Unit
			min	typ	max	
Collector Cutoff Current	ICBO	V _{CB} =(-)100V, I _E =0A			(-) 1	μ A
Emitter Cutoff Current	IEBO	V _{EB} =(-)4V, I _C =0A			(-) 1	μ A
DC Current Gain	hFE1	V _{CE} =(-)5V, I _C =(-)0.5A	140*		400*	
	hFE2	V _{CE} =(-)5V, I _C =(-)3A	40			
Gain-Bandwidth Product	f _T	V _{CE} =(-)10V, I _C =(-)0.5A		(130) 180		MHz
Output Capacitance	Cob	V _{CB} =(-)10V, f=1MHz		(65) 40		pF
Collector to Emitter Saturation Voltage	V _{CE(sat)}	I _C =(-)2A, I _B =(-)0.2A		(-200) 150	(-500) 400	mV
Base to Emitter Saturation Voltage	V _{BE(sat)}	I _C =(-)2A, I _B =(-)0.2A		(-) 0.9	(-) 1.2	V
Collector to Base Breakdown Voltage	V _{(BR)CBO}	I _C =(-)10 μ A, I _E =0A	(-)120			V
Collector to Emitter Breakdown Voltage	V _{(BR)CEO}	I _C =(-)1mA, R _{BE} = ∞	(-)100			V
Emitter to Base Breakdown Voltage	V _{(BR)EBO}	I _E =(-)10 μ A, I _C =0A	(-) 6			V
Turn-On Time	t _{on}	See specified Test Circuit		100		ns
Storage Time	t _{stg}			(800) 900		ns
Fall Time	t _f			50		ns

Note 2 : Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted.
Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

*: The 2SB1216/2SD1816 are classified by 0.5A hFE as follows:

Rank	S	T
hFE	140 to 280	200 to 400

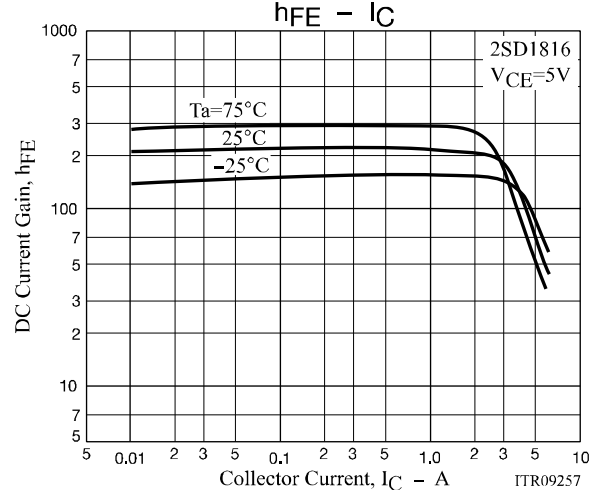
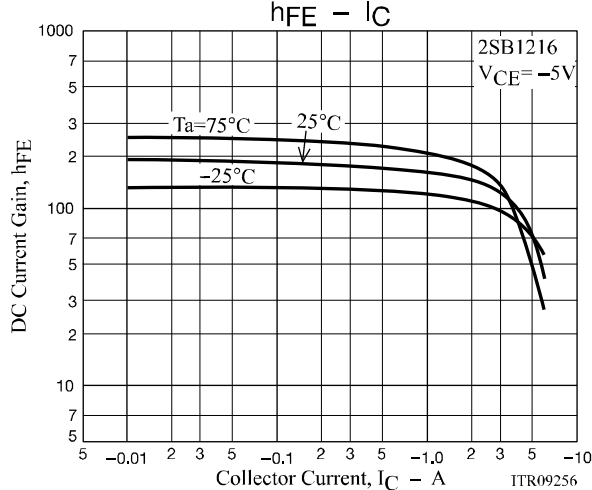
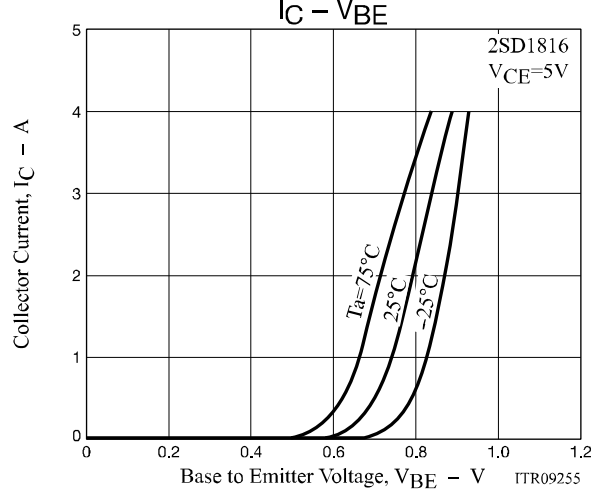
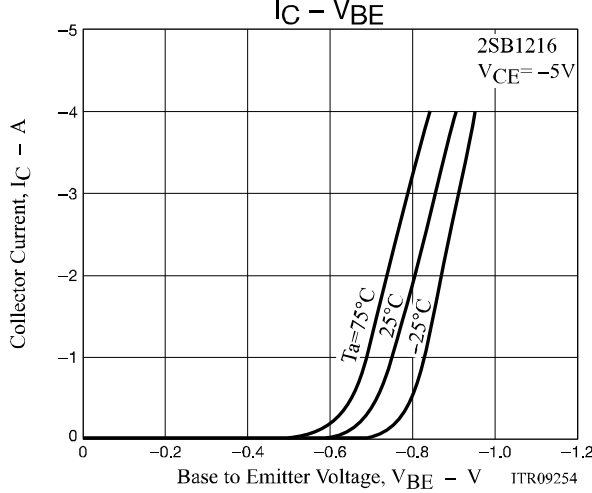
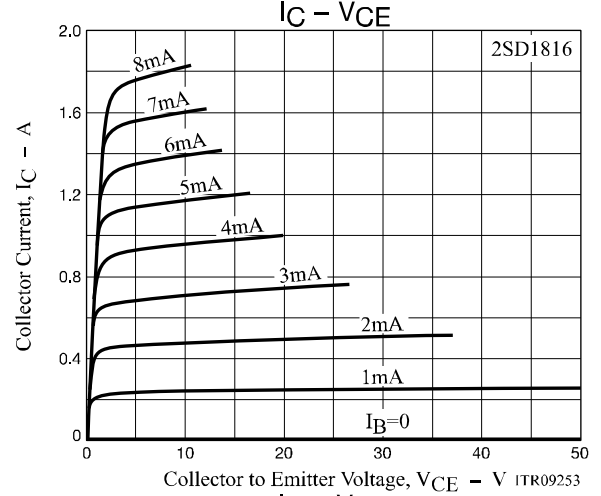
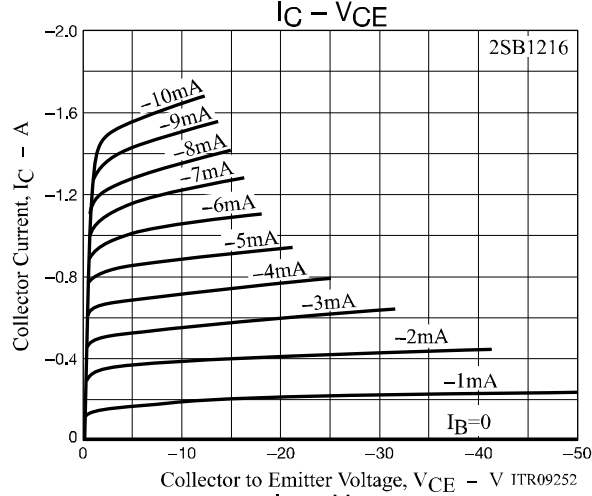
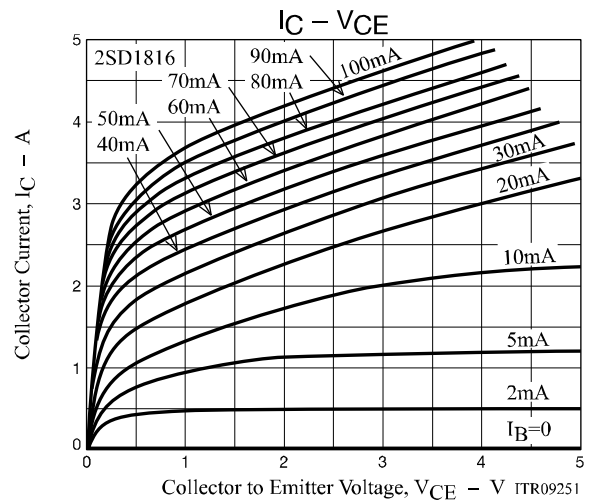
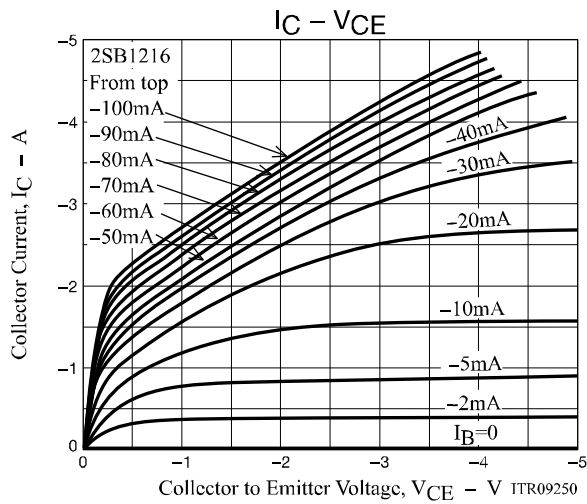
Fig.1 Switching Time Test Circuit



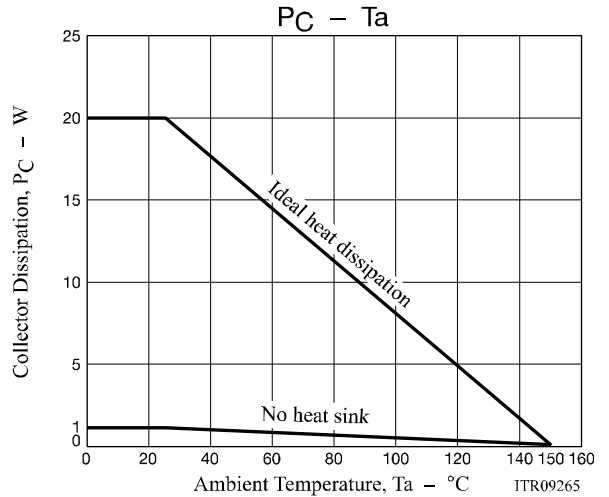
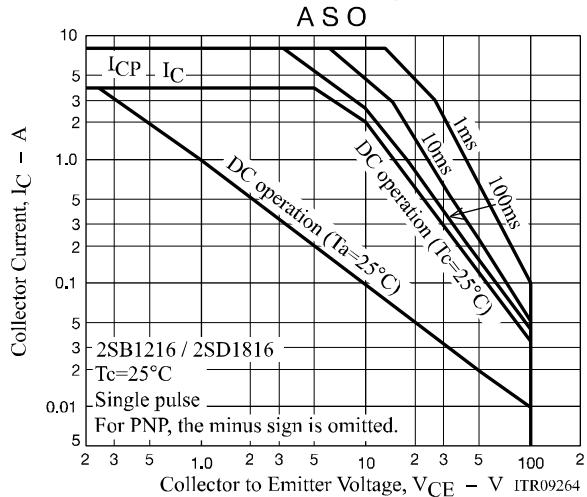
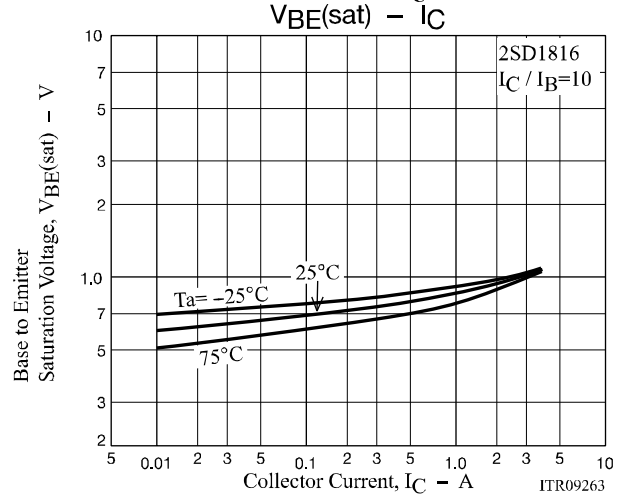
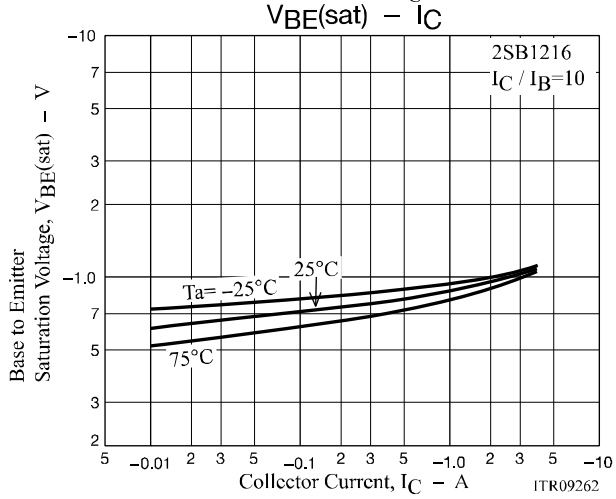
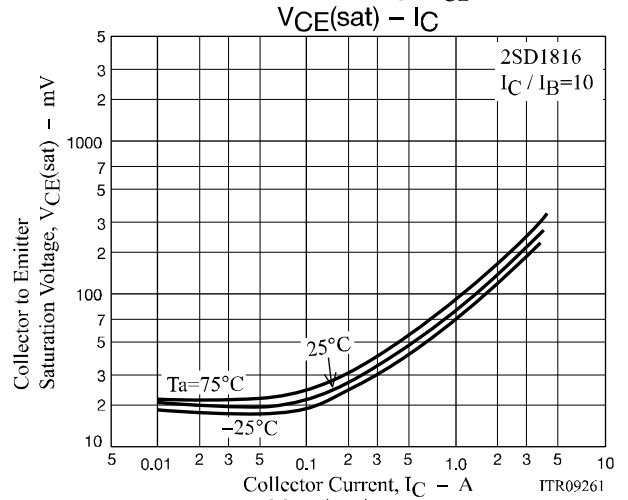
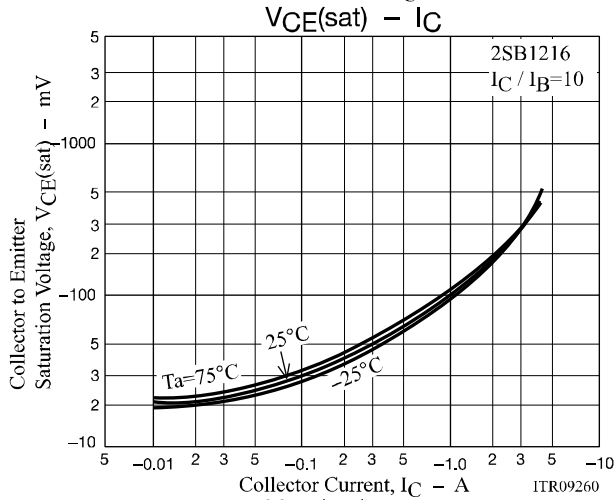
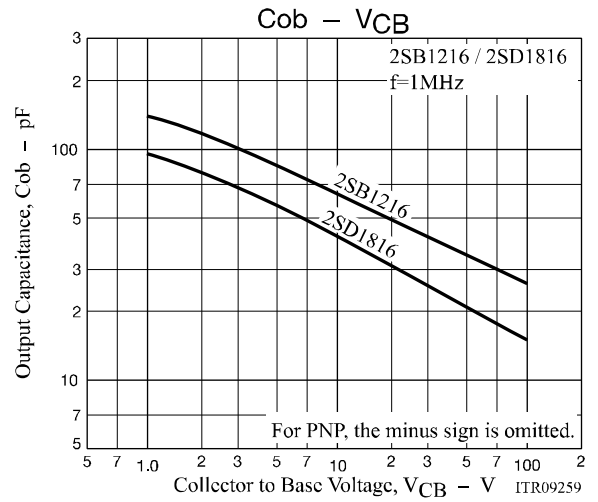
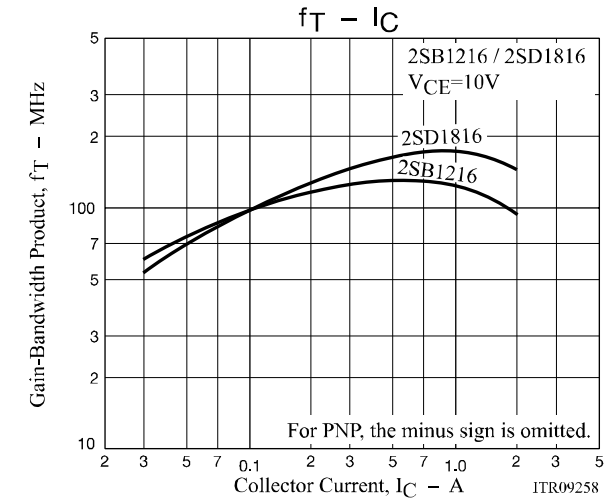
$$I_C = 10 I_{B1} = -10 I_{B2} = 2A$$

For PNP, the polarity is reversed.

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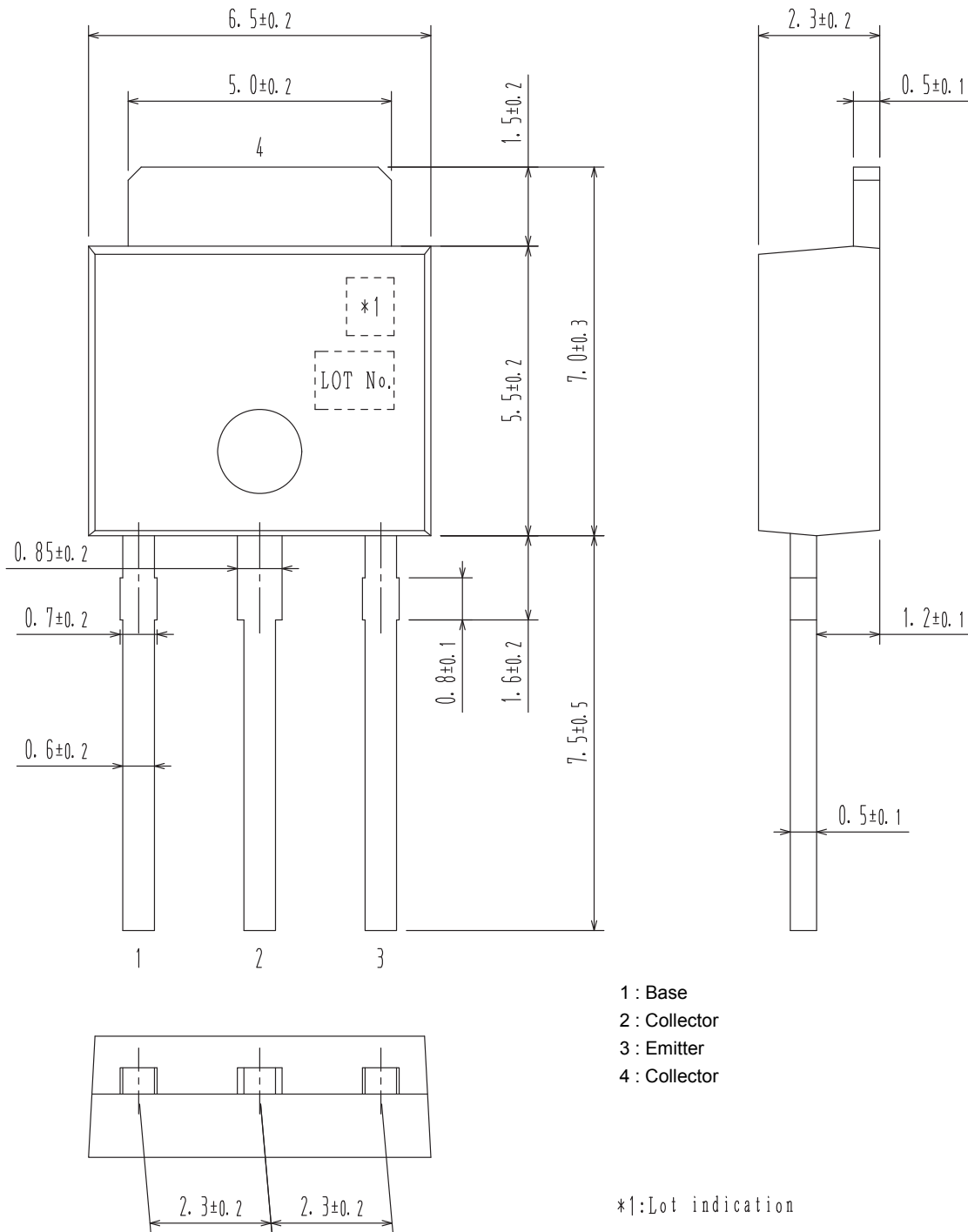
PACKAGE DIMENSIONS

unit : mm

IPAK / TP

CASE 369AJ

ISSUE 0



2SB1216, 2SD1816

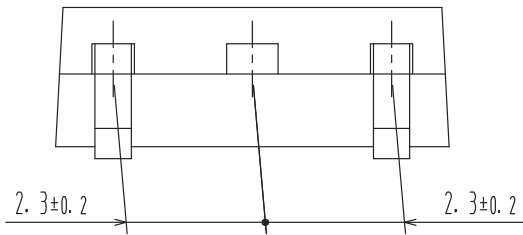
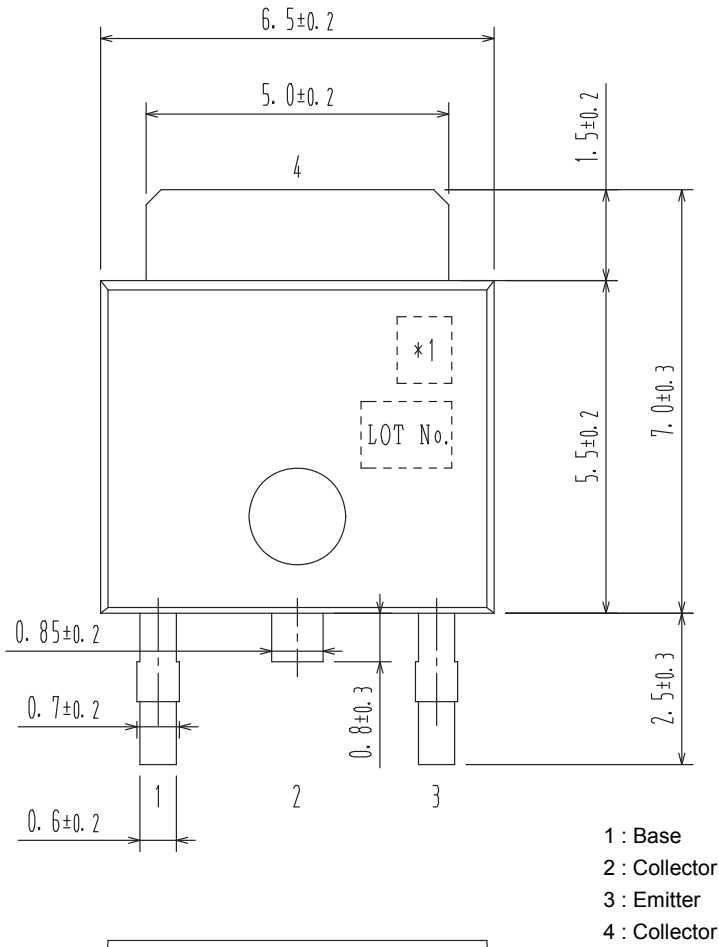
PACKAGE DIMENSIONS

unit : mm

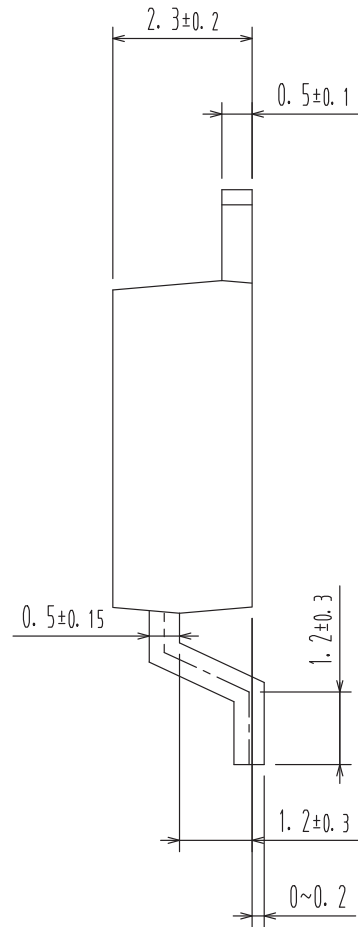
DPAK / TP-FA

CASE 369AH

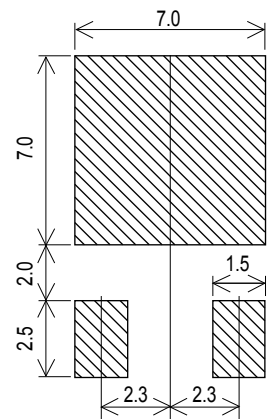
ISSUE O



Pin 2 is idle pin with electrical designation only carried.



Recommended Soldering Footprint



*1: Lot indication

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ORDERING INFORMATION

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Device	Marking	Package	Shipping (Qty / Packing)
2SB1216S-E	B1216	IPAK / TP (Pb-Free)	500/ bag
2SB1216T-E	B1216		
2SD1816S-E	D1816		
2SD1816T-E	D1816		
2SB1216S-H	B1216	IPAK / TP (Pb-Free / Halogen Free)	
2SB1216T-H	B1216		
2SD1816S-H	D1816		
2SD1816T-H	D1816		
2SB1216S-TL-E	B1216	DPAK / TP-FA (Pb-Free)	700/ Tape & Reel
2SB1216T-TL-E	B1216		
2SD1816S-TL-E	D1816		
2SD1816T-TL-E	D1816		
2SB1216S-TL-H	B1216	DPAK / TP-FA (Pb-Free / Halogen Free)	
2SB1216T-TL-H	B1216		
2SD1816S-TL-H	D1816		
2SD1816T-TL-H	D1816		

† For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D. http://www.onsemi.com/pub_link/Collateral/BRD8011-D.PDF

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