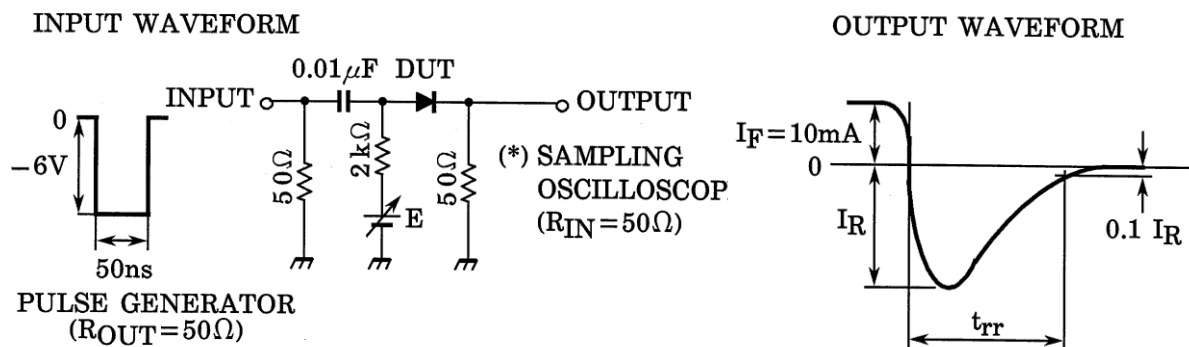


**Fig.1 Reverse recovery time ( $t_{rr}$ ) test circuit**



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