

**Maximum Ratings** @T<sub>A</sub> = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit
Non-Repetitive Peak Reverse Voltage	V <sub>RM</sub>	100	V
Peak Repetitive Reverse Voltage	V <sub>RRM</sub>	75	V
Working Peak Reverse Voltage	V <sub>RWM</sub>		
DC Blocking Voltage	V <sub>R</sub>		
RMS Reverse Voltage	V <sub>R(RMS)</sub>	53	V
Forward Continuous Current	I <sub>FM</sub>	500	mA
Average Rectified Output Current	I <sub>O</sub>	250	mA
Non-Repetitive Peak Forward Surge Current	I <sub>FSM</sub>	@ t = 1.0μs	4
		@ t = 1.0s	0.5

**Thermal Characteristics**

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 4)	P <sub>D</sub>	400	mW
Thermal Resistance Junction to Ambient Air (Note 4)	R <sub>θJA</sub>	313	°C/W
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-65 to +150	°C

**Electrical Characteristics** @T<sub>A</sub> = 25°C unless otherwise specified

Characteristic	Symbol	Min	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 5)	V <sub>(BR)R</sub>	75	—	V	I <sub>R</sub> = 100μA
Forward Voltage	V <sub>F</sub>	0.62	0.72	V	I <sub>F</sub> = 5.0mA
		—	0.855		I <sub>F</sub> = 10mA
		—	1.0		I <sub>F</sub> = 100mA
		—	1.25		I <sub>F</sub> = 150mA
Leakage Current (Note 5)	I <sub>R</sub>	—	2.5	μA	V <sub>R</sub> = 75V
		—	50	μA	V <sub>R</sub> = 75V, T <sub>J</sub> = 150°C
		—	30	μA	V <sub>R</sub> = 25V, T <sub>J</sub> = 150°C
		—	25	nA	V <sub>R</sub> = 20V
Total Capacitance	C <sub>T</sub>	—	4.0	pF	V <sub>R</sub> = 0, f = 1.0MHz
Reverse Recovery Time	t <sub>rr</sub>	—	4.0	ns	I <sub>F</sub> = I <sub>R</sub> = 10mA, I <sub>rr</sub> = 0.1 x I <sub>R</sub> , R <sub>L</sub> = 100Ω

Notes: 4. Part mounted on FR-4 PC board with minimum recommended pad layouts, which can be found on our website at <http://www.diodes.com>.  
5. Short duration pulse test used to minimize self-heating.

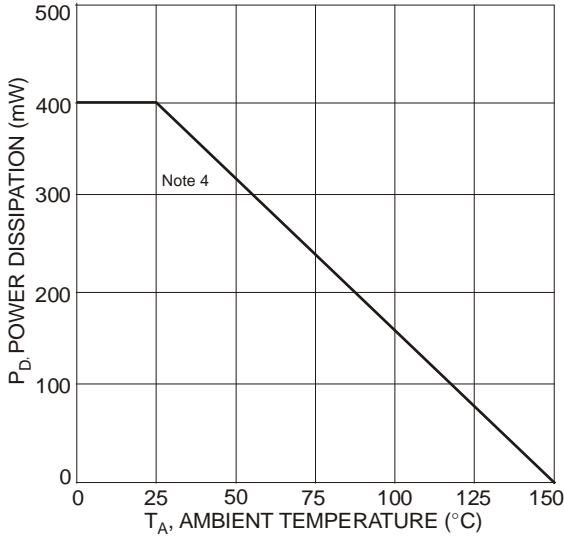


Fig. 1 Forward Current Derating Curve

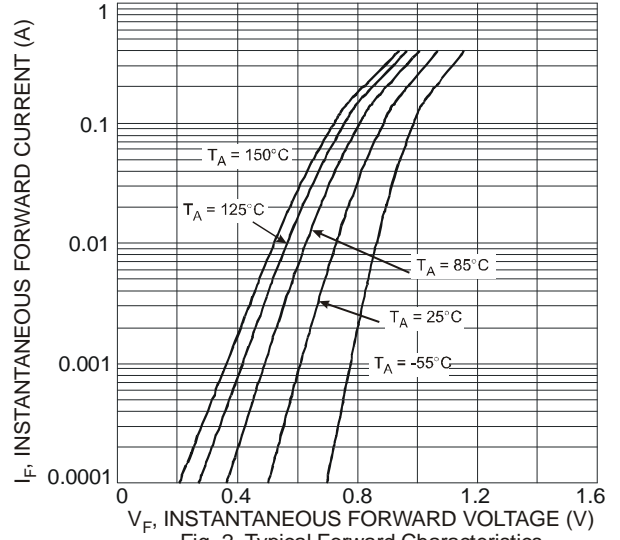


Fig. 2 Typical Forward Characteristics

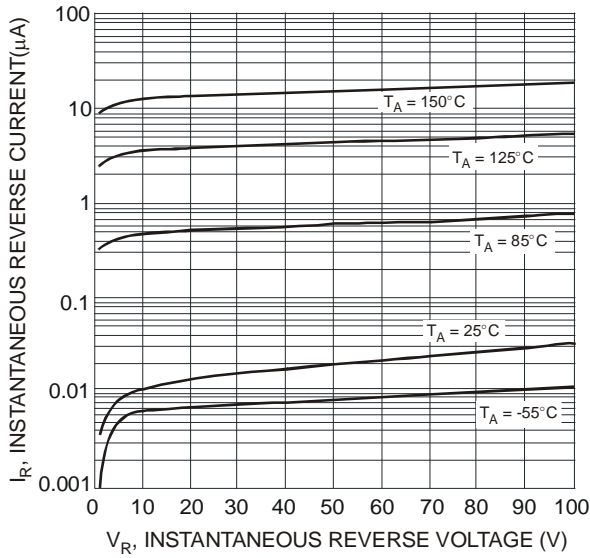


Fig. 3 Typical Reverse Characteristics

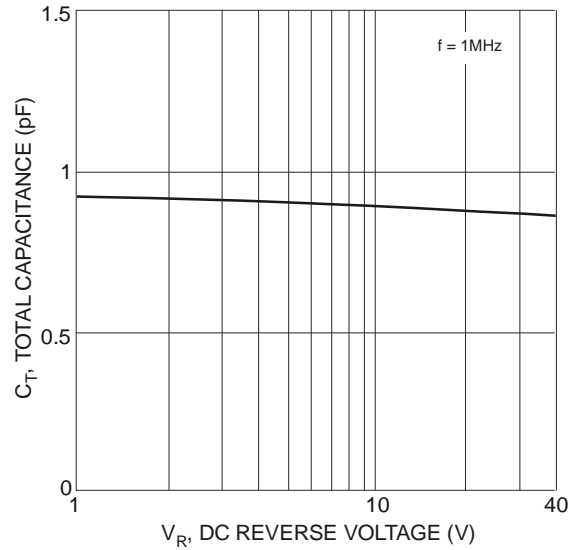
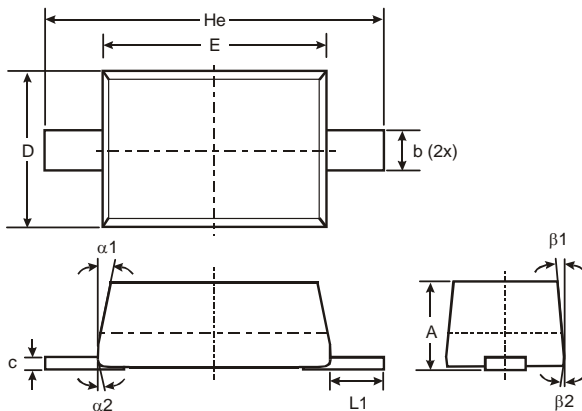


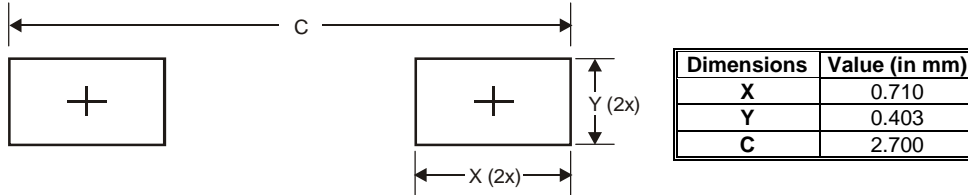
Fig. 4 Total Capacitance vs. Reverse Voltage

## Package Outline Dimensions



SOD323F			
Dim	Min	Max	Typ
A	0.60	0.75	-
b	0.25	0.35	-
c	0.05	0.26	-
D	1.15	1.35	1.25
E	1.60	1.80	1.70
He	2.30	2.70	2.50
L1	0.30	0.50	0.40
$\alpha 1$	-	-	7°
$\alpha 2$	-	-	3°
$\beta 1$	-	-	7°
$\beta 2$	-	-	3°
All Dimensions in mm			

## Suggested Pad Layout



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