

**2N2323, A, AS, S; 2N2324, A, AS, S; 2N2326, A, AS, S; 2N2328, A, AS, S; 2N232, S JAN SERIES**

**ELECTRICAL CHARACTERISTICS (con't)**

| Characteristics   | Symbol   | Min.             | Max.                      | Unit   |
|---|--|------------------|---------------------------|--|
| Forward Blocking Current<br>$R_2 = 1\text{ k}\Omega$ 2N2323 thru 2N2329<br>2N2323S thru 2N2329S<br>$R_2 = 2\text{ k}\Omega$ 2N2323A thru 2N2328A<br>2N2323AS thru 2N2328AS<br>$V_R = 50\text{ Vdc}$ 2N2323, S, A, AS<br>$V_R = 100\text{ Vdc}$ 2N2324, S, A, AS<br>$V_R = 200\text{ Vdc}$ 2N2326, S, A, AS<br>$V_R = 300\text{ Vdc}$ 2N2328, S, A, AS<br>$V_R = 400\text{ Vdc}$ 2N2329, S | $I_{FBX1}$                                       |                  | 10                        | $\mu\text{Adc}$                                  |
| Reverse Gate Current<br>$V_{KG} = 6\text{ Vdc}$   | $I_{KG}$   |                  | 200                       | $\mu\text{Adc}$                                  |
| Gate Trigger Voltage and Current<br>$V_2 = V_{FBX} = 6\text{ Vdc}$ ; $R_L = 100\text{ }\Omega$<br>$R_e = 1\text{ k}\Omega$ 2N2323 thru 2N2329 and<br>2N2323S thru 2N2329S<br>$R_e = 2\text{ k}\Omega$ 2N2323A thru 2N2328A and<br>2N2323AS thru 2N2328AS  | $V_{GT1}$<br>$I_{GT1}$<br>$V_{GT1}$<br>$I_{GT1}$ | 0.35<br><br>0.35 | 0.80<br>200<br>0.60<br>20 | Vdc<br>$\mu\text{Adc}$<br>Vdc<br>$\mu\text{Adc}$ |

**SUBGROUP 4 TESTING**

|  |           |  |     |       |
|--|-----------|--|-----|-------|
| Exponential Rate of Voltage Rise $T_A = 125^\circ\text{C}$<br>$50\text{ }\Omega \leq R_L \leq 400\text{ }\Omega$ , $C = 0.1$ to $1.0\text{ }\mu\text{F}$ , repetition rate = 60 pps,<br>test duration = 15 seconds<br>$dv/dt = 1.8\text{ v}/\mu\text{s}$ , $R_3 = 1\text{ k}\Omega$ 2N2323 thru 2N2329 and<br>2N2323S thru 2N2329S<br>$dv/dt = 0.7\text{ v}/\mu\text{s}$ , $R_3 = 2\text{ k}\Omega$ 2N2323A thru 2N2328A and<br>2N2323AS thru 2N2328AS<br>$V_{AA} = 50\text{ Vdc}$ 2N2323, S, A, AS<br>$V_{AA} = 100\text{ Vdc}$ 2N2324, S, A, AS<br>$V_{AA} = 200\text{ Vdc}$ 2N2326, S, A, AS<br>$V_{AA} = 300\text{ Vdc}$ 2N2328, S, A, AS<br>$V_{AA} = 400\text{ Vdc}$ 2N2329, S | $V_{FBX}$ |  |     | Vdc   |
| Forward "on" Voltage<br>$i_{FM} = 4\text{ a (pk)}$ (pulse), pulse width = 8.5 ms, max; duty cycle = 2% max   | $V_{FM}$  |  | 2.2 | V(pk) |
| Holding Current<br>$V_{AA} = 24\text{ Vdc}$ max, $I_{F1} = 100\text{ mAdc}$ , $I_{F2} = 10\text{ mAdc}$<br>Gate trigger source voltage = 6 Vdc,<br>trigger pulse width = 25 $\mu\text{s}$ min., $R_2 = 330\text{ }\Omega$<br>$R_3 = 1\text{ k}\Omega$ 2N2323 thru 2N2329 and<br>2N2323S thru 2N2329S<br>$R_3 = 2\text{ k}\Omega$ 2N2323A thru 2N2328A and<br>2N2323AS thru 2N2328AS  | $I_{HOX}$ |  | 2.0 | mAdc  |