PHYSICAL DIMENSIONS:

A 6.10 [.240] ± 0.13 [.005]

+ 0.08 [.003] 4.50 [.177] - 0.10 [.004]

B1 7.04 [.277] ± MAX

4.19 [.165] ± 0.10 [.004]

C1 4.83 [.190] ± MAX

2.24 [.088] ± 0.08 [.003]

1.27 [.050] ± 0.13 [.005]

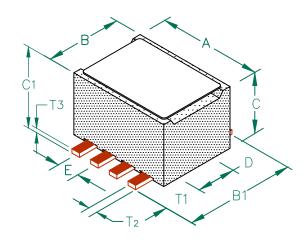
WIRE DIMENSIONS:

± 0.25 [.010] T1 2.16 [.085]

T2 0.50 [.020] TYP.

T3 0.25 [.010] TYP.

29F0418-1SR-10



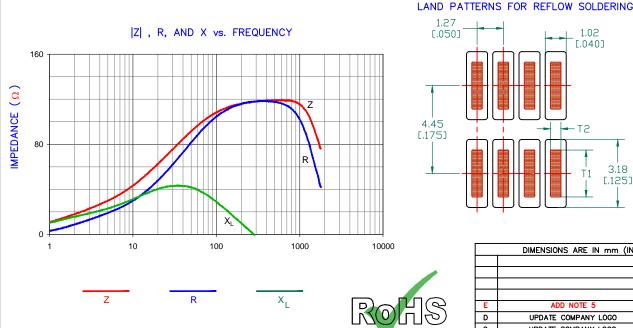
1.27

[.050]

ELECTRICAL CHARACTERISTICS:			
Z @ 100MHz (<u>Ω</u>)		DCR $\left(\begin{array}{c}\Omega\end{array}\right)$	Rated Current
Nominal	119		
Minimum	90		
Maximum	149	0.01	6,000 mA

NOTES: UNLESS OTHERWISE SPECIFIED

- 1. TAPED AND REELED per CURRENT EIA SPECIFICATIONS 13" REELS, 900 PCS/REEL.
- 2. COMPONENTS SHOULD BE ADEQUATELY PREHEATED BEFORE SOLDERING.
- 3. REF. CARRIER TAPE SPECIFICATION # CART 418-13.
- 4. TERMINATION FINISH IS 100% TIN.
- 5. OPERATION TEMPERATURE (INCLUDING SELF-HEATING): $-40 \sim +125$ °C.



RECOMMENDED SOLDERING CONDITIONS REFLOW SOLDERING 1.02 PRE-HEATING [.040]130 [266]

DIMENSIONS ARE IN mm (INCHES) This print is the property of Laird Tech, and is loaned in confidence subject to return upon request and with the understanding that no copies shall be made without the written consent of Laird Tech. All rights to design or invention are ADD NOTE 5 UPDATE COMPANY LOGO 10/21/08 JRK UPDATE COMPANY LOGO 05/01/07 JRK CHANGE TOLS ON E DIMS, ADD ROHS 03/19/07 JRK DATE: ORIGINAL DRAFT 04/27/04 JRK Α DESCRIPTION DATE INT

[.125]

SOLDERING 100 SECONDS 60 SECONDS 5 - 10 SECONDS

Laird PART TYPE: **ASSEMBLY**

29F0418-1SR-10 04/27/04 NTS 1 of 2 29F0418-1SR-10-E-1

HP 4396A Network/Spectrum Analyzer

HP 43961A Impedance Test Kit, HP 16192A Test Fixture